

N-Channel Logic Level Enhancement Mode Power MOSFET

MTB05N03HQ8

BV _{DSS}	30V
I _D	20A
R _{DS(on)(max)}	5mΩ

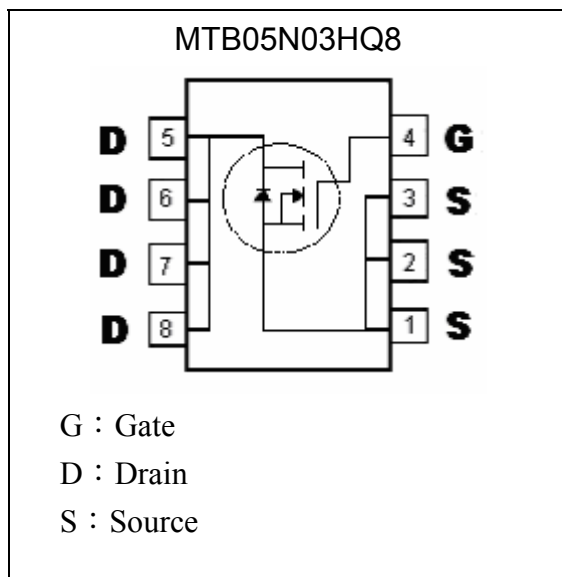
Description

The MTB05N03HQ8 is a N-channel enhancement-mode MOSFET, providing the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost effectiveness. The SOP-8 package is universally preferred for all commercial-industrial surface mount applications and suited for low voltage applications such as DC/DC converters.

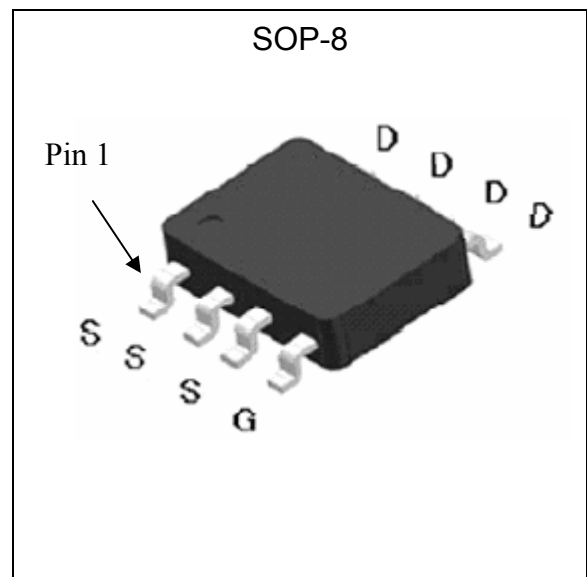
Features

- Single Drive Requirement
- Low On-resistance
- Fast Switching Characteristic
- Dynamic dv/dt rating
- Repetitive Avalanche Rated
- UIS, Rg 100% tested
- Pb-free Lead Plating and Halogen-free Package

Symbol



Outline





Absolute Maximum Ratings (Tc=25°C, unless otherwise noted)

Parameter	Symbol	Limits	Unit	
Drain-Source Voltage	V _{DS}	30	V	
Gate-Source Voltage	V _{GS}	±20		
Continuous Drain Current @ Tc=25°C	I _D	20	A	
Continuous Drain Current @ Tc=100°C		13		
Pulsed Drain Current	I _{DM}	80 *1		
Avalanche Current	I _{AS}	20		
Avalanche Energy @ L=0.1mH, I _D =20A, R _G =25 Ω	E _{AS}	20	mJ	
Repetitive Avalanche Energy @ L=0.05mH	E _{AR}	10 *2		
Total Power Dissipation	P _D	T _A =25°C	3 *3	W
		T _A =100°C	1.5	
Operating Junction and Storage Temperature Range	T _j , T _{stg}	-55~+175	°C	

Thermal Data

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-case, max	R _{th,j-c}	25	°C/W
Thermal Resistance, Junction-to-ambient, max	R _{th,j-a}	50 *3	°C/W

- Note : 1. Pulse width limited by maximum junction temperature
 2. Duty cycle ≤ 1%
 3. Surface mounted on 1 in² copper pad of FR-4 board, 125°C/W when mounted on minimum copper pad

Characteristics (Tc=25°C, unless otherwise specified)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static					
BV _{DSS}	30	-	-	V	V _{GS} =0, I _D =250μA
V _{GS(th)}	1	1.5	3	V	V _{DS} = V _{GS} , I _D =250μA
G _{FS} *1	-	28	-	S	V _{DS} =5V, I _D =20A
I _{GSS}	-	-	±100	nA	V _{GS} =±20
I _{DSS}	-	-	1	μA	V _{DS} =24V, V _{GS} =0
	-	-	25		V _{DS} =20V, V _{GS} =0, T _J =125°C
I _{D(ON)} *1	20	-	-	A	V _{DS} =10V, V _{GS} =10V
R _{DS(ON)} *1	-	3.9	5	mΩ	V _{GS} =10V, I _D =20A
	-	7.1	9	mΩ	V _{GS} =4.5V, I _D =15A
Dynamic					
C _{iss}	-	2935	-	pF	V _{GS} =0V, V _{DS} =15V, f=1MHz
C _{oss}	-	217	-		
C _{rss}	-	142	-		



Characteristics (Tc=25°C, unless otherwise specified)

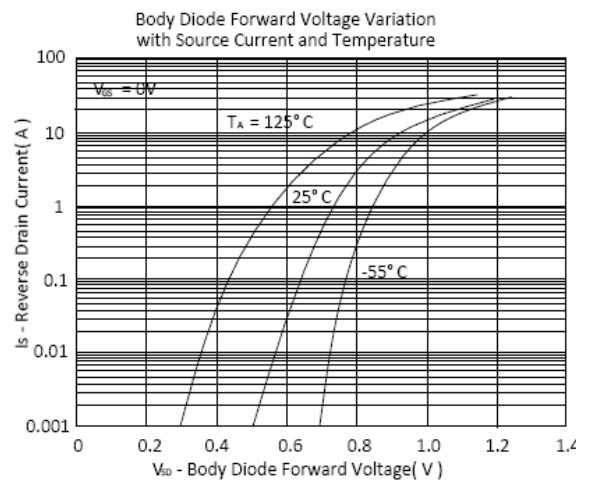
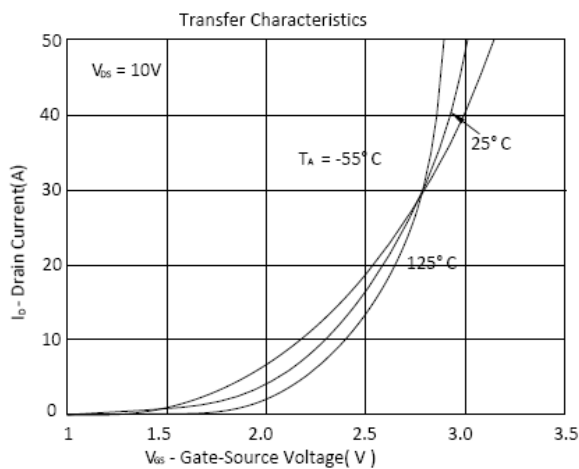
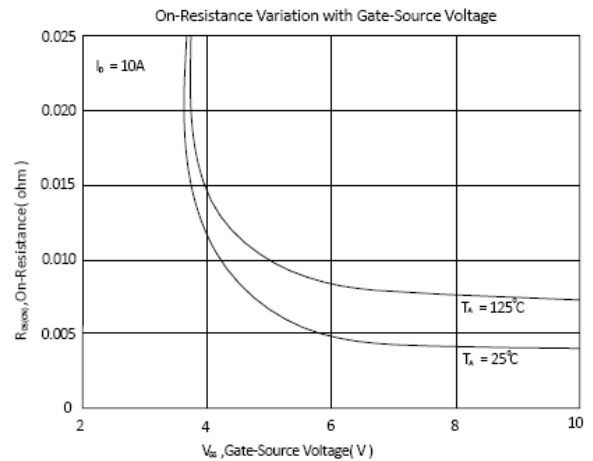
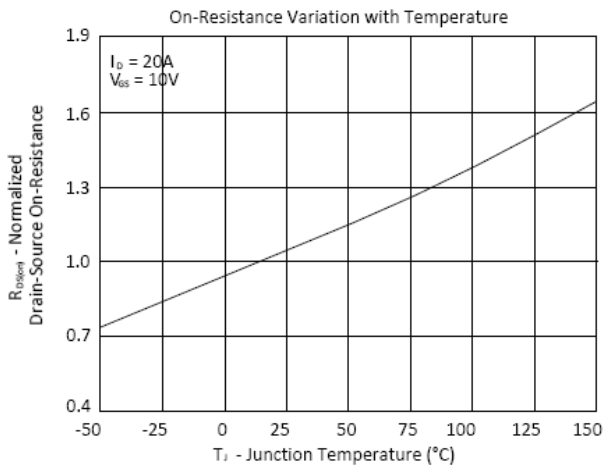
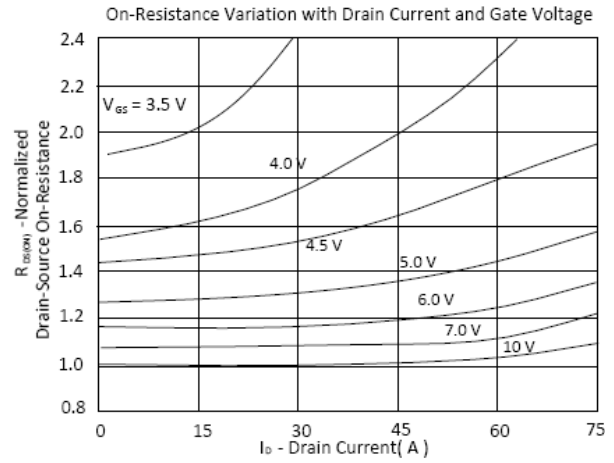
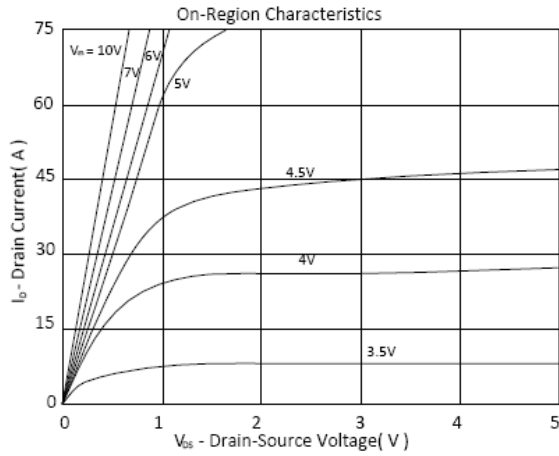
Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Qg (VGS=10V) *1, 2	-	58	-	nC	VDS=15V, VGS=10V, ID=20A
Qg (VGS=4.5V) *1, 2	-	32	-		
Qgs *1, 2	-	10	-		
Qgd *1, 2	-	18	-		
td(ON) *1, 2	-	25	-	ns	VDS=15V, ID=1A, VGS=10V, RGS=2.7Ω
tr *1, 2	-	20	-		
td(OFF) *1, 2	-	70	-		
tf *1, 2	-	15	-		
Rg	-	1.3	-	Ω	VGS=15mV, VDS=0V, f=1MHz
Source-Drain Diode					
IS *1	-	-	10	A	
ISM *3	-	-	40		
VSD *1	-	-	1.2	V	IF=IS, VGS=0V
trr	-	35	-	ns	IF=IS, dIF/dt=100A/μs
Qrr	-	15	-	nC	
IRM(REC)	-	50	-	A	

Note : *1.Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%
 *2.Independent of operating temperature
 *3.Pulse width limited by maximum junction temperature.

Ordering Information

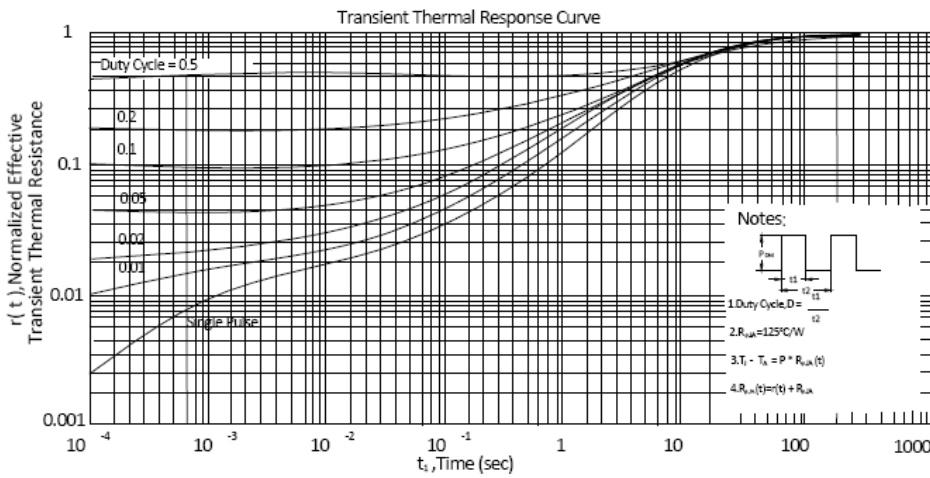
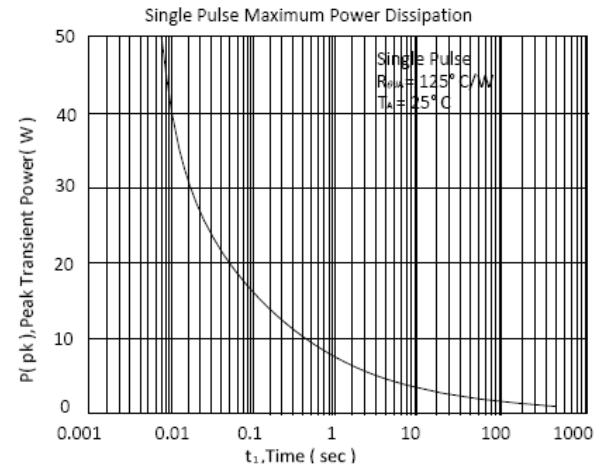
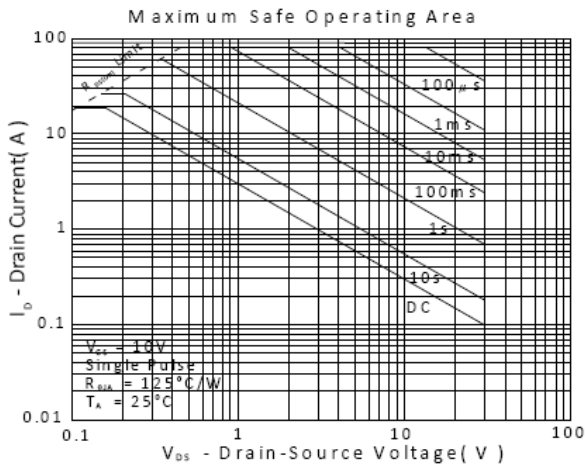
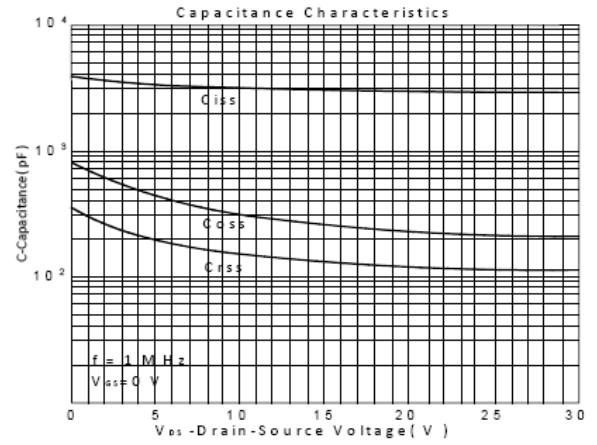
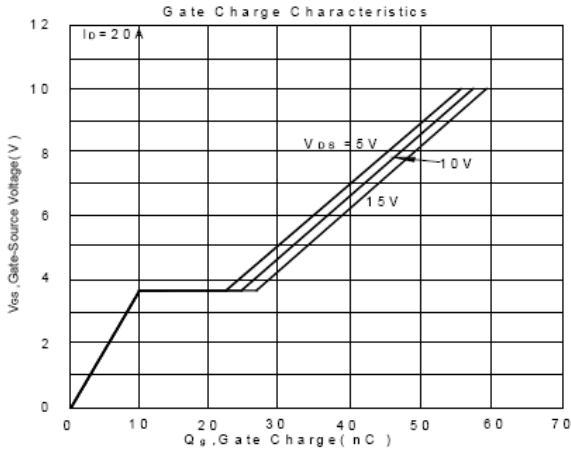
Device	Package	Shipping	Marking
MTB05N03HQ8	SOP-8 (Pb-free & Halogen-free package)	2500 pcs / Tape & Reel	B05N03H

Characteristic Curves

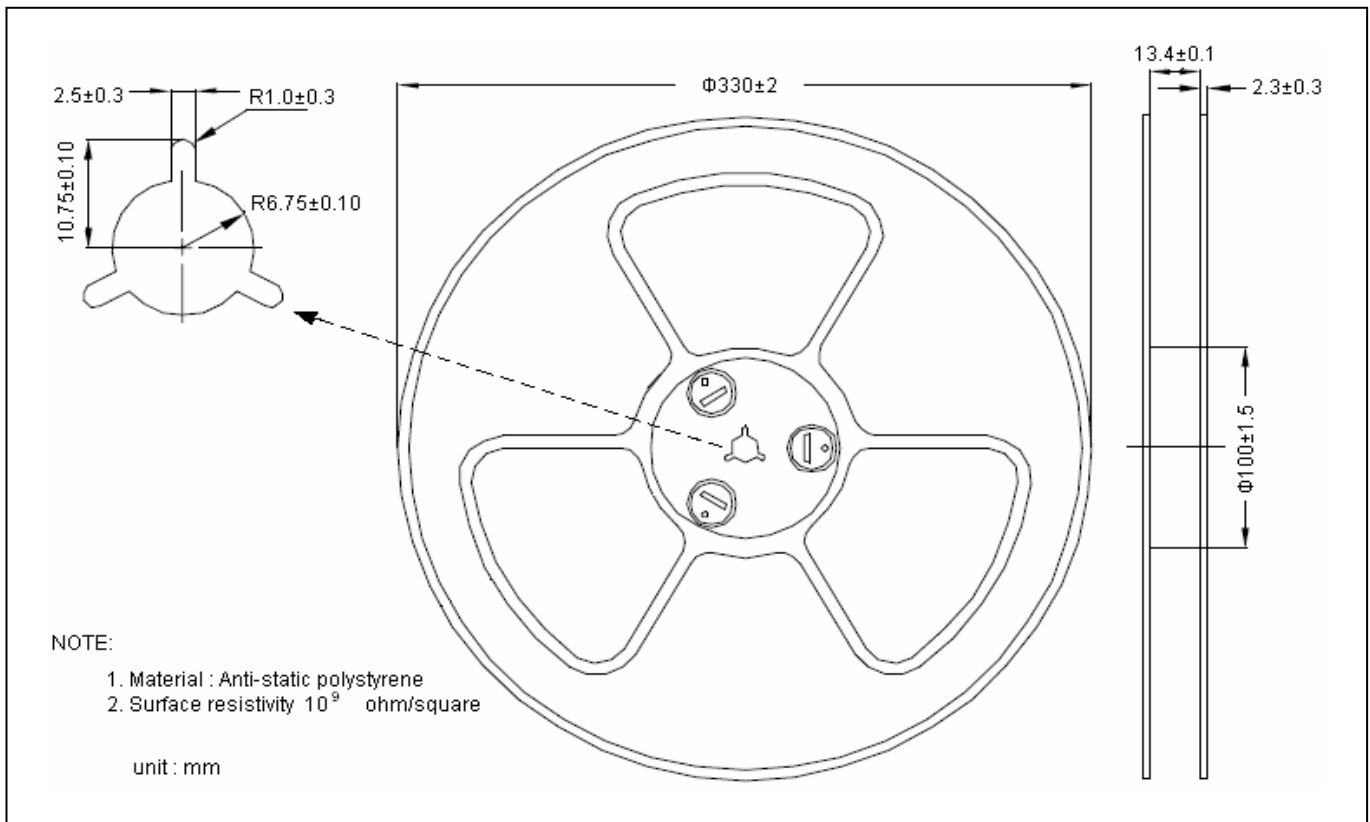




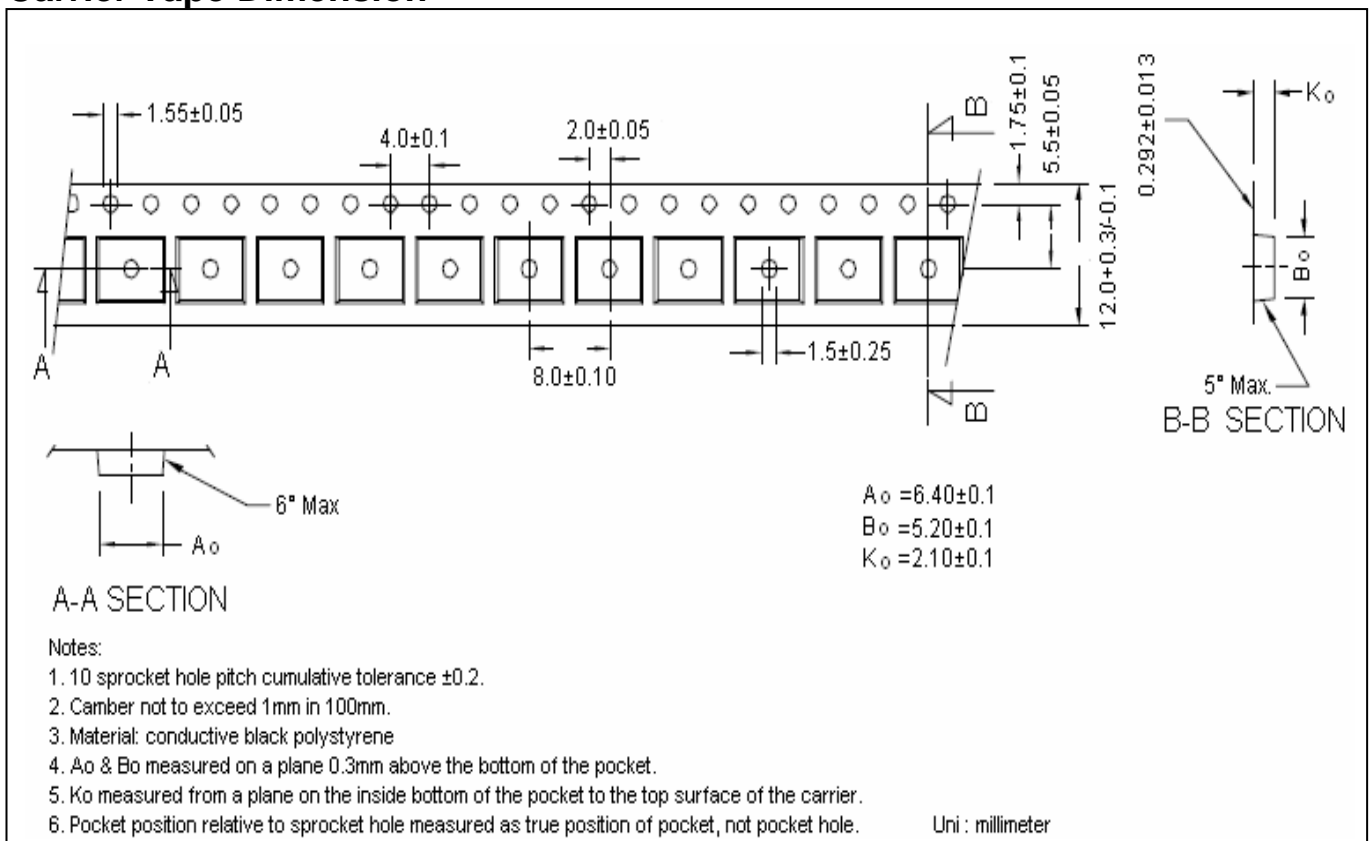
Characteristic Curves(Cont.)



Reel Dimension



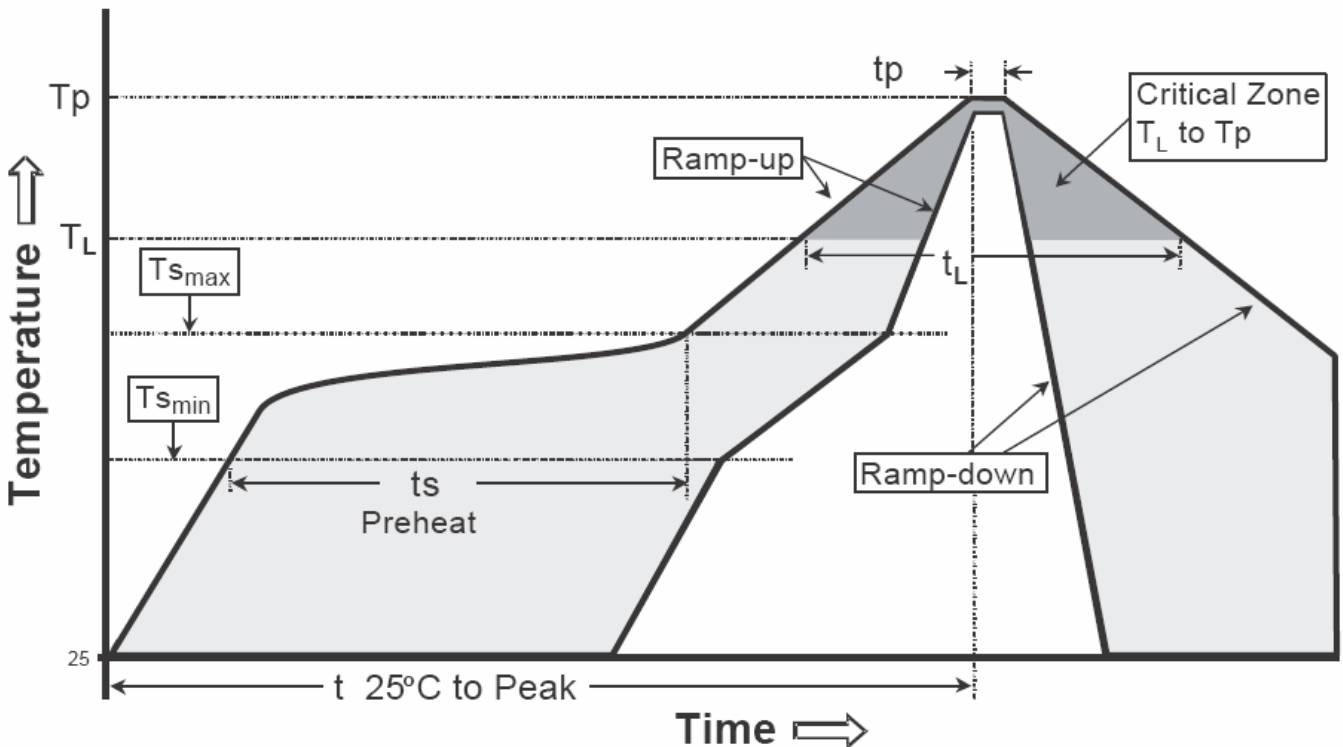
Carrier Tape Dimension



Recommended wave soldering condition

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

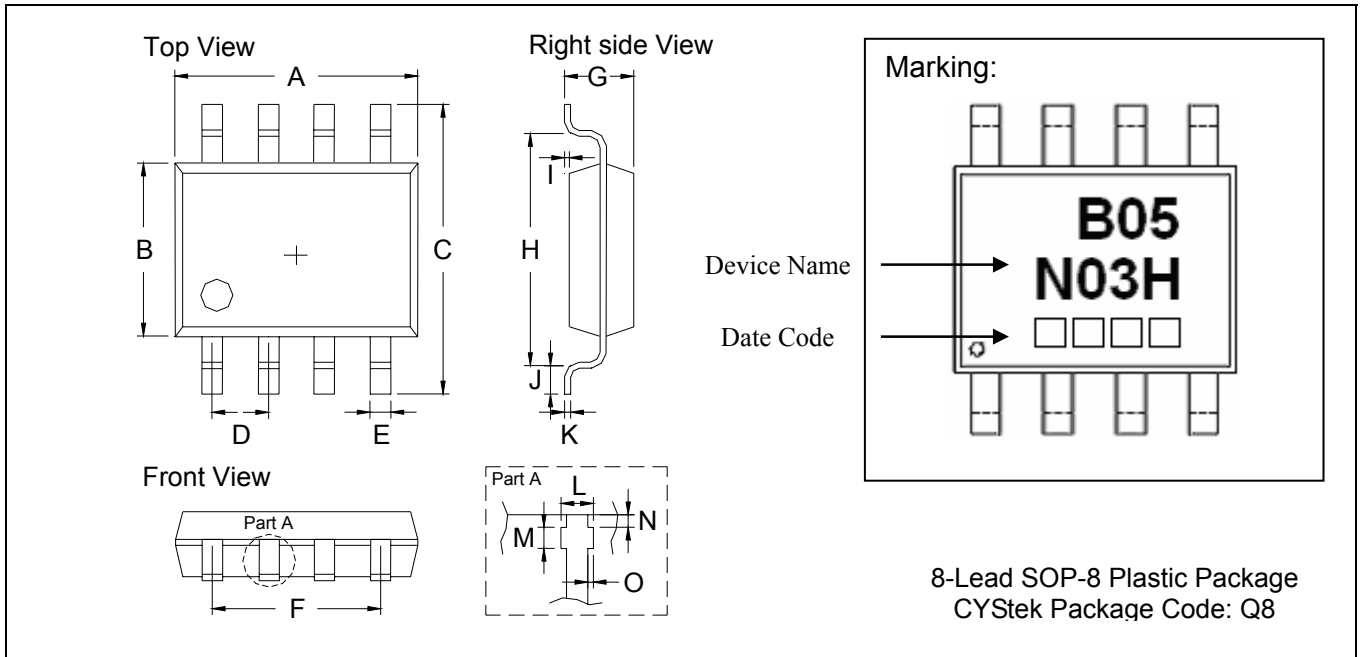
Recommended temperature profile for IR reflow



Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (Tsmax to Tp)	3°C/second max.	3°C/second max.
Preheat		
-Temperature Min(Ts min)	100°C	150°C
-Temperature Max(Ts max)	150°C	200°C
-Time(ts min to ts max)	60-120 seconds	60-180 seconds
Time maintained above:		
-Temperature (Tl)	183°C	217°C
- Time (tl)	60-150 seconds	60-150 seconds
Peak Temperature(Tp)	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.

SOP-8 Dimension



*: Typical

DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.1850	0.2007	4.70	5.10	I	0.0031	0.0110	0.08	0.28
B	0.1457	0.1614	3.70	4.10	J	0.0157	0.0323	0.40	0.83
C	0.2283	0.2441	5.80	6.20	K	0.0074	0.0102	0.19	0.26
D	0.0500*		1.27*		L	0.0145	0.0204	0.37	0.52
E	0.0130	0.0201	0.33	0.51	M	0.0118	0.0197	0.30	0.50
F	0.1472	0.1527	3.74	3.88	N	0.0031	0.0051	0.08	0.13
G	0.0472	0.0638	1.20	1.62	O	0.0000	0.0059	0.00	0.15
H	0.1889	0.2007	4.80	5.10					

Notes: 1.Controlling dimension: millimeters.
 2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.
 3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

Material:

- Lead: Pure tin plated.
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0.

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