

Dual N-Channel Enhancement Mode Power MOSFET

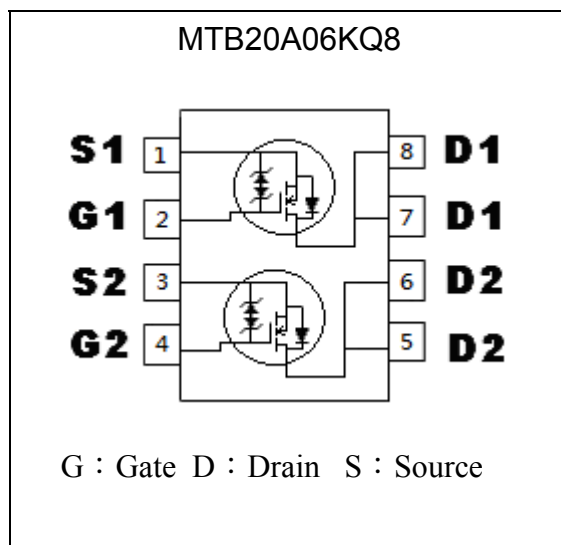
MTB20A06KQ8

Features

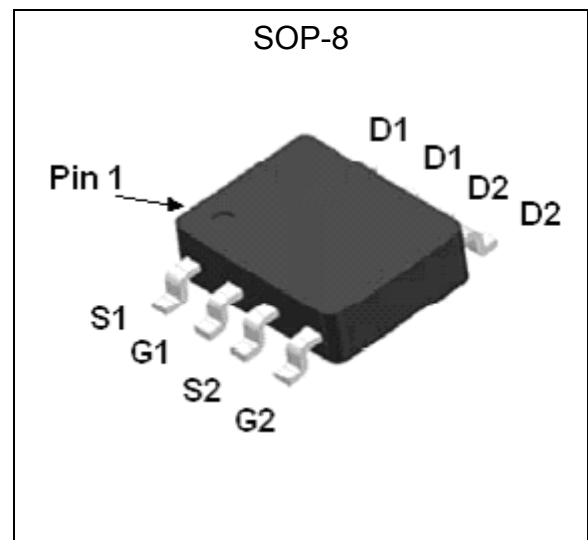
- Single Drive Requirement
- Low On-resistance
- Fast Switching Characteristic
- ESD Protected
- Pb-free & Halogen-free package

BV_{DSS}	60V
I_D@V_{GS}=10V, T_A=25°C	6.3A
I_D@V_{GS}=10V, T_A=70°C	5.0A
R_{DS(ON)}@V_{GS}=10V, I_D=6A	17 mΩ (typ)
R_{DS(ON)}@V_{GS}=4.5V, I_D=6A	20 mΩ (typ)

Symbol

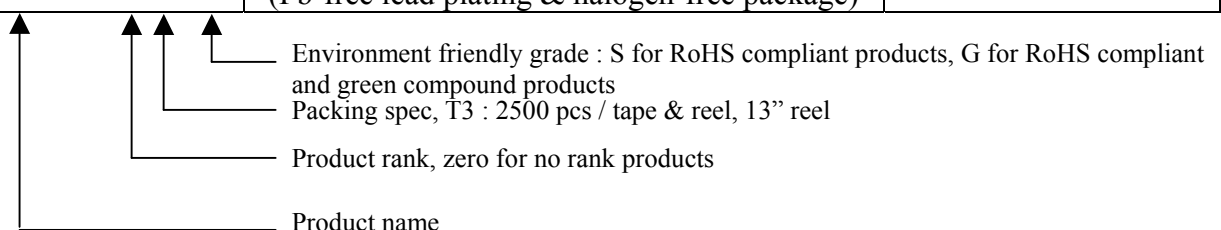


Outline



Ordering Information

Device	Package	Shipping
MTB20A06KQ8-0-T3-G	SOP-8 (Pb-free lead plating & halogen-free package)	2500 pcs / Tape & Reel



**Absolute Maximum Ratings** (Tc=25°C, unless otherwise noted)

Parameter		Symbol	Limits	Unit
Drain-Source Voltage		V _{DS}	60	V
Gate-Source Voltage		V _{GS}	±20	
Continuous Drain Current (Note 2)	T _A =25°C, V _{GS} =10V	I _D	6.3	A
	T _A =70°C, V _{GS} =10V		5.0	
Pulsed Drain Current (Note 1)		I _{DM}	40	
Avalanche Current		I _{AS}	6	
Avalanche Energy @ L=1mH, I _D =6A, V _{DD} =15V		E _{AS}	18 (Note 4)	mJ
Power Dissipation for Dual Operation		P _D	2	W
Power Dissipation for Single Operation			1.6 (Note 2)	
			0.9 (Note 3)	
Operating Junction and Storage Temperature		T _j , T _{stg}	-55~+150	°C

Thermal Data

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-case, max	R _{th,j-c}	40	°C/W
Thermal Resistance, Junction-to-ambient, max, dual	R _{th,j-a}	62.5	
Thermal Resistance, Junction-to-ambient, max , single operation		78 (Note 2)	
		135 (Note 3)	

Note : 1. Pulse width limited by maximum junction temperature.

2. Surface mounted on 1 in² pad of 2 oz copper, t≤10s.

3. Surface mounted on minimum copper pad, pulse width≤10s.

4. 100% tested by conditions of L=1mH, I_{AS}=3A, V_{GS}=10V, V_{DD}=15V.**Characteristics (T_j=25°C, unless otherwise specified)**

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static					
BV _{DSS}	60	-	-	V	V _{GS} =0V, I _D =250μA
V _{GS(th)}	1.0	-	2.5		V _{DS} = V _{GS} , I _D =250μA
G _{FS}	-	15	-	S	V _{DS} =5V, I _D =6A
I _{GSS}	-	-	±10	μA	V _{GS} =±16V
I _{DSS}	-	-	1		V _{DS} =60V, V _{GS} =0V
	-	-	25		V _{DS} =48V, V _{GS} =0V, T _j =85°C
*R _{DS(ON)}	-	17	23	mΩ	V _{GS} =10V, I _D =6A
	-	20	30		V _{GS} =4.5V, I _D =6A
Dynamic					
Q _g *1, 2	-	18.5	-	nC	I _D =6.3A, V _{DS} =48V, V _{GS} =10V
Q _{gs} *1, 2	-	1.9	-		
Q _{gd} *1, 2	-	6.5	-		
C _{iss}	-	714	-	pF	V _{GS} =0V, V _{DS} =30V, f=1MHz
C _{oss}	-	116	-		
C _{rss}	-	58	-		

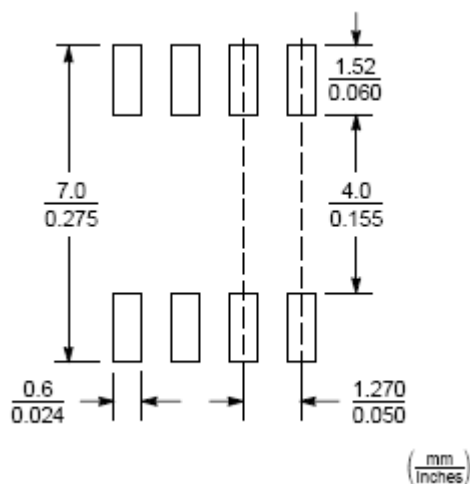
Characteristics (Cont. T_j=25°C, unless otherwise specified)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Dynamic					
t _d (ON) *1, 2	-	8.6	-	ns	V _{DS} =30V, I _D =1A, V _{GS} =10V, R _G =6Ω
t _r *1, 2	-	17.6			
t _d (OFF) *1, 2	-	39.8	-		
t _f *1, 2	-	20	-		
Source-Drain Diode Ratings and Characteristics					
I _S *1	-	-	6	A	
I _{SM} *3	-	-	24		
V _{SD} *1	-	0.82	1.2	V	I _S =6A, V _{GS} =0V
t _{rr}	-	14	-	ns	I _F =6A, dI _F /dt=100A/μs
Q _{rr}	-	10	-	nC	

Note : *1.Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

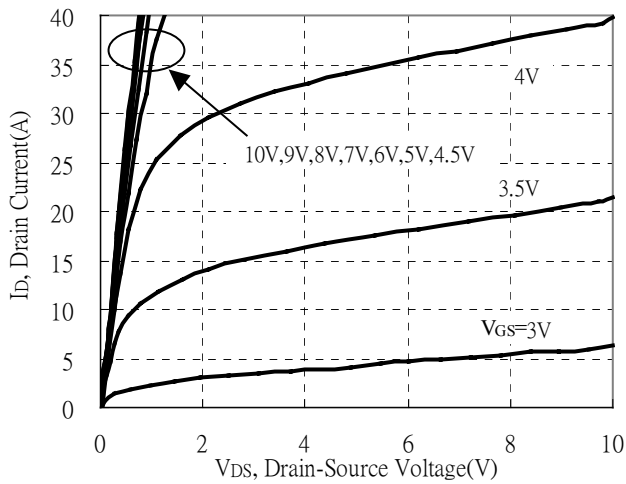
*2.Independent of operating temperature

*3.Pulse width limited by maximum junction temperature.

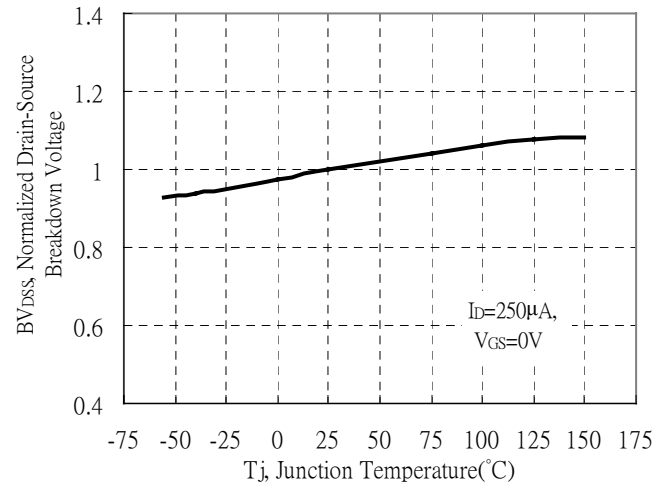
Recommended Soldering Footprint


Typical Characteristics

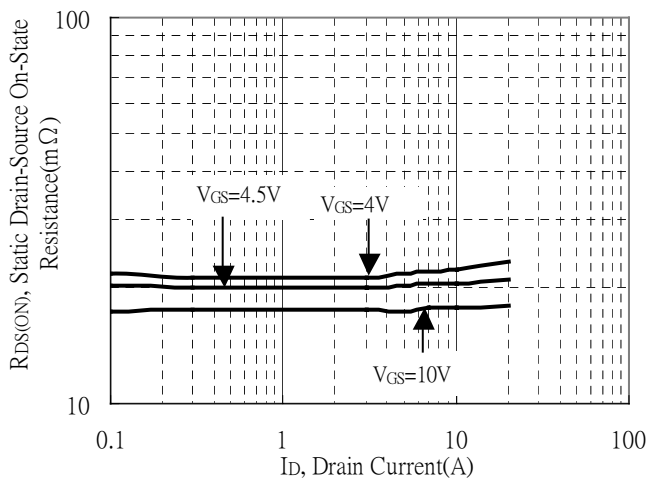
Typical Output Characteristics



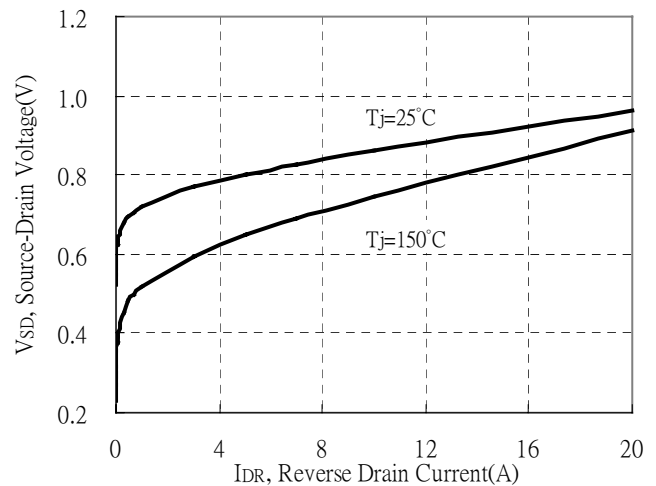
Brekdown Voltage vs Ambient Temperature



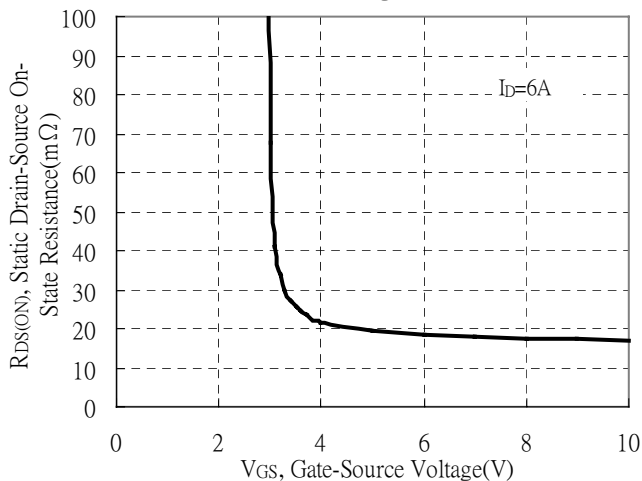
Static Drain-Source On-State resistance vs Drain Current



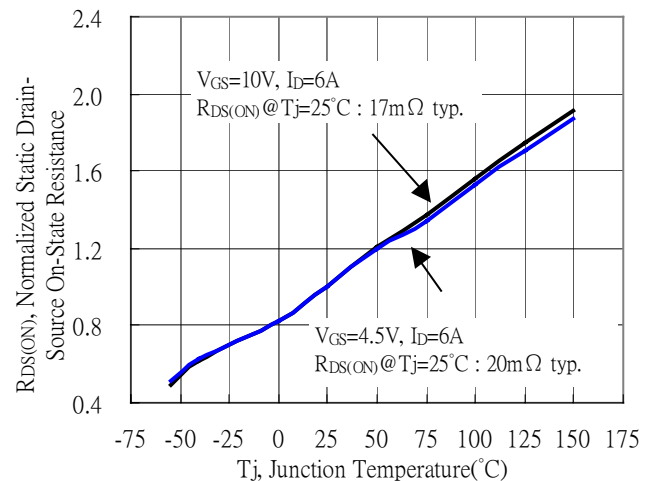
Reverse Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

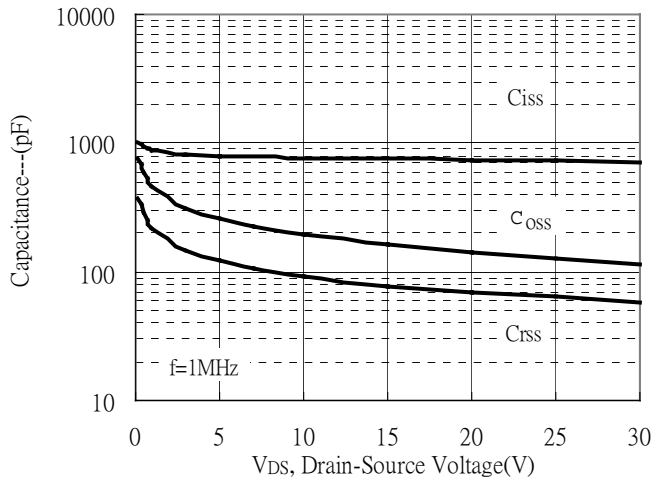


Drain-Source On-State Resistance vs Junction Temperature

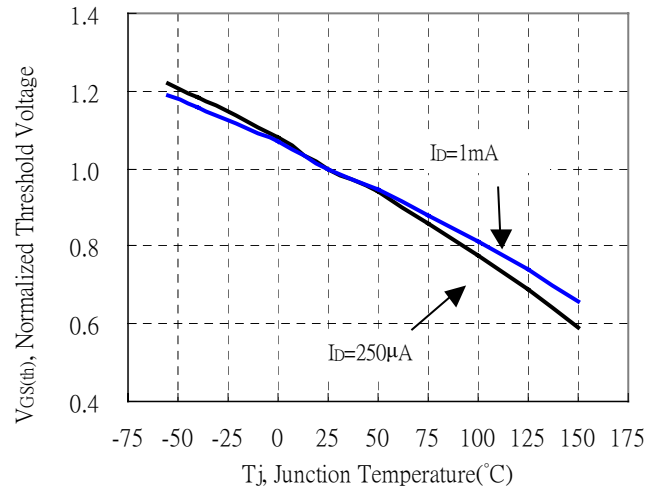


Typical Characteristics(Cont.)

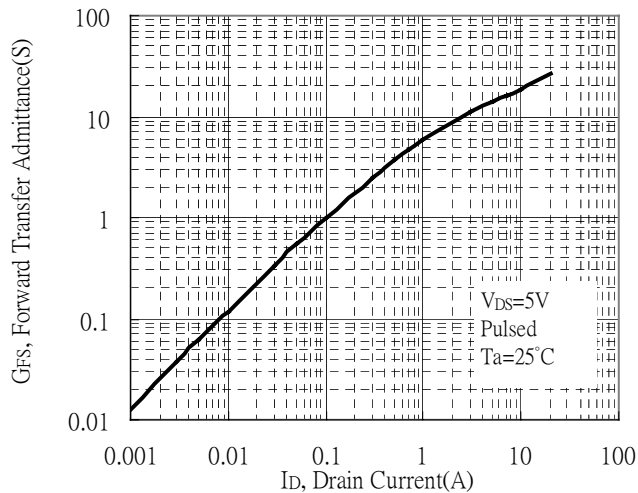
Capacitance vs Drain-to-Source Voltage



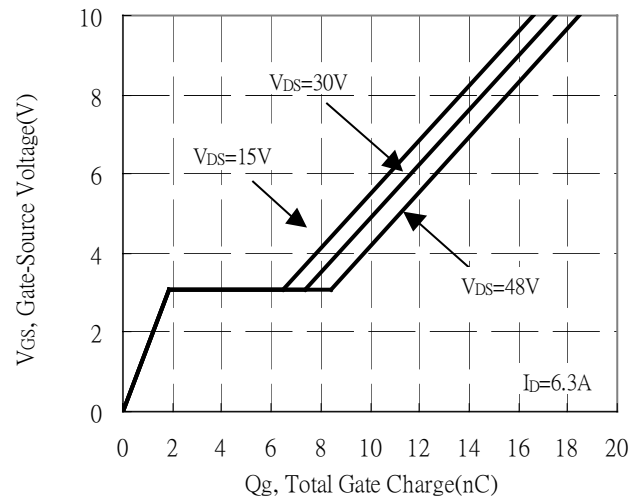
Threshold Voltage vs Junction Temperature



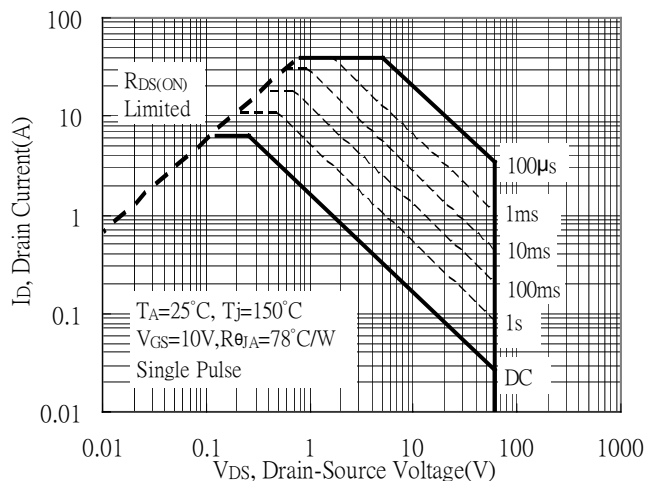
Forward Transfer Admittance vs Drain Current



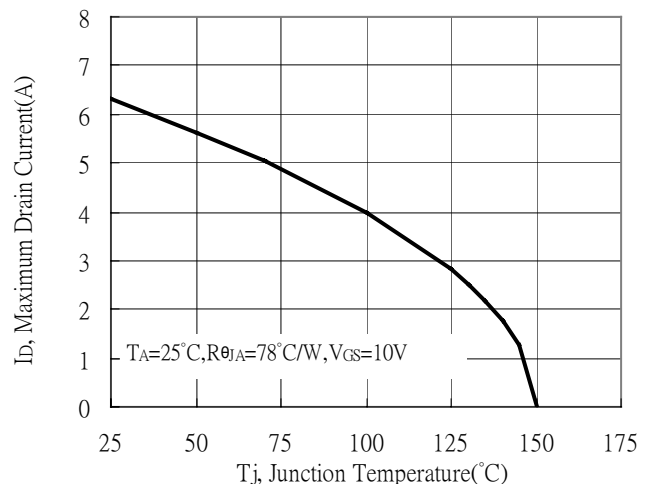
Gate Charge Characteristics



Maximum Safe Operating Area



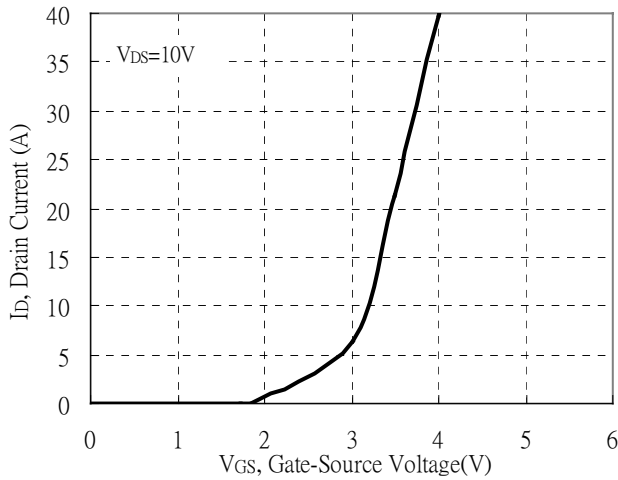
Maximum Drain Current vs Junction Temperature



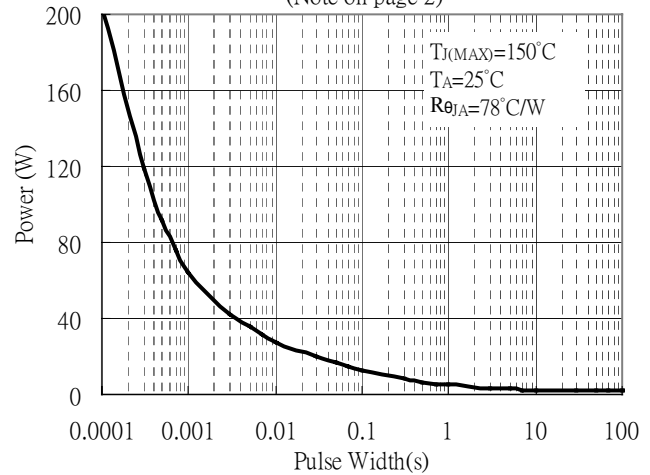


Typical Characteristics(Cont.)

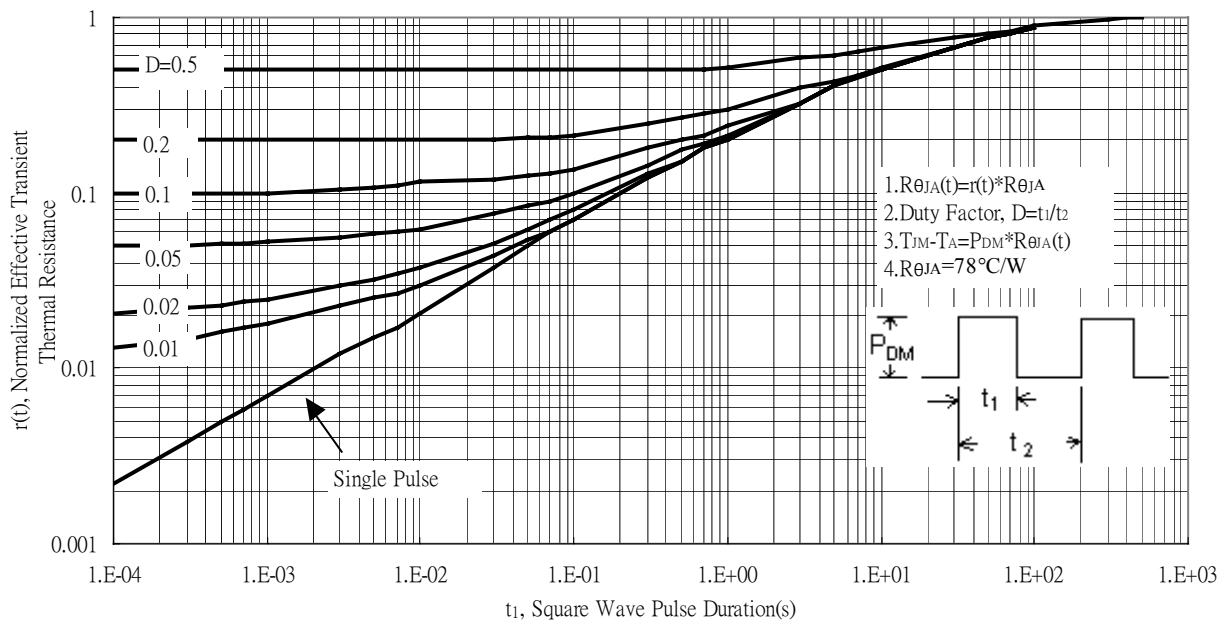
Typical Transfer Characteristics



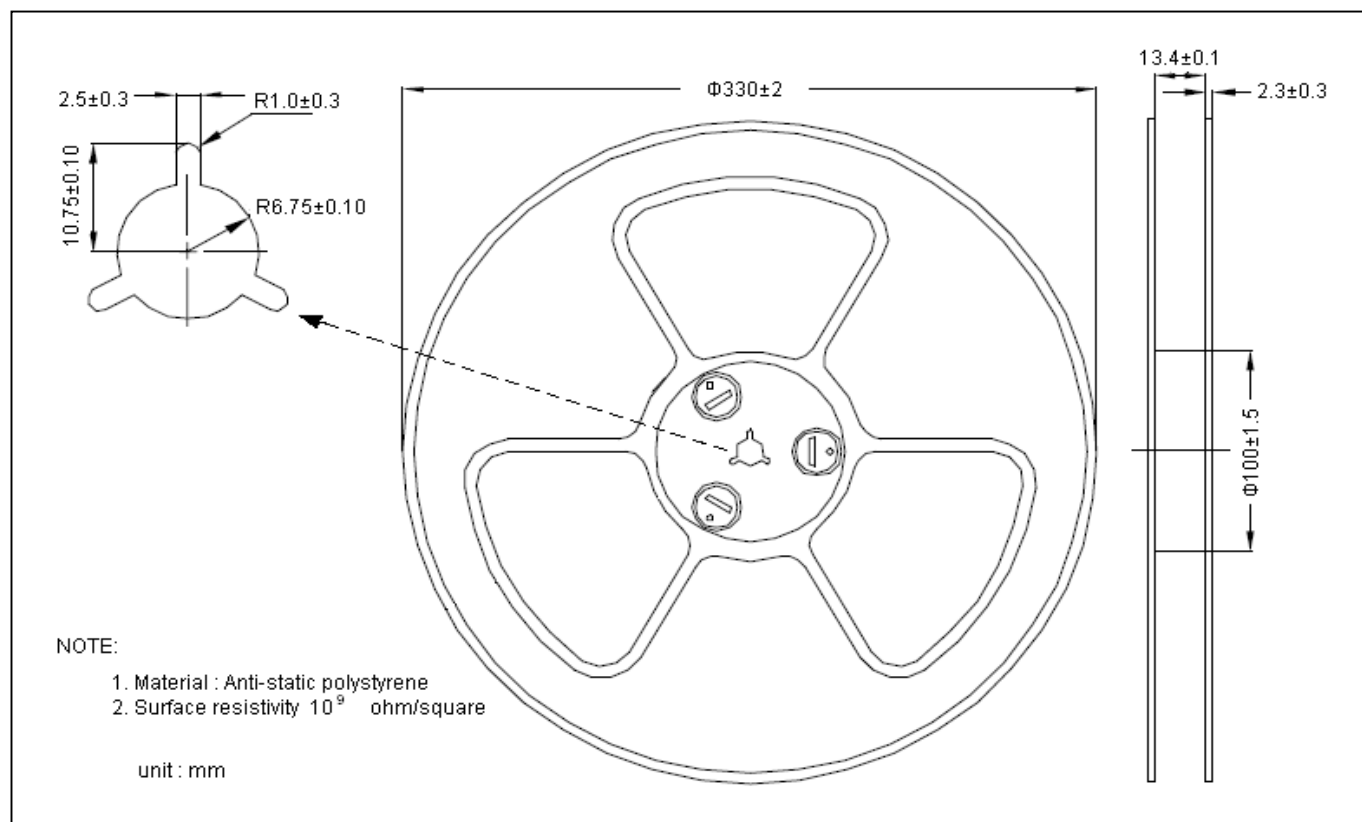
Single Pulse Power Rating, Junction to Ambient
(Note on page 2)



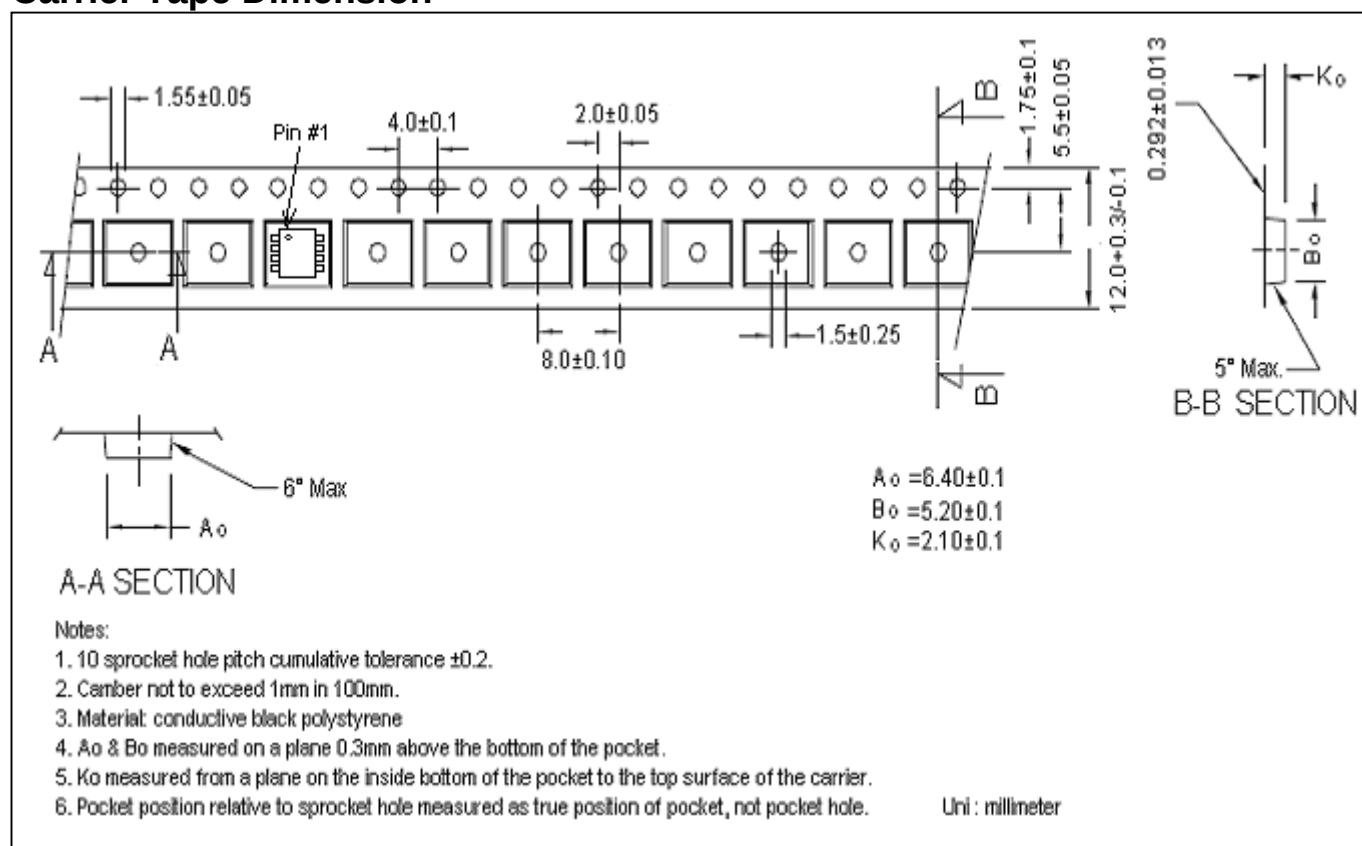
Transient Thermal Response Curves



Reel Dimension



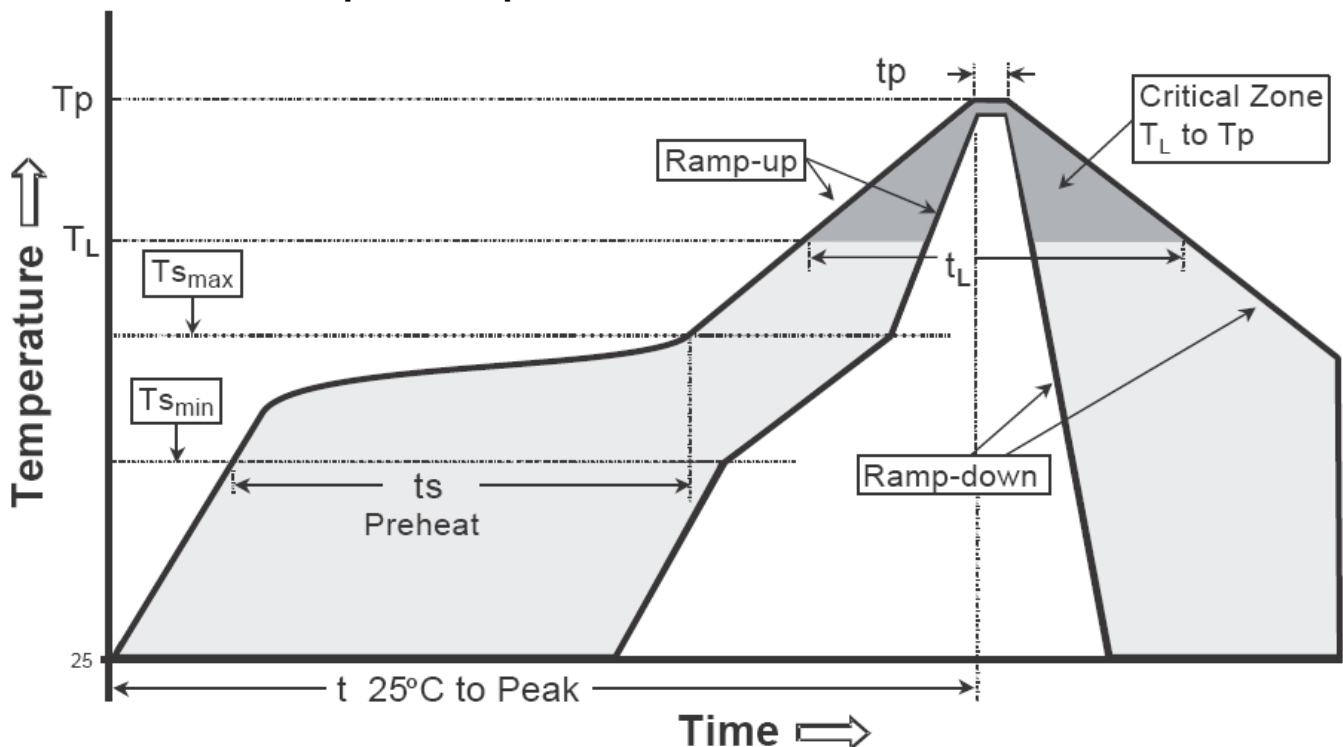
Carrier Tape Dimension



Recommended wave soldering condition

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

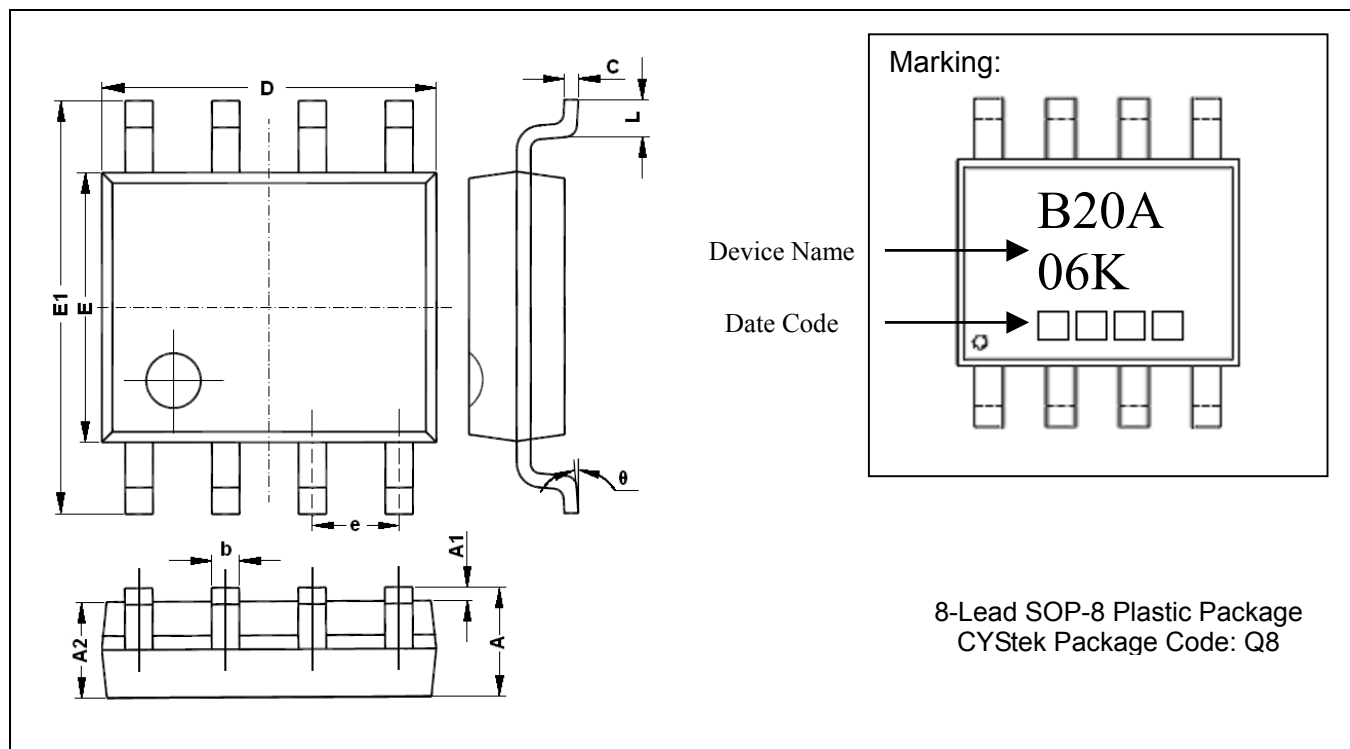
Recommended temperature profile for IR reflow



Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (Tsmax to Tp)	3°C/second max.	3°C/second max.
Preheat		
-Temperature Min(Ts min)	100°C	150°C
-Temperature Max(Ts max)	150°C	200°C
-Time(ts min to ts max)	60-120 seconds	60-180 seconds
Time maintained above:		
-Temperature (Tl)	183°C	217°C
- Time (tL)	60-150 seconds	60-150 seconds
Peak Temperature(Tp)	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.

SOP-8 Dimension



DIM	Millimeters		Inches		DIM	Millimeters		Inches	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	1.350	1.750	0.053	0.069	E	3.800	4.000	0.150	0.157
A1	0.100	0.250	0.004	0.010	E1	5.800	6.200	0.228	0.244
A2	1.350	1.550	0.053	0.061	e	1.270	(BSC)	0.050	(BSC)
b	0.330	0.510	0.013	0.020	L	0.400	1.270	0.016	0.050
c	0.170	0.250	0.006	0.010	θ	0	8°	0	8°
D	4.700	5.100	0.185	0.200					

Notes: 1. Controlling dimension: millimeters.

2. Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.

3. If there is any question with packing specification or packing method, please contact your local CYStek sales office.

Material:

- Lead: Pure tin plated.
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0.

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