

Dual P-Channel Logic Level Enhancement Mode Power MOSFET

MTB60B06Q8

BV_{DSS}	-60V
I_D	-4.5A
$R_{DS(ON)(MAX)}@V_{GS}=-10V, I_D=-3.5A$	56m Ω (typ.)
$R_{DS(ON)(MAX)}@V_{GS}=-4.5V, I_D=-3A$	66m Ω (typ.)

Description

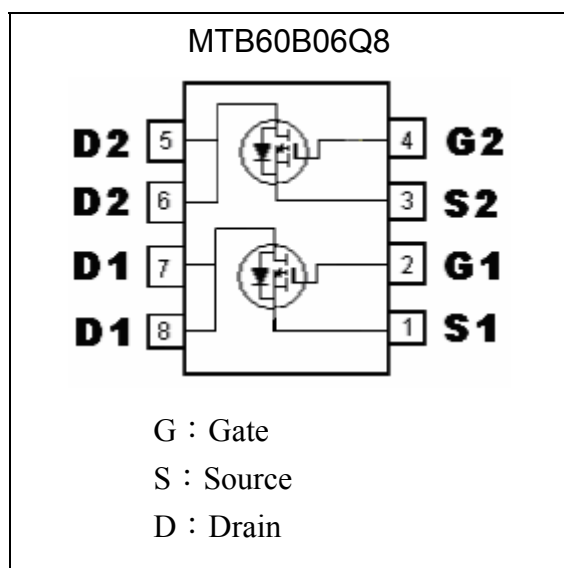
The MTB60B06Q8 provides the designer with the best combination of fast switching, ruggedized device design, ultra low on-resistance and cost effectiveness.

The SOP-8 package is universally preferred for all commercial-industrial surface mount applications.

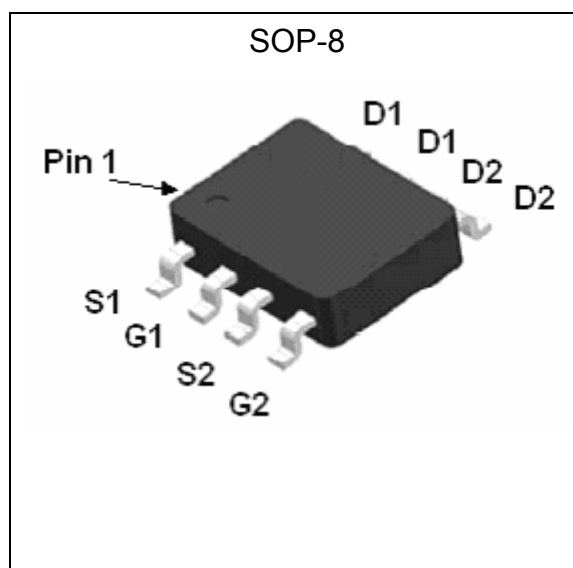
Features

- $R_{DS(ON)}=75m\Omega(max.)@V_{GS}=-10V, I_D=-3.5A$
- Simple drive requirement
- Low on-resistance
- Fast switching speed
- Dual P-ch MOSFET package
- Pb-free lead plating & halogen-free package

Equivalent Circuit



Outline



**Absolute Maximum Ratings** (Ta=25°C)

Parameter		Symbol	Limits	Unit
Drain-Source Voltage		V _{DS}	-60	V
Gate-Source Voltage		V _{GS}	±20	
Continuous Drain Current, T _C =25 °C		I _D	-7	A
Continuous Drain Current, T _C =100 °C			-5	
Continuous Drain Current, T _A =25 °C			-4.5	
Continuous Drain Current, T _A =70 °C			-3.8	
Pulsed Drain Current (Note 1)		I _{DM}	-20	
Power Dissipation	T _A =25°C (Note 3)	P _D	2.4	W
	T _A =100°C		1.3	
Operating Junction and Storage Temperature Range		T _j ; T _{stg}	-55~+175	°C

Thermal Data

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-case, max	R _{th,j-c}	25	°C/W
Thermal Resistance, Junction-to-ambient, max	R _{th,j-a}	62.5 *3	°C/W

Note : 1. Pulse width limited by maximum junction temperature
2. Duty cycle ≤ 1%
3. Surface mounted on 1 in² copper pad of FR-4 board, 125°C/W when mounted on minimum copper pad

**Characteristics (T_j=25°C, unless otherwise specified)**

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static					
BV _{DSS}	-60	-	-	V	V _{GS} =0V, I _D =-250μA
V _{GS(th)}	-1	-1.5	-2.5	V	V _{DS} = V _{GS} , I _D =-250μA
I _{GSS}	-	-	±100	nA	V _{GS} =±20V
I _{DSS}	-	-	-1	μA	V _{DS} =-48V, V _{GS} =0V
	-	-	-10		V _{DS} =-48V, V _{GS} =0V, T _j =125°C
I _{D(ON)} *1	-4.5	-	-	A	V _{DS} =-5V, V _{GS} =-10V
*R _{DS(ON)} *1	-	56	75	mΩ	V _{GS} =-10V, I _D =-3.5A
	-	66	90		V _{GS} =-4.5V, I _D =-3A
G _{FS} *1	-	9	-	S	V _{DS} =-5V, I _D =-3.5A
Dynamic					
Q _g *1, 2	-	21	-	nC	V _{DS} =-30V, I _D =-4.5A, V _{GS} =-10V
Q _{gs} *1, 2	-	4.4	-		
Q _{gd} *1, 2	-	5.1	-		
t _{d(ON)} *1, 2	-	15	-	ns	V _{DS} =-30V, I _D =-1A, V _{GS} =-10V, R _G =6Ω
t _r *1, 2	-	10	-		
t _{d(OFF)} *1, 2	-	47	-		
t _f *1, 2	-	25	-		
C _{iss}	-	1460	-	pF	V _{GS} =0V, V _{DS} =-25V, f=1MHz
C _{oss}	-	72	-		
C _{rss}	-	61	-		
Source-Drain Diode					
I _S *1	-	-	-2.3	A	
I _{SM} *3	-	-	-9.2		
V _{SD} *1	-	-0.78	-1.2	V	I _F = I _S , V _{GS} =0V
t _{rr} *1	-	12	-	ns	I _F = I _S , dI _F /dt=100A/μs
Q _{rr} *1	-	8	-	nC	

Note : *1.Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

*2.Independent of operating temperature

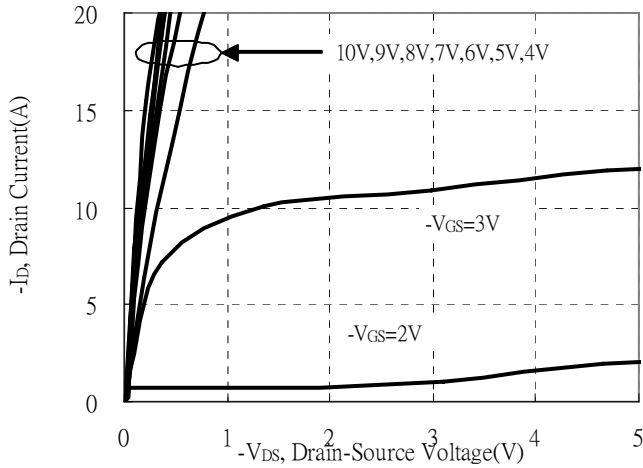
*3.Pulse width limited by maximum junction temperature.

Ordering Information

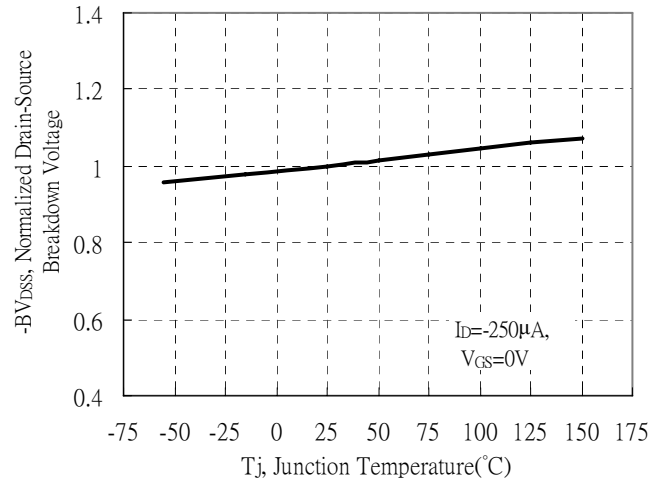
Device	Package	Shipping
MTB60B06Q8-0-T3-G	SOP-8 (Pb-free lead plating & Halogen-free package)	2500 pcs / Tape & Reel

Typical Characteristics

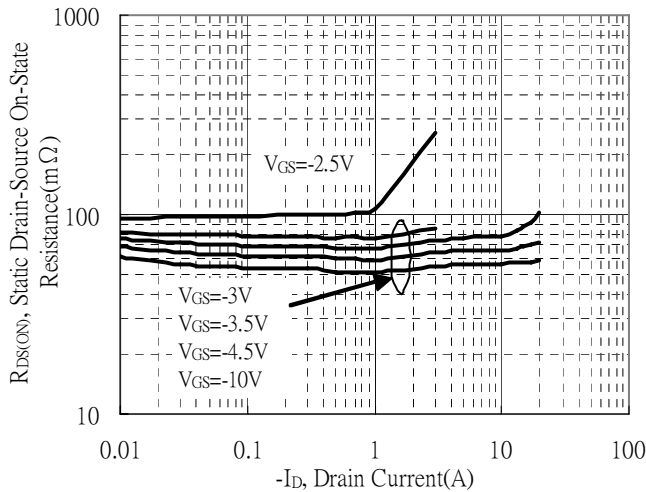
Typical Output Characteristics



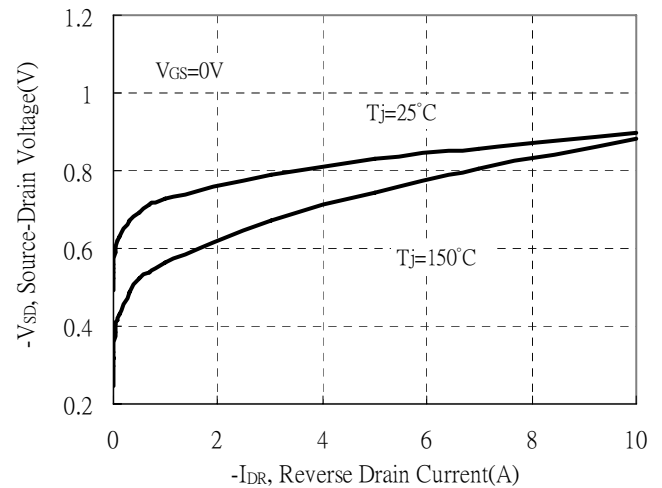
Breakdown Voltage vs Ambient Temperature



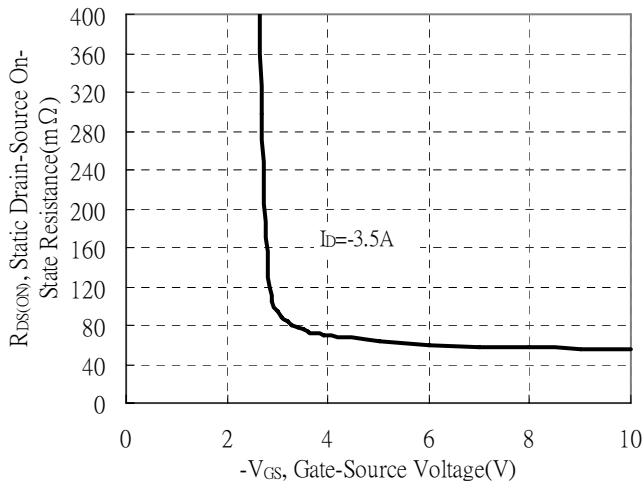
Static Drain-Source On-State resistance vs Drain Current



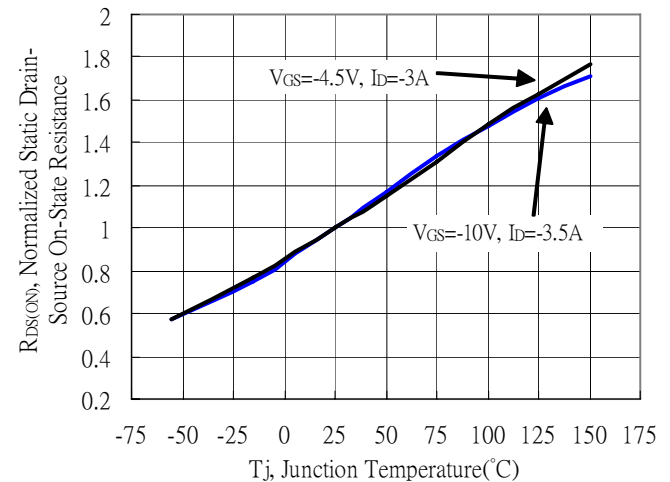
Reverse Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

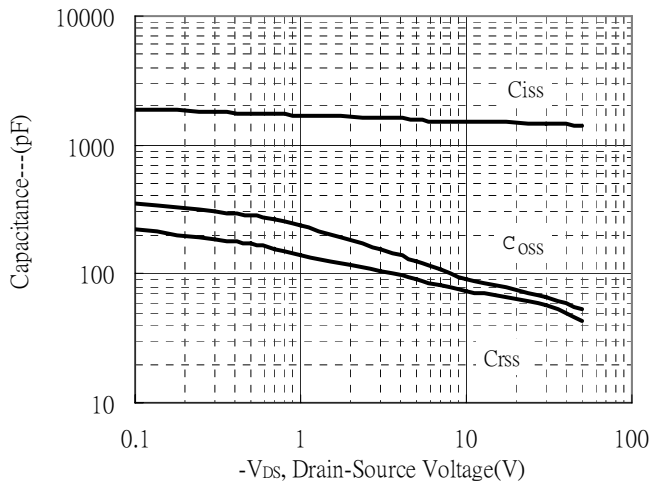


Drain-Source On-State Resistance vs Junction Temperature

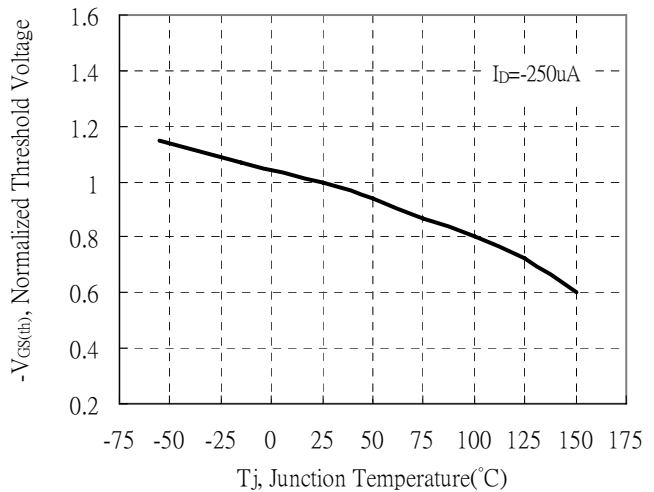


Typical Characteristics(Cont.)

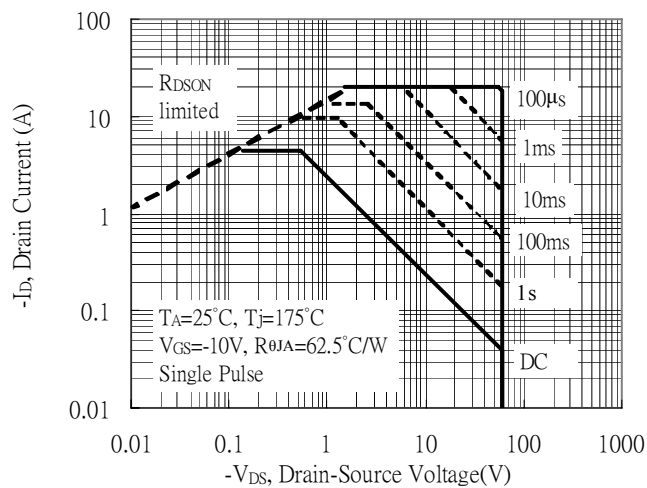
Capacitance vs Drain-to-Source Voltage



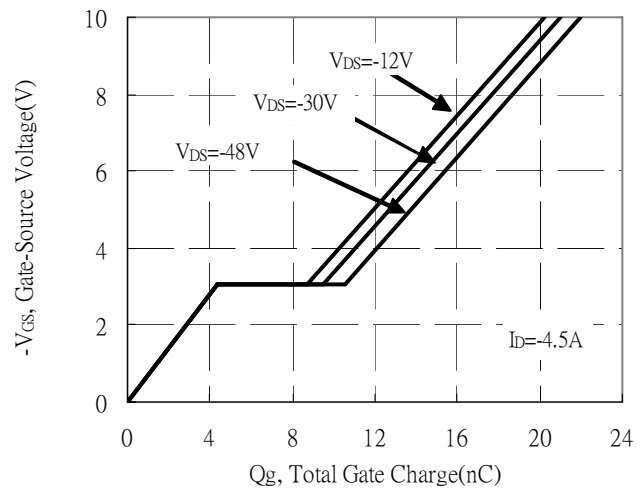
Threshold Voltage vs Junction Temperature



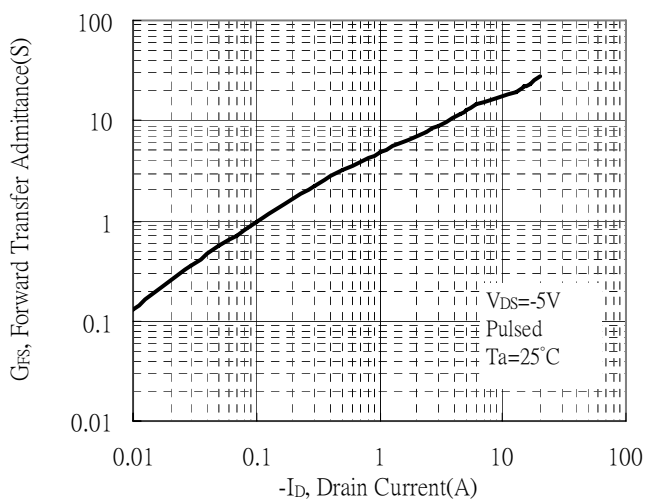
Maximum Safe Operating Area



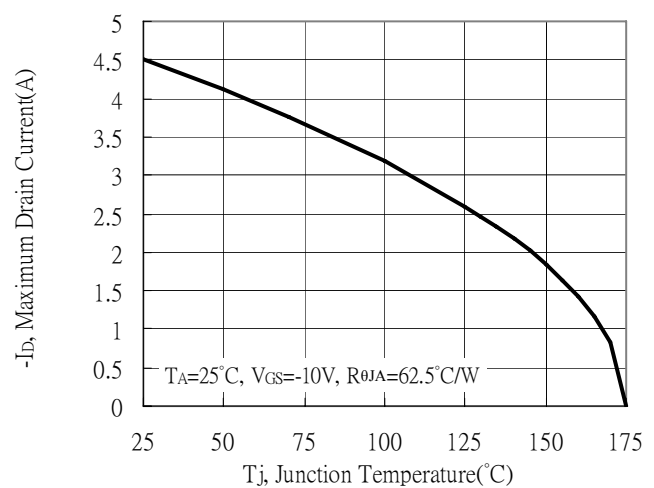
Gate Charge Characteristics



Forward Transfer Admittance vs Drain Current

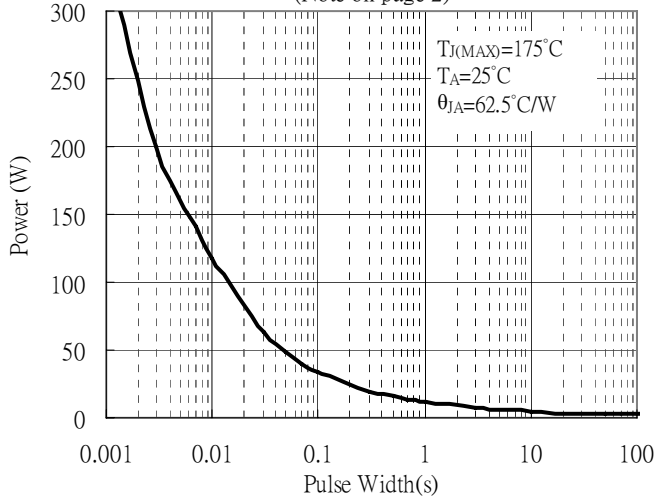


Maximum Drain Current vs Junction Temperature

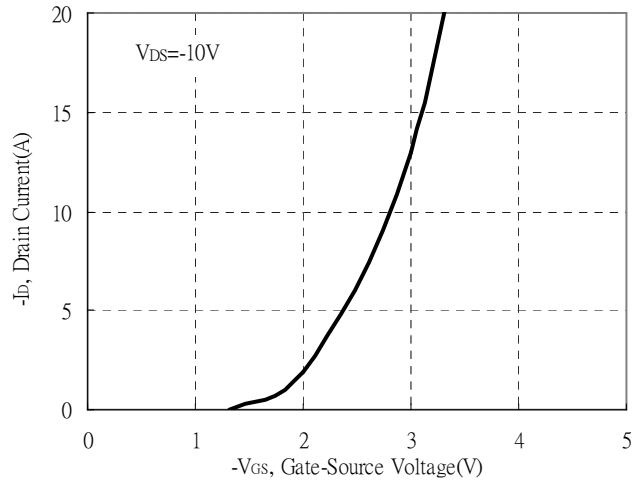


Typical Characteristics(Cont.)

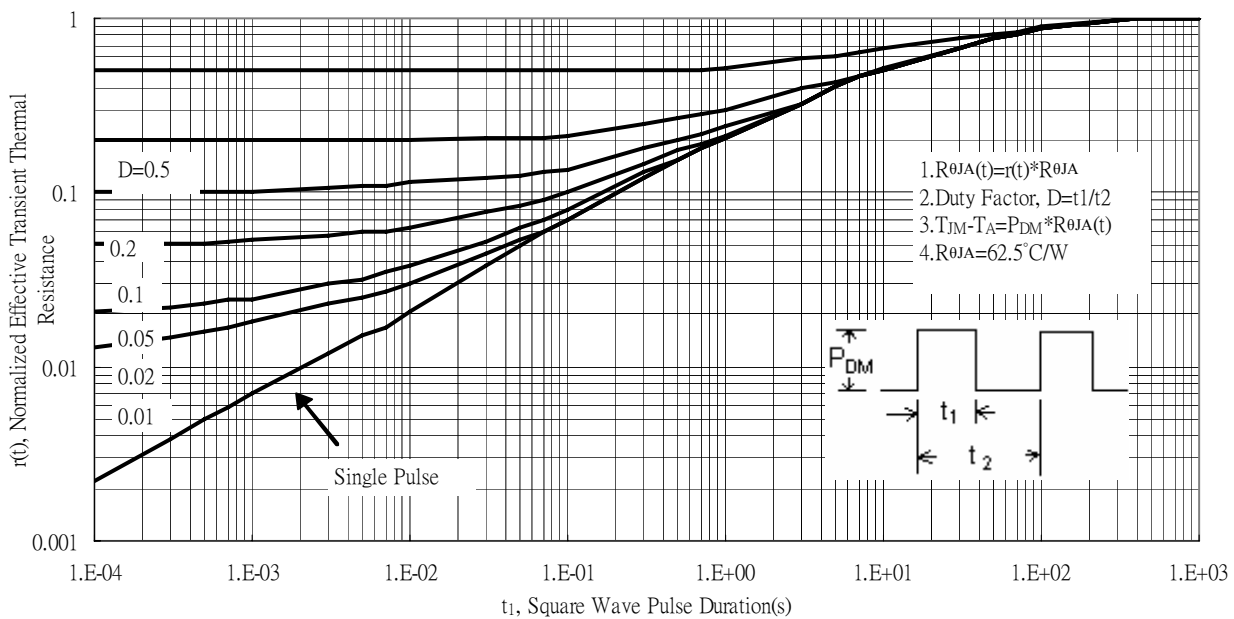
Single Pulse Power Rating, Junction to Ambient
 (Note on page 2)



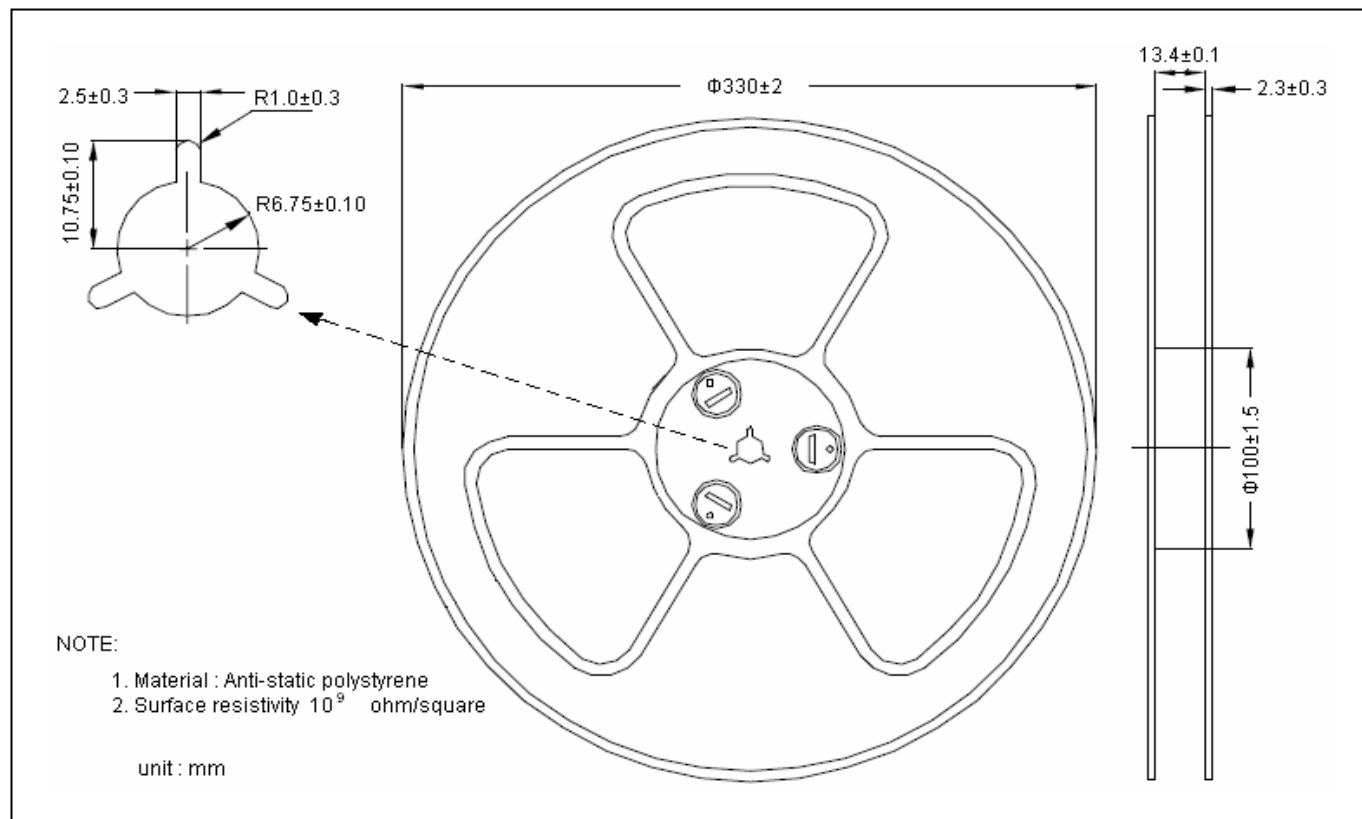
Typical Transfer Characteristics



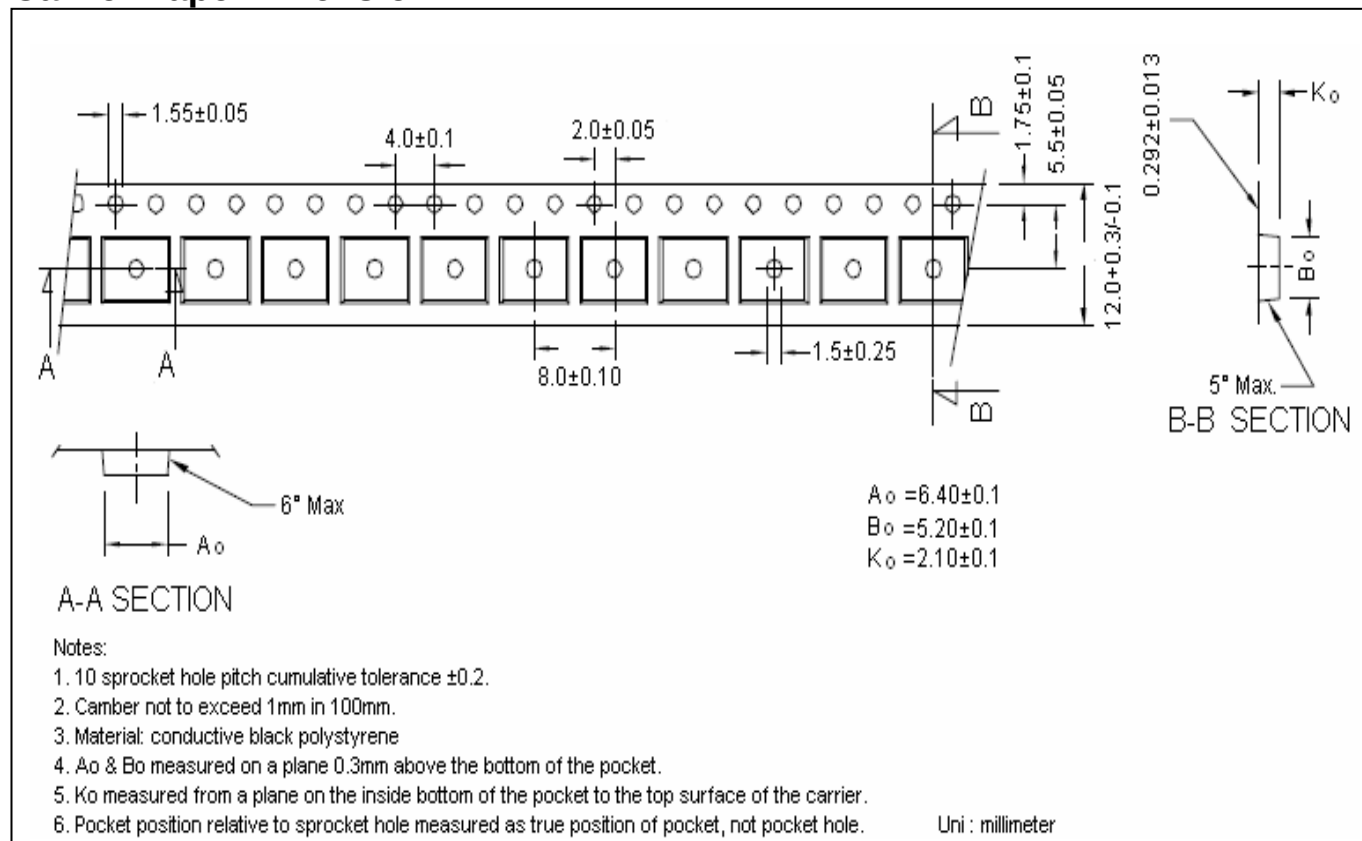
Transient Thermal Response Curves



Reel Dimension



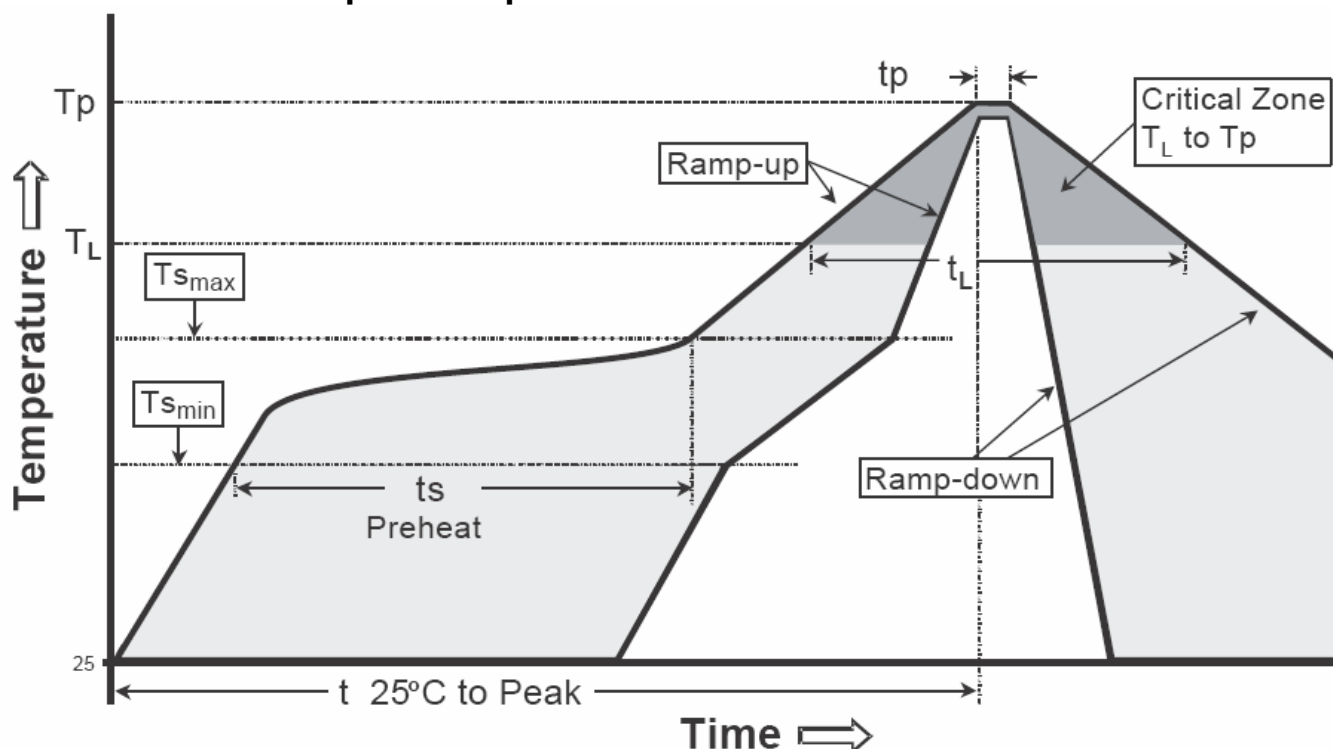
Carrier Tape Dimension



Recommended wave soldering condition

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

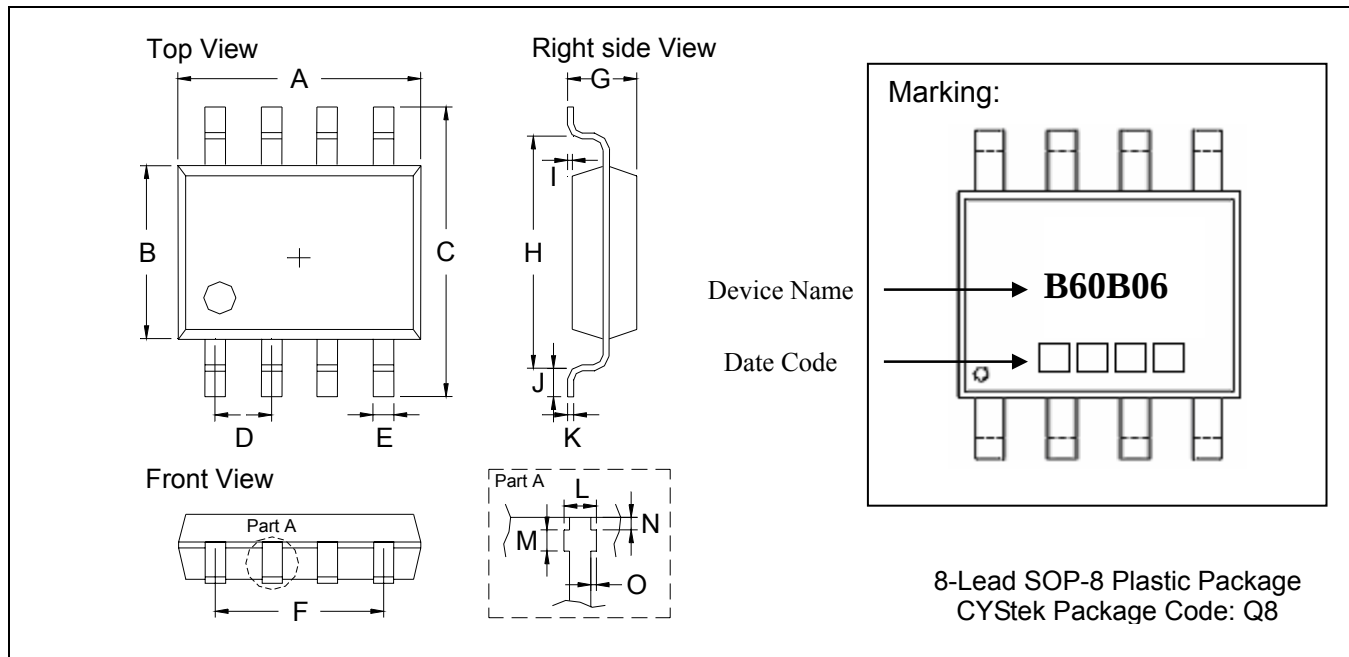
Recommended temperature profile for IR reflow



Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (Tsmax to Tp)	3°C/second max.	3°C/second max.
Preheat		
-Temperature Min(Ts min)	100°C	150°C
-Temperature Max(Ts max)	150°C	200°C
-Time(ts min to ts max)	60-120 seconds	60-180 seconds
Time maintained above:		
-Temperature (Tl)	183°C	217°C
- Time (tL)	60-150 seconds	60-150 seconds
Peak Temperature(Tp)	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.

SOP-8 Dimension



*: Typical

DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.1850	0.2007	4.70	5.10	I	0.0031	0.0110	0.08	0.28
B	0.1457	0.1614	3.70	4.10	J	0.0157	0.0323	0.40	0.83
C	0.2283	0.2441	5.80	6.20	K	0.0074	0.0102	0.19	0.26
D	0.0500*		1.27*		L	0.0145	0.0204	0.37	0.52
E	0.0130	0.0201	0.33	0.51	M	0.0118	0.0197	0.30	0.50
F	0.1472	0.1527	3.74	3.88	N	0.0031	0.0051	0.08	0.13
G	0.0472	0.0638	1.20	1.62	O	0.0000	0.0059	0.00	0.15
H	0.1889	0.2007	4.80	5.10					

Notes: 1. Controlling dimension: millimeters.

2. Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.

3. If there is any question with packing specification or packing method, please contact your local CYStek sales office.

Material:

- Lead: pure tin plated.
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0.

Important Notice:

- All rights are reserved. Reproduction in whole or in part is prohibited without the prior written approval of CYStek.
- CYStek reserves the right to make changes to its products without notice.
- CYStek **semiconductor products are not warranted to be suitable for use in Life-Support Applications, or systems.**
- CYStek assumes no liability for any consequence of customer product design, infringement of patents, or application assistance.