N-Channel Enhancement Mode Power MOSFET

MTE05N10FP

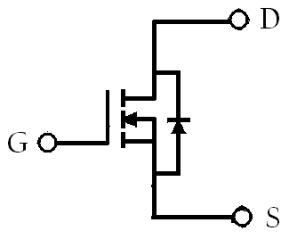
BV_{DSS}	100V
I_D @ V_{GS}=10V, T_C=25°C	109A
$R_{DSON(TYP)}$ @ V_{GS}=10V, I_D=20A	5.9mΩ
$R_{DSON(TYP)}$ @ V_{GS}=7V, I_D=20A	6.2mΩ

Features

- Low On Resistance
- Simple Drive Requirement
- Low Gate Charge
- Fast Switching Characteristic
- Insulating package, front/back side insulating voltage=2500V(AC)
- RoHS compliant package

Symbol

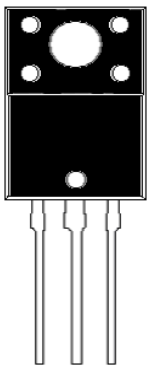
MTE05N10FP



G : Gate D : Drain S : Source

Outline

TO-220FP



G D S

Ordering Information

Device	Package	Shipping
MTE05N10FP-0-UB-S	TO-220FP (RoHS compliant package)	50 pcs/tube, 20 tubes/box, 4 boxes / carton

Environment friendly grade : S for RoHS compliant products, G for RoHS compliant and green compound products

Packing spec, UB : 50 pcs / tube, 20 tubes/box

Product rank, zero for no rank products

Product name

Absolute Maximum Ratings (T_C=25°C, unless otherwise noted)

Parameter		Symbol	Limits	Unit
Drain-Source Voltage		V_{DS}	100	V
Gate-Source Voltage		V_{GS}	±30	
Continuous Drain Current @ T_C=25°C, V_{GS}=10V(silicon limit)		I_D	109	A
Continuous Drain Current @ T_C=100°C, V_{GS}=10V(silicon limit)			77	
Continuous Drain Current @ T_C=25°C, V_{GS}=10V(package limit)	(Note 1)		60	
Pulsed Drain Current	(Note 3)	I_{DM}	300	
Continuous Drain Current @ T_A=25°C, V_{GS}=10V	(Note 2)	I_{DSM}	11	
Continuous Drain Current @ T_A=70°C, V_{GS}=10V	(Note 2)		8.8	
Avalanche Current	(Note 3)	I_{AS}	25	
Avalanche Energy @ L=1mH, I_D=25A, R_G=25Ω	(Note 2)	E_{AS}	323	mJ
Repetitive Avalanche Energy@ L=0.1mH	(Note 3)	E_{AR}	20	
Power Dissipation	T_C=25°C (Note 1)	P_D	200	W
	T_C=100°C (Note 1)		100	
Power Dissipation	T_A=25°C (Note 2)	P_{DSM}	2	W
	T_A=70°C (Note 2)		1.3	
Operating Junction and Storage Temperature		Tj, Tstg	-55~+175	°C

Thermal Data

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-case, max	$R_{th,j-c}$	0.75	°C/W
Thermal Resistance, Junction-to-ambient, max, t≤10s (Note 2)	$R_{th,j-a}$	15	°C/W
Thermal Resistance, Junction-to-ambient, max (Note 2)		62.5	°C/W

Note : 1.The power dissipation P_D is based on $T_{J(MAX)}$=175°C, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.

2. The value of $R_{\theta JA}$ is measured with the device mounted on 1 in² FR-4 board with 2 oz. copper, in a still air environment with T_A=25°C. The power dissipation PDSM is based on $R_{\theta JA}$ and the maximum allowed junction temperature of 150°C. The value in any given application depends on the user's specific board design, and the maximum temperature of 175°C may be used if the PCB allows it.

3. Repetitive rating, pulse width limited by junction temperature $T_{J(MAX)}$=175°C. Ratings are based on low frequency and low duty cycles to keep initial T_J=25°C.

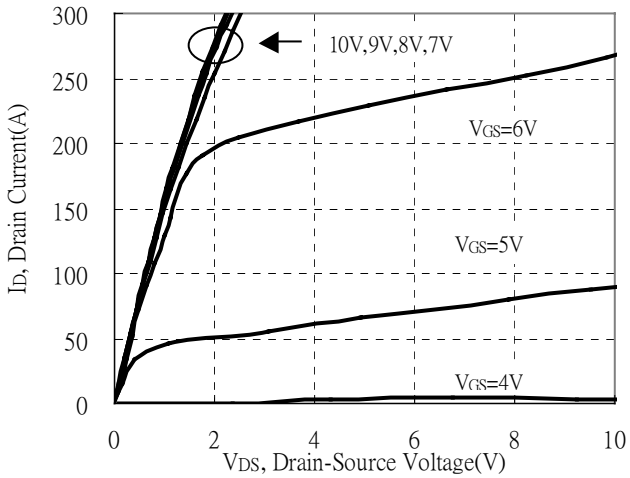
Characteristics (Tj=25°C, unless otherwise specified)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static					
BV_{DSS}	100	-	-	V	V_{GS}=0V, I_D=250μA
$\Delta BV_{DSS}/\Delta Tj$	-	0.1	-	V/°C	Reference to 25°C, I_D=250μA
$V_{GS(th)}$	2.0	-	4.0	V	V_{DS} = V_{GS}, I_D=250μA
*G_{FS}	-	52	-	S	V_{DS} =5V, I_D=20A
I_{GSS}	-	-	±100	nA	V_{GS}=±30V
I_{DSS}	-	-	1	μA	V_{DS} =80V, V_{GS} =0V
	-	-	25		V_{DS} =80V, V_{GS} =0V, Tj=55°C
*$R_{DS(ON)}$	-	5.9	8.3	mΩ	V_{GS} =10V, I_D=20A
	-	6.2	8.6		V_{GS} =7V, I_D=10A
Dynamic					
*Qg	-	116	-	nC	I_D=20A, V_{DS}=50V, V_{GS}=10V
*Qgs	-	27	-		
*Qgd	-	37	-		
*$t_{d(ON)}$	-	37	-	ns	V_{DS}=50V, I_D=20A, V_{GS}=10V, R_G=3Ω
*tr	-	35	-		
*$t_{d(OFF)}$	-	90	-		
*t_f	-	24	-		
Ciss	-	5797	-	pF	V_{GS}=0V, V_{DS}=30V, f=1MHz
Coss	-	660	-		
Crss	-	206	-		
Source-Drain Diode					
*I_S	-	-	60	A	
*V_{SD}	-	0.65	1	V	I_S=1A, V_{GS}=0V
*trr	-	50	-	ns	V_{GS}=0V, I_F=20A, dI/dt=100A/μs
*Qrr	-	104	-	nC	

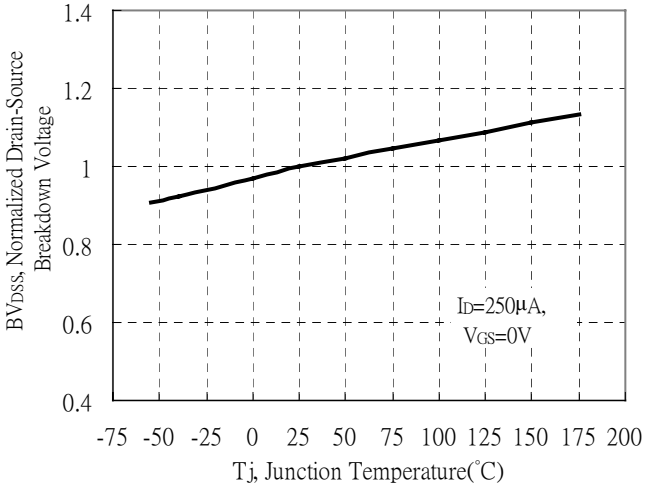
*Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

Typical Characteristics

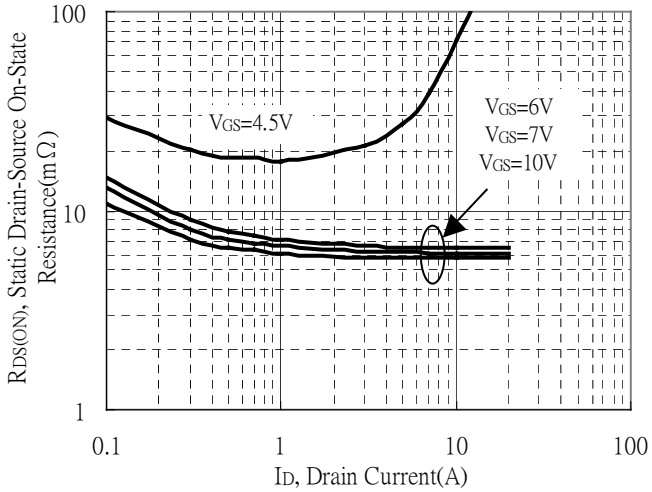
Typical Output Characteristics



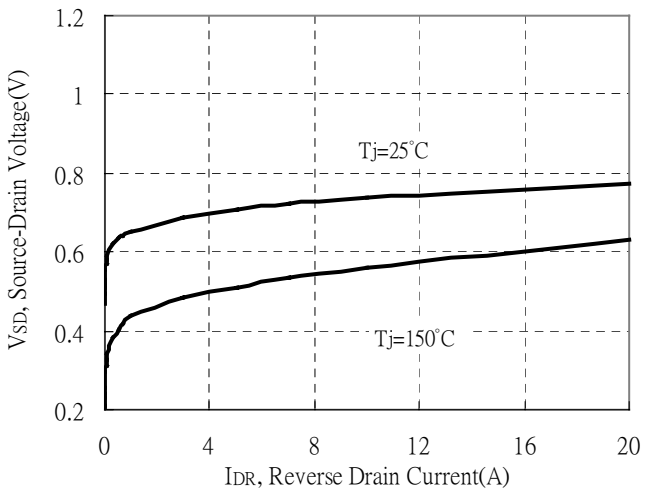
Brekdown Voltage vs Junction Temperature



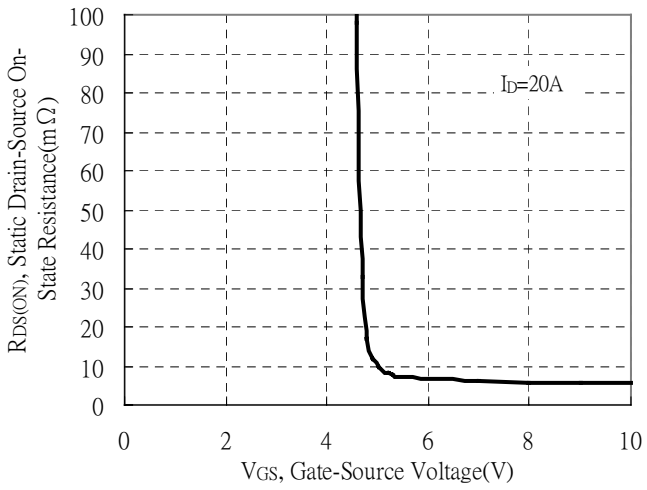
Static Drain-Source On-State resistance vs Drain Current



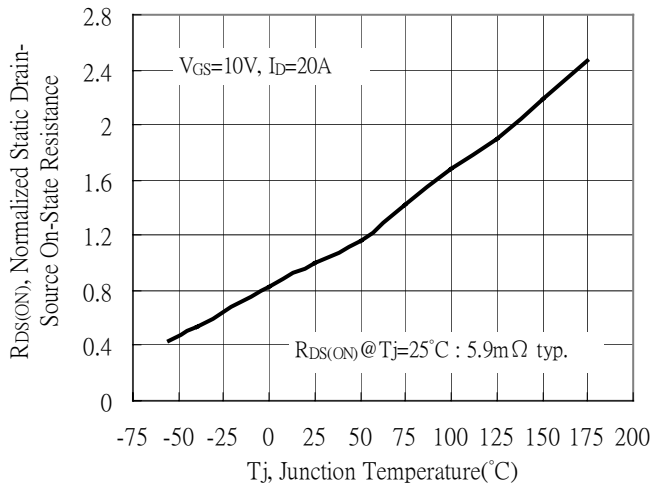
Reverse Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

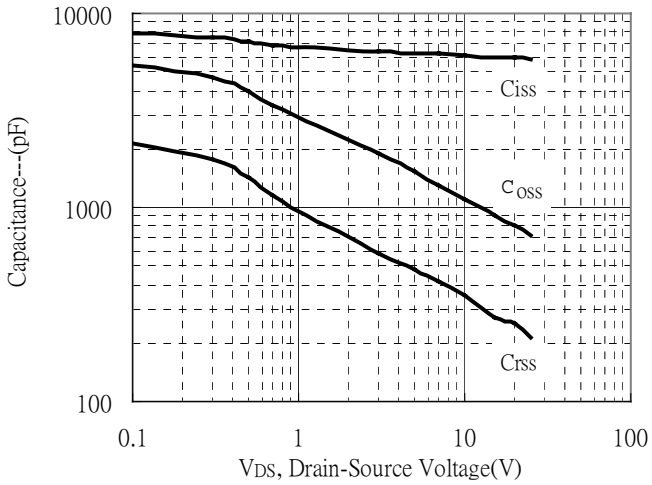


Drain-Source On-State Resistance vs Junction Tempearture

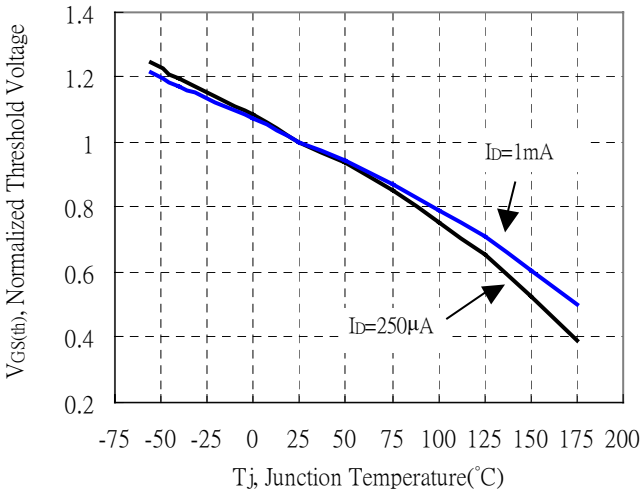


Typical Characteristics(Cont.)

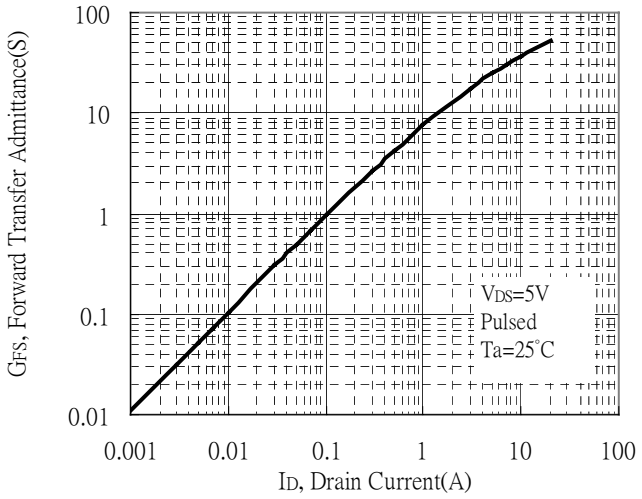
Capacitance vs Drain-to-Source Voltage



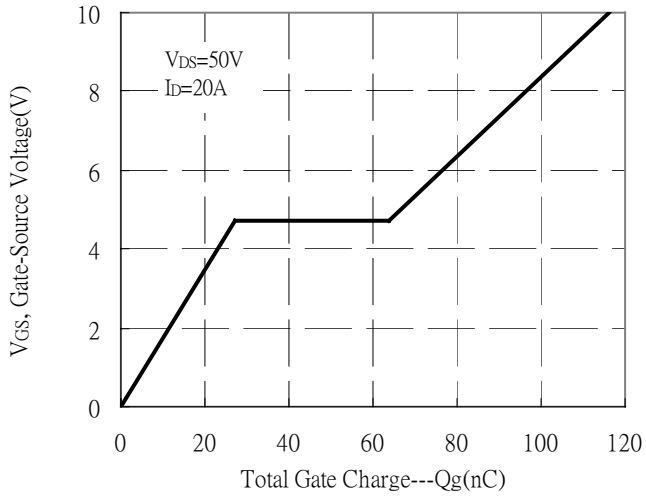
Threshold Voltage vs Junction Tempearture



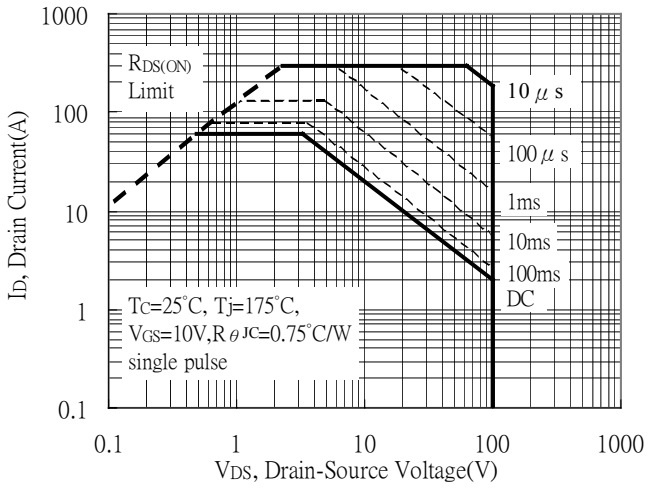
Forward Transfer Admittance vs Drain Current



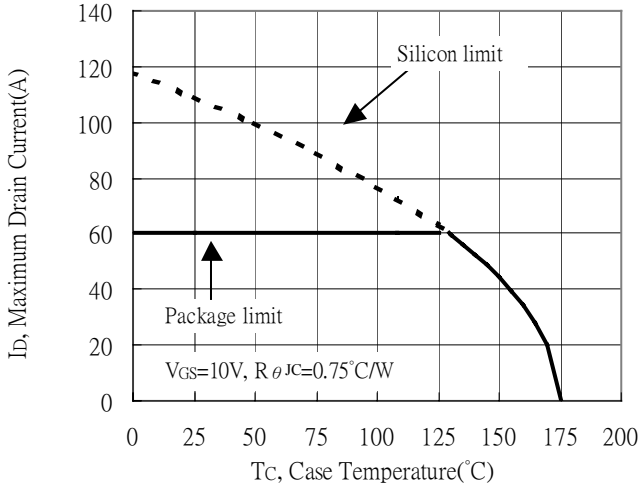
Gate Charge Characteristics



Maximum Safe Operating Area

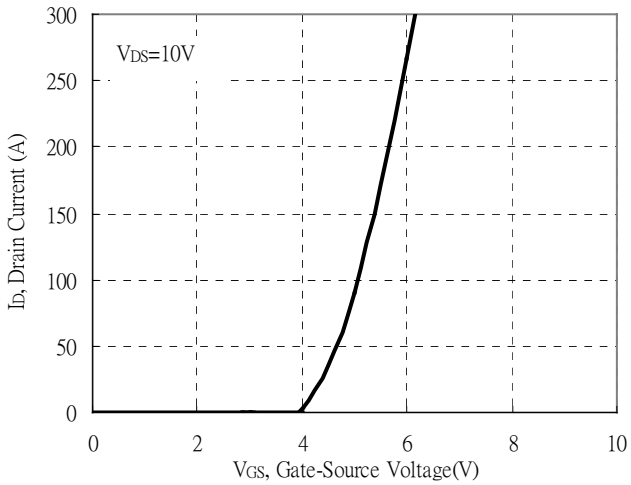


Maximum Drain Current vs Case Temperature

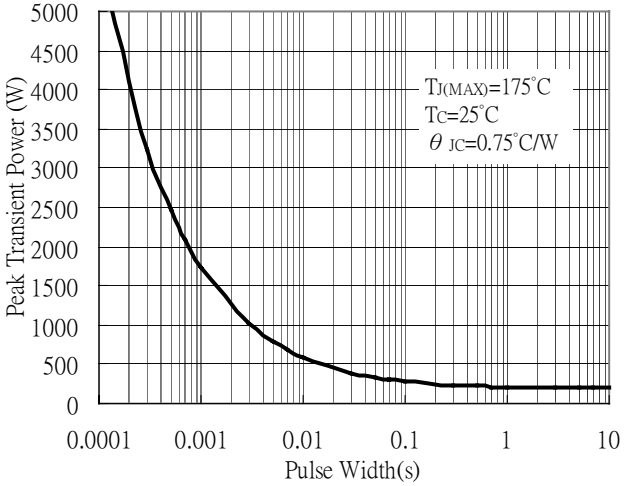


Typical Characteristics(Cont.)

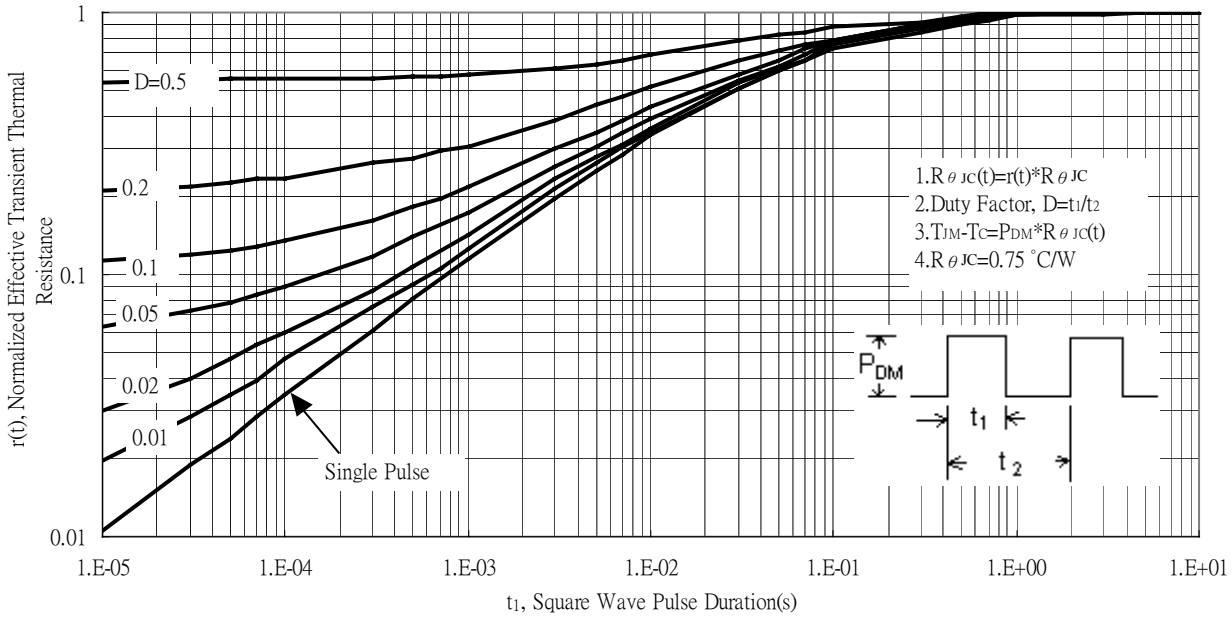
Typical Transfer Characteristics



Single Pulse Maximum Power Dissipation



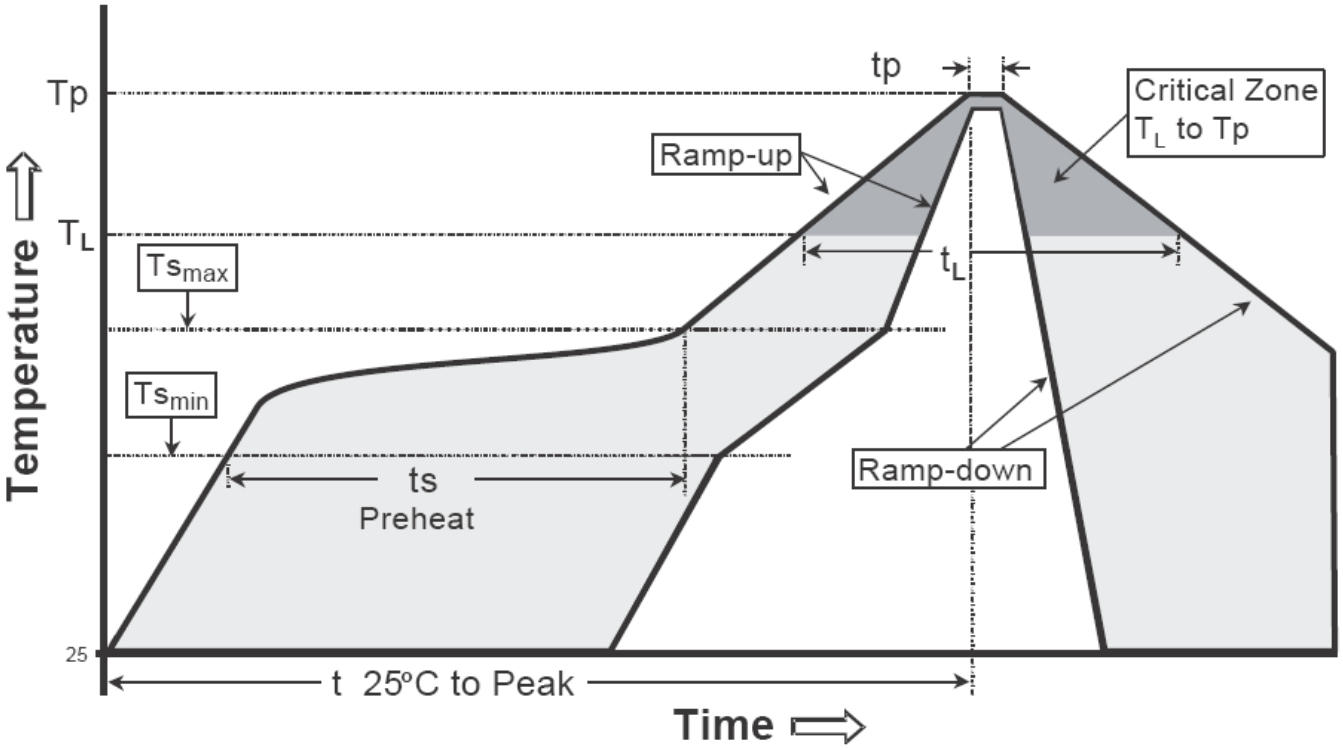
Transient Thermal Response Curves



Recommended wave soldering condition

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

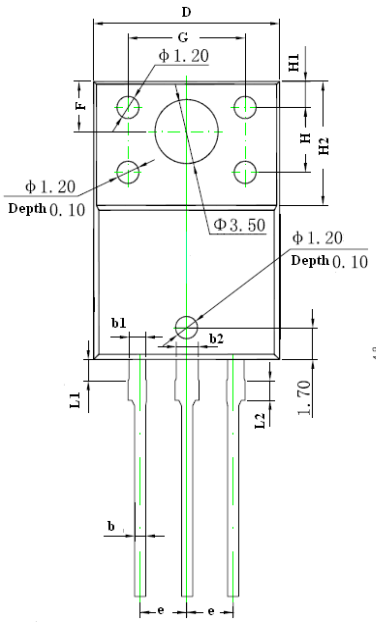
Recommended temperature profile for IR reflow

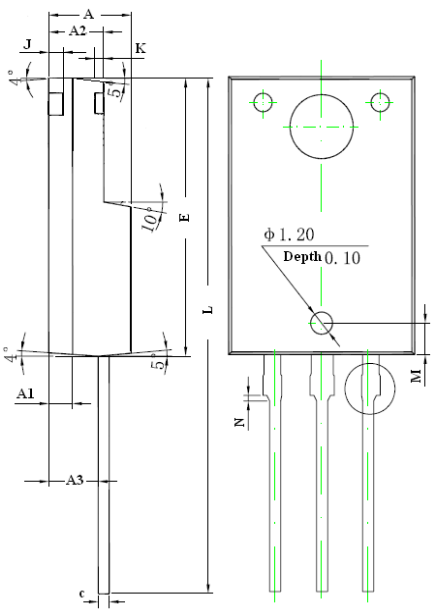


Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (Tsmax to Tp)	3°C/second max.	3°C/second max.
Preheat −Temperature Min(Ts min) −Temperature Max(Ts max) −Time(ts min to ts max)	 100°C 150°C 60-120 seconds	 150°C 200°C 60-180 seconds
Time maintained above: −Temperature (TL) − Time (tL)	 183°C 60-150 seconds	 217°C 60-150 seconds
Peak Temperature(TP)	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

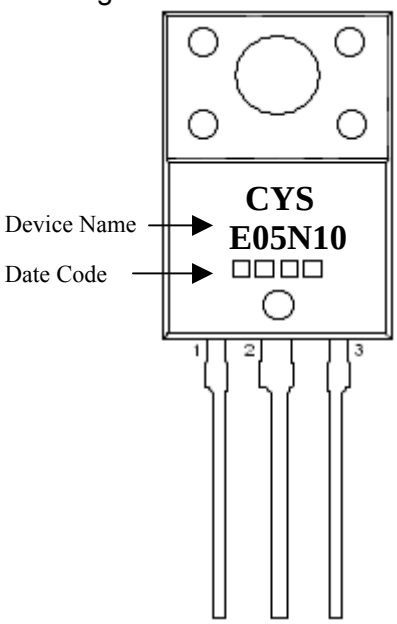
Note : All temperatures refer to topside of the package, measured on the package body surface.

TO-220FP Dimension





Marking:



3-Lead TO-220FP Plastic Package
CYStek Package Code: FP

Style: Pin 1.Gate 2.Drain 3.Source

*Typical

DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.171	0.183	4.35	4.65	G	0.246	0.258	6.25	6.55
A1	0.051 REF		1.300 REF		H	0.138 REF		3.50 REF	
A2	0.112	0.124	2.85	3.15	H1	0.055 REF		1.40 REF	
A3	0.102	0.110	2.60	2.80	H2	0.256	0.272	6.50	6.90
b	0.020	0.030	0.50	0.75	J	0.031 REF		0.80 REF	
b1	0.031	0.041	0.80	1.05	K	0.020		0.50 REF	
b2	0.047 REF		1.20 REF		L	1.102	1.118	28.00	28.40
c	0.020	0.030	0.500	0.750	L1	0.043	0.051	1.10	1.30
D	0.396	0.404	10.06	10.26	L2	0.036	0.043	0.92	1.08
E	0.583	0.598	14.80	15.20	M	0.067 REF		1.70 REF	
e	0.100 *		2.54*		N	0.012 REF		0.30 REF	
F	0.106 REF		2.70 REF						

Notes: 1.Controlling dimension: millimeters.
2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.
3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

Material:

- Lead: Pure tin plated.
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0.