

- Fast Switching Speed
- Super Low Gate Charge
- High-Side Switching
- Low Threshold
- ESD Protected up to 2KV

Product Summary

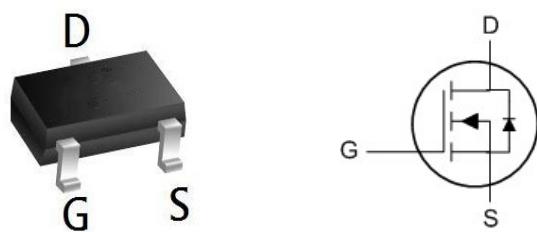
BVDSS	RDS(ON)	ID
20V	280 mΩ	0.9A

Description

The MTE3134K is the high cell density trenched N-ch MOSFETs, which provides excellent RDS(ON) and efficiency for most of the small power switching and load switch applications.

The MTE3134K meets the RoHS and Green Product requirement with full function reliability approved.

SOT523 Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	20	V
V _{GS}	Gate-Source Voltage	±8	V
I _D @T _A =25°C	Continuous Drain Current, V _{GS} @ 4.5V ₁	0.9	A
I _D @T _A =70°C	Continuous Drain Current, V _{GS} @ 4.5V ₁	0.6	A
I _{DM}	Pulsed Drain Current ²	4	A
P _D @T _A =25°C	Total Power Dissipation ³	0.25	W
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction-ambient ¹	---	500	°C/W
R _{θJC}	Thermal Resistance Junction-Case ¹	---	300	°C/W

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$\text{V}_{\text{GS}}=0\text{V}$, $\text{I}_D=250\mu\text{A}$	20	---	---	V
$\text{R}_{\text{DS(ON)}}$	Static Drain-Source On-Resistance ²	$\text{V}_{\text{GS}}=4.5\text{V}$, $\text{I}_D=800\text{mA}$	---	280	400	$\text{m}\Omega$
		$\text{V}_{\text{GS}}=2.5\text{V}$, $\text{I}_D=500\text{mA}$	---	350	500	
		$\text{V}_{\text{GS}}=1.8\text{V}$, $\text{I}_D=300\text{mA}$	---	650	800	
$\text{V}_{\text{GS(th)}}$	Gate Threshold Voltage	$\text{V}_{\text{GS}}=\text{V}_{\text{DS}}$, $\text{I}_D=250\mu\text{A}$	0.5	0.7	1	V
I_{DSS}	Drain-Source Leakage Current	$\text{V}_{\text{DS}}=20\text{V}$, $\text{V}_{\text{GS}}=0\text{V}$, $\text{T}_J=25^\circ\text{C}$	---	---	1	uA
		$\text{V}_{\text{DS}}=20\text{V}$, $\text{V}_{\text{GS}}=0\text{V}$, $\text{T}_J=55^\circ\text{C}$	---	---	5	
I_{GSS}	Gate-Source Leakage Current	$\text{V}_{\text{GS}}=\pm 10\text{V}$, $\text{V}_{\text{DS}}=0\text{V}$	---	---	± 10	uA
g_{fs}	Forward Transconductance	$\text{V}_{\text{DS}}=10\text{V}$, $\text{I}_D=800\text{mA}$	---	10.7	---	S
Q_g	Total Gate Charge (4.5V)	$\text{V}_{\text{DS}}=10\text{V}$, $\text{V}_{\text{GS}}=4.5\text{V}$, $\text{I}_D=550\text{mA}$	---	4.8	---	nC
Q_{gs}	Gate-Source Charge		---	2.5	---	
Q_{gd}	Gate-Drain Charge		---	0.8	---	
$\text{T}_{\text{d(on)}}$	Turn-On Delay Time	$\text{V}_{\text{DD}}=10\text{V}$, $\text{V}_{\text{GS}}=4.5\text{V}$, $\text{R}_G=10\Omega$ $\text{I}_D=500\text{mA}$	---	10	---	ns
T_r	Rise Time		---	3.9	---	
$\text{T}_{\text{d(off)}}$	Turn-Off Delay Time		---	26	---	
T_f	Fall Time		---	4.8	---	
C_{iss}	Input Capacitance	$\text{V}_{\text{DS}}=6\text{V}$, $\text{V}_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	480	---	pF
C_{oss}	Output Capacitance		---	107	---	
C_{rss}	Reverse Transfer Capacitance		---	72	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current ^{1,4}	$\text{V}_G=\text{V}_D=0\text{V}$, Force Current	---	---	0.9	A
V_{SD}	Diode Forward Voltage ²	$\text{V}_{\text{GS}}=0\text{V}$, $\text{I}_s=1\text{A}$, $\text{T}_J=25^\circ\text{C}$	---	---	1.2	V

Note :

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$
- 3.The power dissipation is limited by 150°C junction temperature
- 4.The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

Typical Characteristics

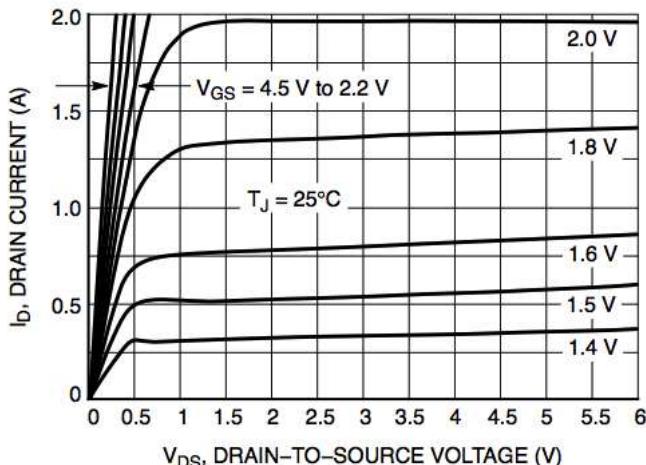


Figure 1. On-Region Characteristics

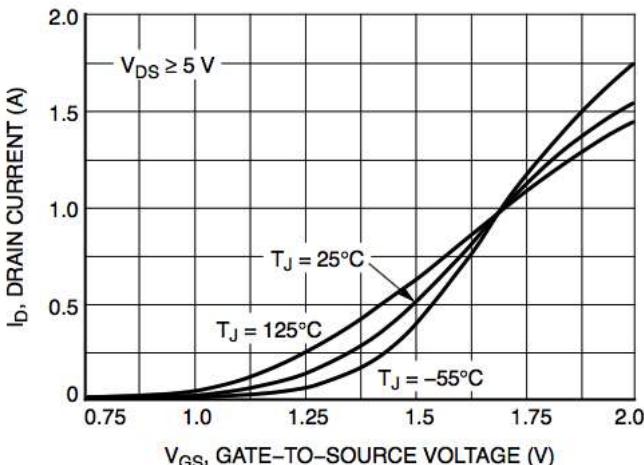


Figure 2. Transfer Characteristics

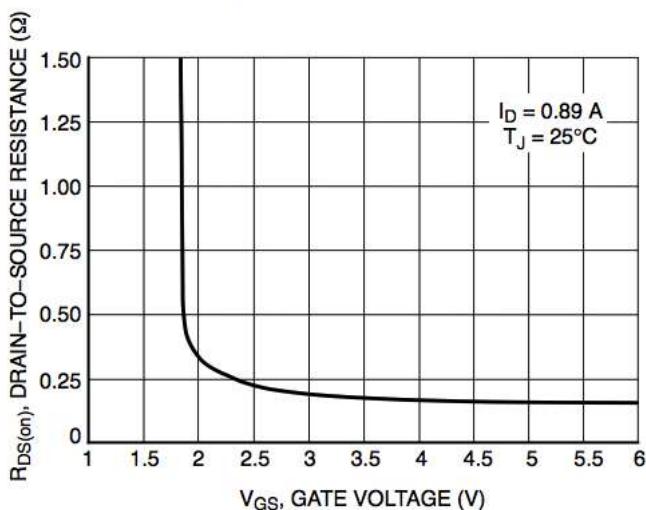


Figure 3. On-Resistance vs. Gate-to-Source Voltage

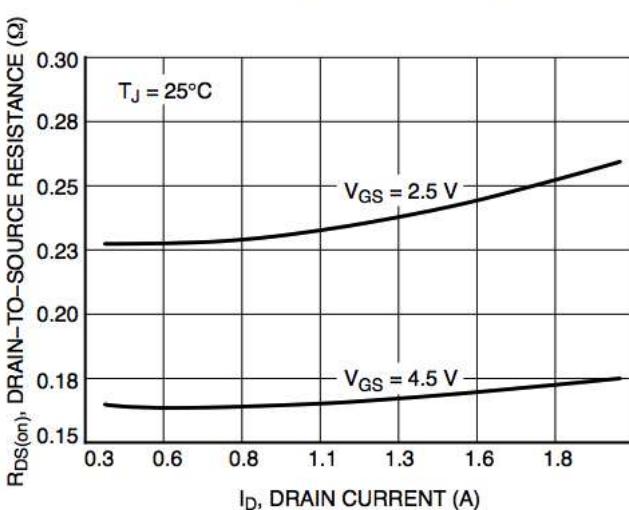


Figure 4. On-Resistance vs. Drain Current and Gate Voltage

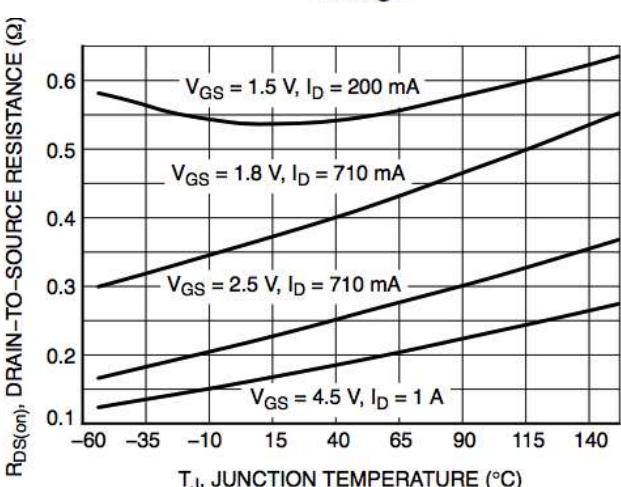


Figure 5. On-Resistance Variation with Temperature

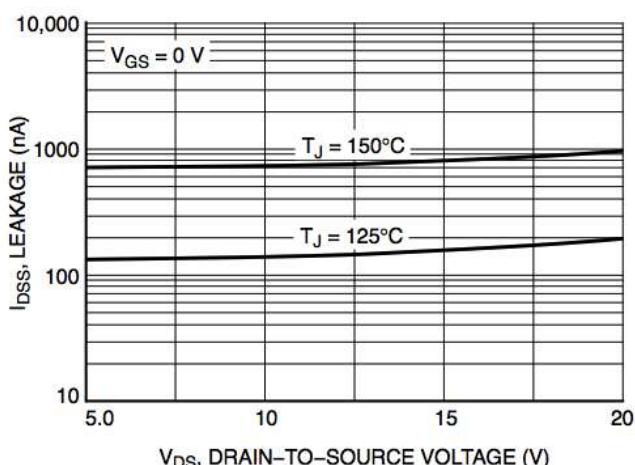


Figure 6. Drain-to-Source Leakage Current vs. Voltage

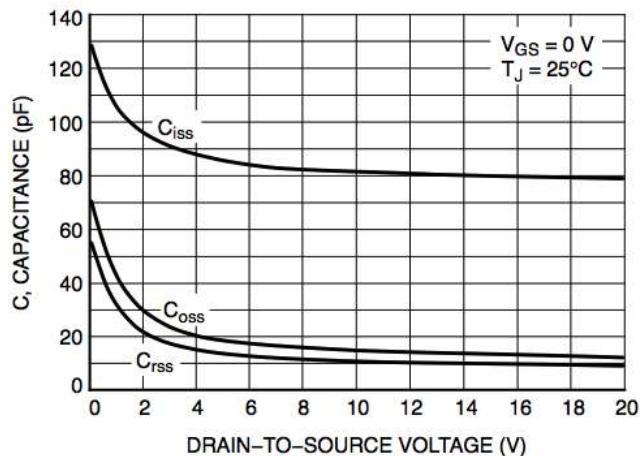
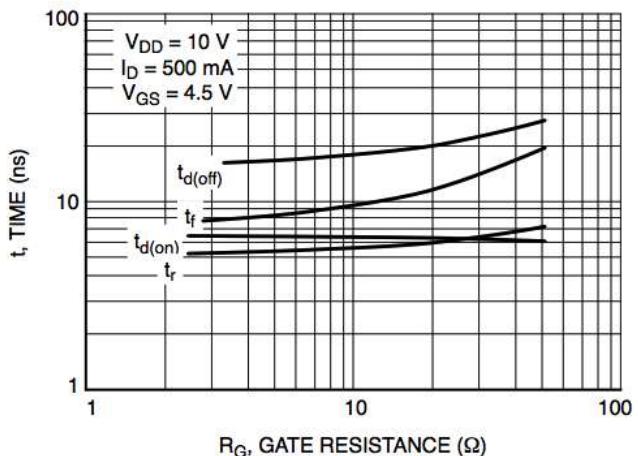


Figure 7. Capacitance Variation



**Figure 8. Resistive Switching Time Variation
vs. Gate Resistance**

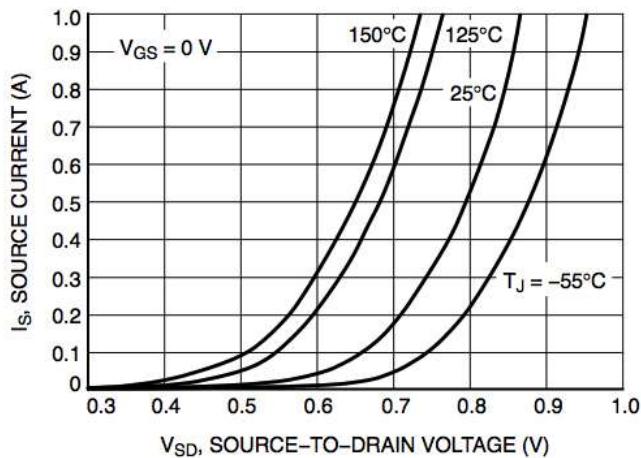
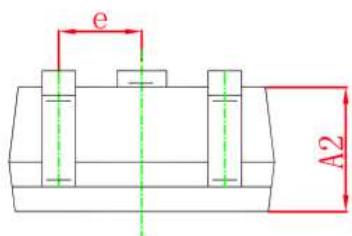
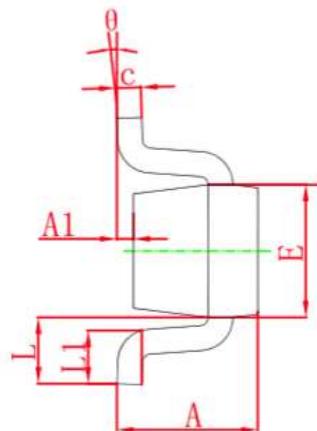
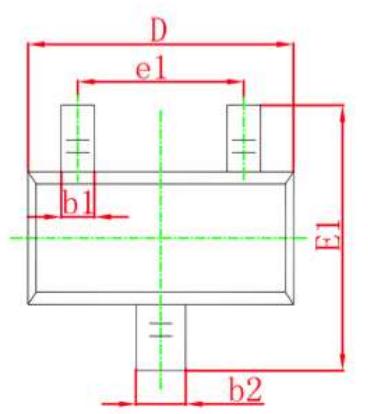


Figure 9. Diode Forward Voltage vs. Current

SOT523 Pin Configuration



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.700	0.900	0.028	0.035
A1	0.000	0.100	0.000	0.004
A2	0.700	0.800	0.028	0.031
b1	0.150	0.250	0.006	0.010
b2	0.250	0.350	0.010	0.014
c	0.100	0.200	0.004	0.008
D	1.500	1.700	0.059	0.067
E	0.700	0.900	0.028	0.035
E1	1.450	1.750	0.057	0.069
e	0.500 TYP.		0.020 TYP.	
e1	0.900	1.100	0.035	0.043
L	0.400 REF.		0.016 REF.	
L1	0.260	0.460	0.010	0.018
θ	0°	8°	0°	8°