

N & P-Channel Enhancement Mode Power MOSFET

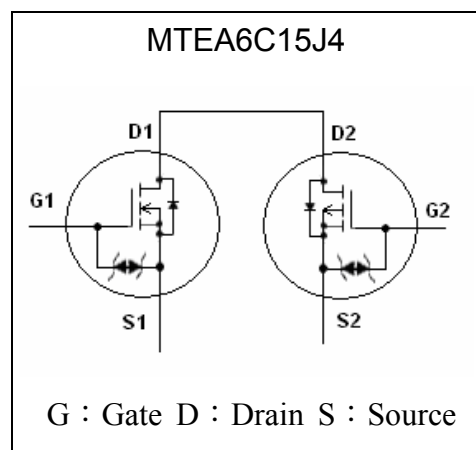
MTEA6C15J4

Features

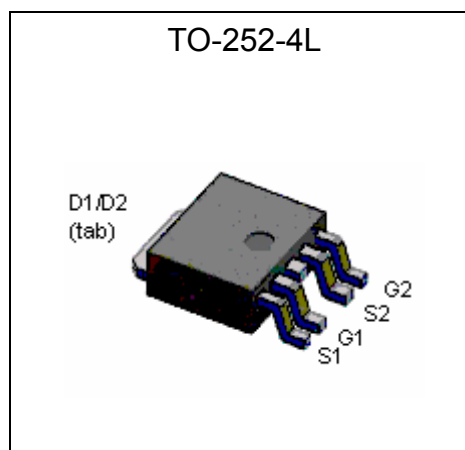
- Low gate charge
- Simple drive requirement
- ESD protected
- Pb-free lead plating and halogen-free package

	N-CH	P-CH
BV_{DSS}	150V	-150V
I_D @ $V_{GS}=10V(-10V)$	9.3A	-7.1A
$R_{DS(on)(TYP)}@V_{GS}=10V(-10V)$	163m Ω	283m Ω
$R_{DS(on)(TYP)}@V_{GS}=6V(-6V)$	177m Ω	308 Ω

Equivalent Circuit



Outline



Absolute Maximum Ratings (T_A=25°C, unless otherwise noted)

Parameter	Symbol	Limits		Unit
		N-channel	P-channel	
Drain-Source Voltage	V _{DS}	150	-150	V
Gate-Source Voltage	V _{GS}	±20	±20	
Continuous Drain Current @ T _C =25°C, V _{GS} =10V(-10V) (Note1)	I _D	9.3	-7.1	A
Continuous Drain Current @ T _C =100°C, V _{GS} =10V(-10V) (Note1)		6.6	-5.0	
Continuous Drain Current @ T _A =25°C, V _{GS} =10V(-10V) (Note2)	I _{DSM}	2	-1.5	
Continuous Drain Current @ T _A =70°C, V _{GS} =10V(-10V) (Note2)		1.7	-1.3	
Pulsed Drain Current *1 (Note3)	I _{DM}	20	-20	
Total Power Dissipation (T _C =25°C) (Note1)	P _D	37.5		W
Total Power Dissipation (T _C =100°C) (Note1)		18.7		
Total Power Dissipation (T _A =25°C) (Note2)	P _{DSM}	2.4		
Total Power Dissipation (T _A =70°C) (Note2)		1.7		
Operating Junction and Storage Temperature Range	Tj, Tstg	-55~+175		°C

Note : *1. Pulse width limited by maximum junction temperature

*2. Duty cycle ≤ 1%

**Thermal Data**

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-case, max	$R_{th,j-c}$	4	°C/W
Thermal Resistance, Junction-to-ambient, max (Note2)	$R_{th,j-a}$	62.5	
Thermal Resistance, Junction-to-ambient, max (Note4)		90	

- Note : 1. The power dissipation P_D is based on $T_{J(MAX)}=175^{\circ}\text{C}$, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.
2. The value of $R_{\theta JA}$ is measured with the device mounted on 1 in² FR-4 board with 2 oz. copper, in a still air environment with $T_A=25^{\circ}\text{C}$. The power dissipation P_{DSM} is based on $R_{\theta JA}$ and the maximum allowed junction temperature of 150°C . The value in any given application depends on the user's specific board design.
3. Repetitive rating, pulse width limited by junction temperature $T_{J(MAX)}=175^{\circ}\text{C}$. Ratings are based on low frequency and low duty cycles to keep initial $T_J=25^{\circ}\text{C}$.
4. When mounted on the minimum pad size recommended (PCB mount), $t_s \leq 10s$.

N-CH Characteristics ($T_c=25^{\circ}\text{C}$, unless otherwise specified)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static					
BV _{DSS}	150	-	-	V	V _{GS} =0V, I _D =250μA
V _{GS(th)}	2.0	3.2	4.0		V _{DS} =V _{GS} , I _D =250μA
G _{FS} *1	-	5	-	S	V _{DS} =10V, I _D =2.5A
I _{GSS}	-	-	±10	μA	V _{GS} =±20V, V _{DS} =0V
I _{DSS}	-	-	1		V _{DS} =120V, V _{GS} =0V
	-	-	25		V _{DS} =120V, V _{GS} =0V, T _j =125°C
R _{DS(ON)} *1	-	163	225	mΩ	V _{GS} =10V, I _D =2.5A
	-	177	235		V _{GS} =6V, I _D =2A
Dynamic					
Q _g *1	-	8.5	-	nC	I _D =2.5A, V _{DS} =120V, V _{GS} =10V
Q _{gs} *1	-	1.8	-		
Q _{gd} *1	-	2.9	-		
t _{d(ON)} *1	-	7	-	ns	V _{DS} =75V, I _D =1A, V _{GS} =10V, R _G =6Ω
t _r *1	-	5	-		
t _{d(OFF)} *1	-	18	-		
t _f *1	-	7	-		
C _{iss}	-	346	-	pF	V _{GS} =0V, V _{DS} =25V, f=1MHz
C _{oss}	-	44	-		
C _{rss}	-	14	-		
Source-Drain Diode					
I _S *1	-	-	2.3	A	
I _{SM} *2	-	-	9.2		
V _{SD} *1	-	0.79	1.3	V	I _S =3A, V _{GS} =0V
t _{rr} *1	-	90	-	ns	I _F =3A, V _{GS} =0V, dI _F /dt=100A/μs
Q _{rr} *1	-	150	-	nC	

Note : *1. Pulse Test : Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$

*2. Pulse width limited by maximum junction temperature.

**P-CH Characteristics (Tc=25°C, unless otherwise specified)**

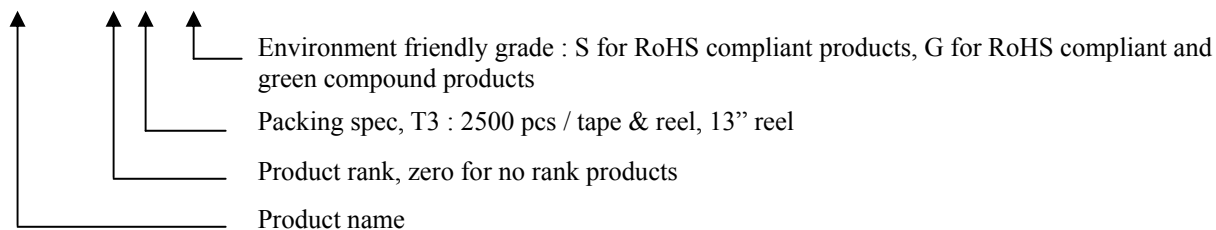
Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static					
BV _{DSS}	-150	-	-	V	V _{GS} =0V, I _D =-250μA
V _{GS(th)}	-2.0	-3.3	-4.0		V _{DS} =V _{GS} , I _D =-250μA
G _{FS} *1	-	4	-	S	V _{DS} =-10V, I _D =-1.5A
I _{GSS}	-	-	±10	μA	V _{GS} =±20V, V _{DS} =0V
I _{DSS}	-	-	-1		V _{DS} =-120V, V _{GS} =0V
	-	-	-25		V _{DS} =-120V, V _{GS} =0V, T _j =125°C
R _{DS(ON)} *1	-	283	340	mΩ	V _{GS} =-10V, I _D =-1.5A
	-	308	370		V _{GS} =-6V, I _D =-1A
Dynamic					
Q _g *1	-	16	-	nC	I _D =-1.5A, V _{DS} =-120V, V _{GS} =-10V
Q _{gs} *1	-	2	-		
Q _{gd} *1	-	5.4	-		
t _{d(ON)} *1	-	8	-	ns	V _{DS} =-75V, I _D =-1A, V _{GS} =-10V, R _G =6Ω
t _r *1	-	9	-		
t _{d(OFF)} *1	-	21	-		
t _f *1	-	13	-		
C _{iss}	-	430	-	pF	V _{GS} =0V, V _{DS} =-25V, f=1MHz
C _{oss}	-	62	-		
C _{rss}	-	28	-		
Source-Drain Diode					
I _S *1	-	-	-2.3	A	
I _{SM} *2	-	-	-9.2		
V _{SD} *1	-	-0.79	-1.3	V	I _S =-2.5A, V _{GS} =0V
t _{rr} *1	-	130	-	ns	I _F =-2.5A, V _{GS} =0V, dI _F /dt=100A/μs
Q _{rr} *1	-	300	-	nC	

Note : *1.Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

*2.Pulse width limited by maximum junction temperature.

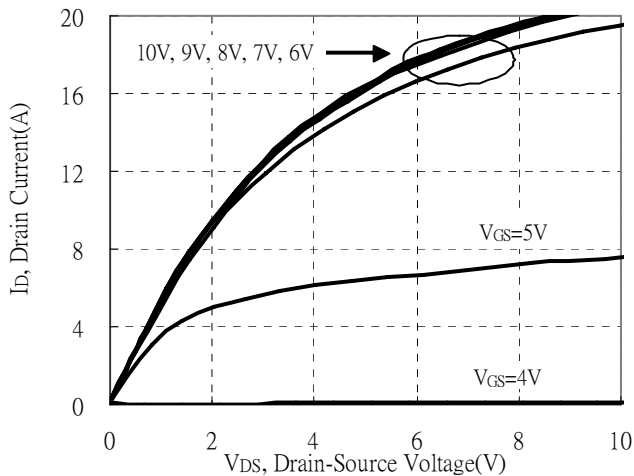
Ordering Information

Device	Package	Shipping
MTEA6C15J4-0-T3-G	TO-252 (Pb-free lead plating and halogen-free package)	2500 pcs / Tape & Reel

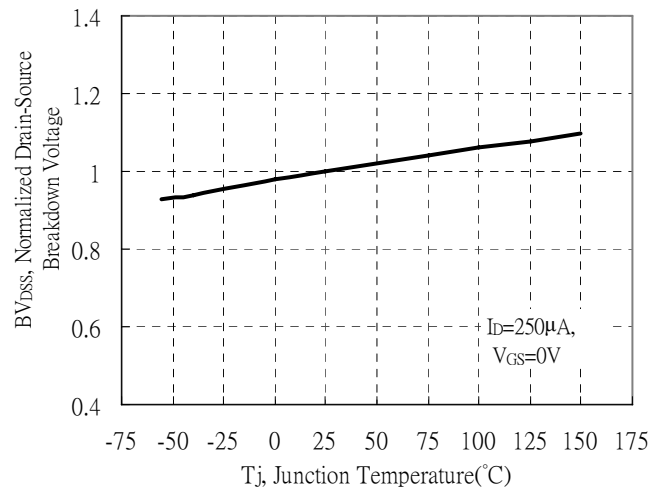


Q1, N-CH Typical Characteristics

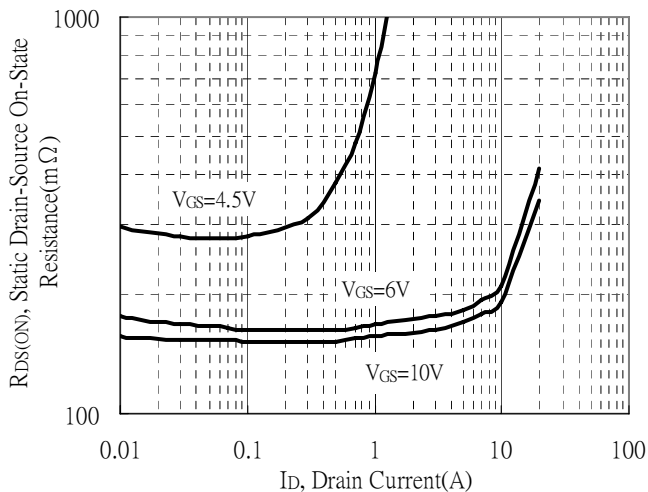
Typical Output Characteristics



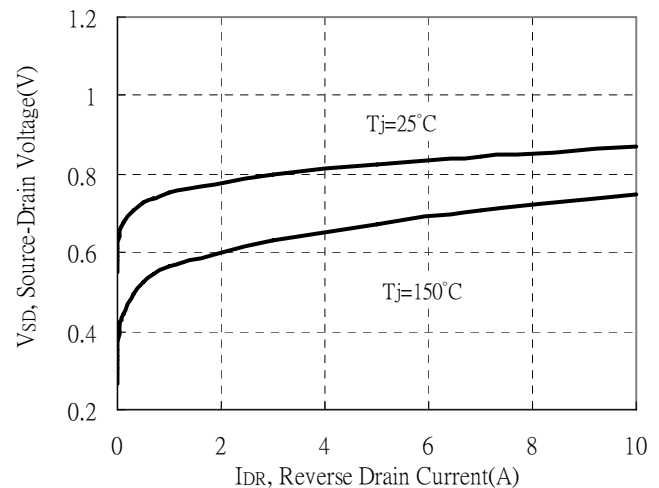
Brekdown Voltage vs Ambient Temperature



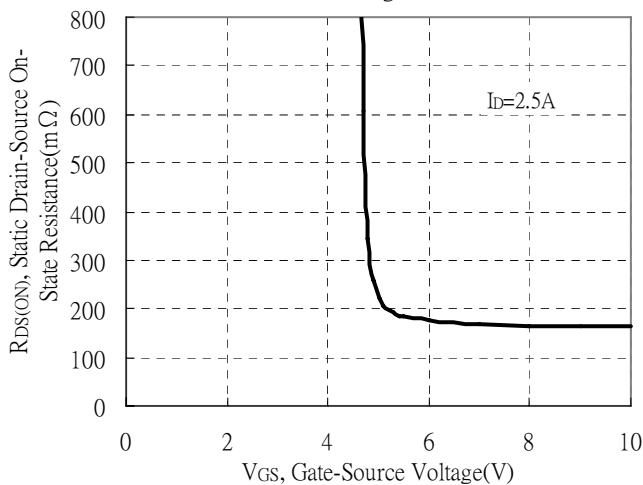
Static Drain-Source On-State resistance vs Drain Current



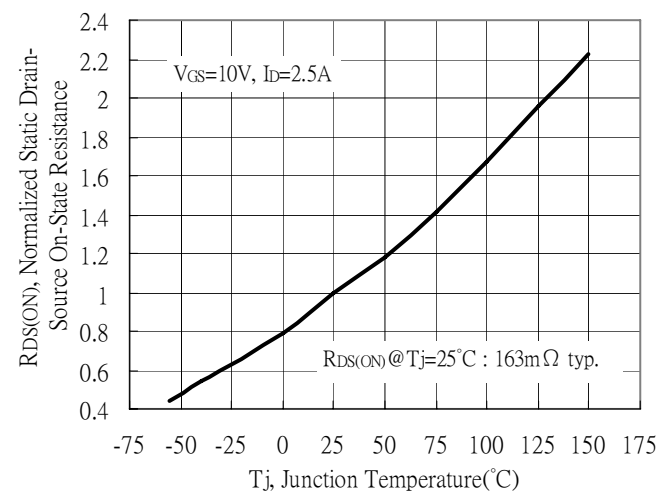
Reverse Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

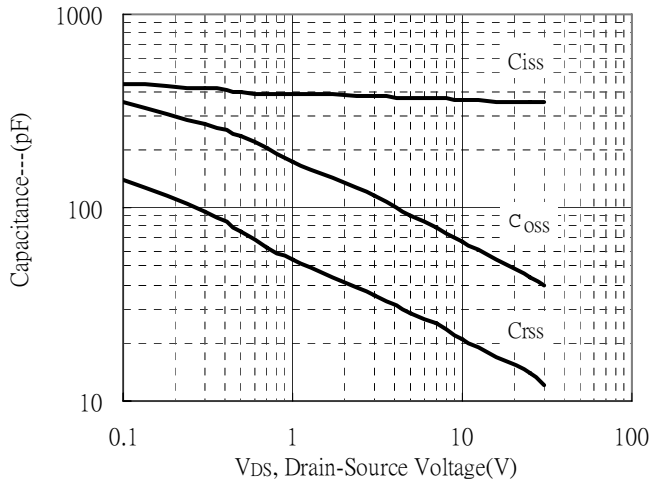


Drain-Source On-State Resistance vs Junction Temperature

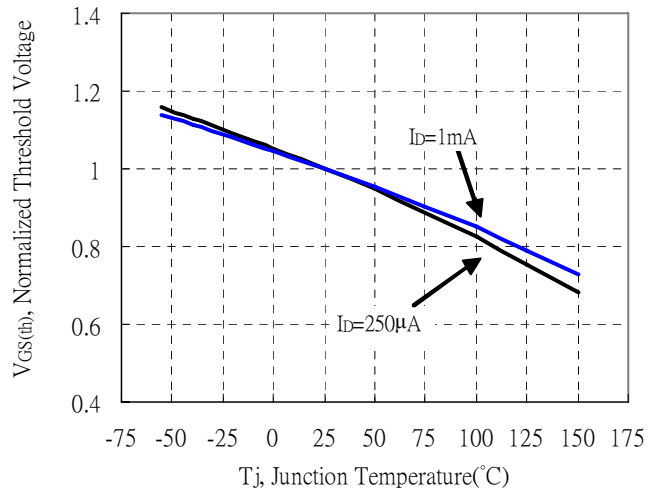


Q1, N-CH Typical Characteristics(Cont.)

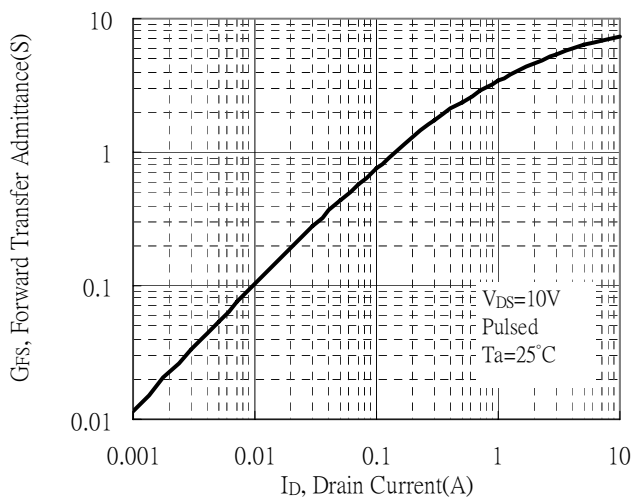
Capacitance vs Drain-to-Source Voltage



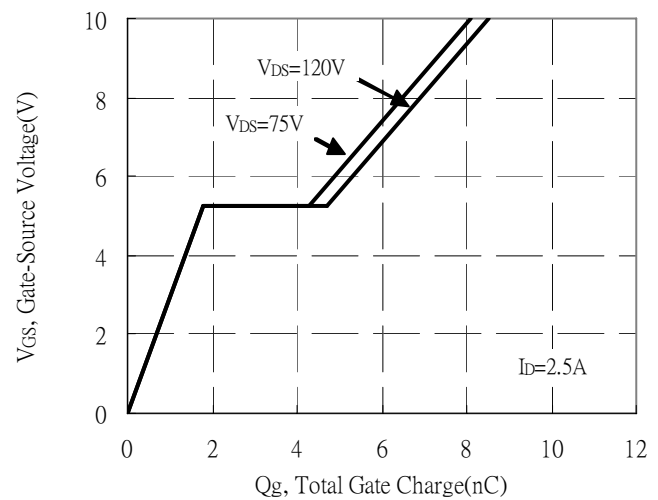
Threshold Voltage vs Junction Temperature



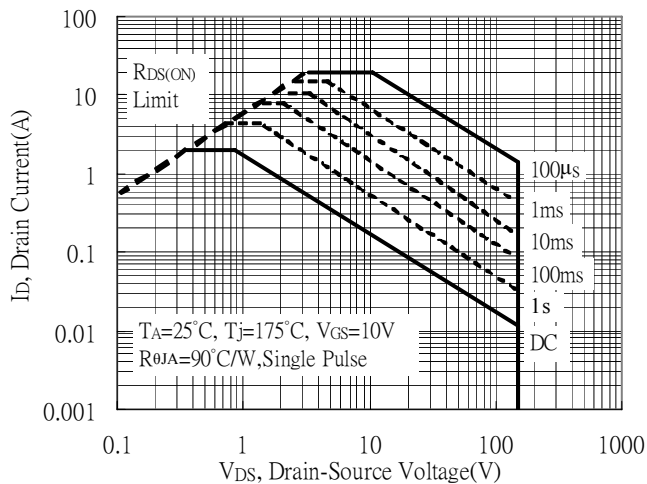
Forward Transfer Admittance vs Drain Current



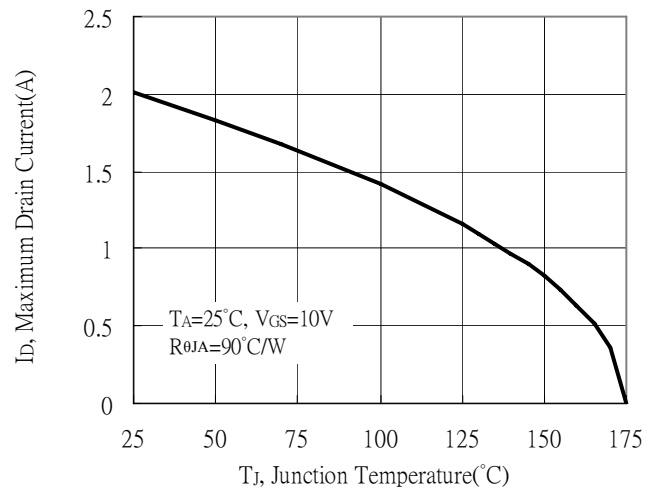
Gate Charge Characteristics



Maximum Safe Operating Area

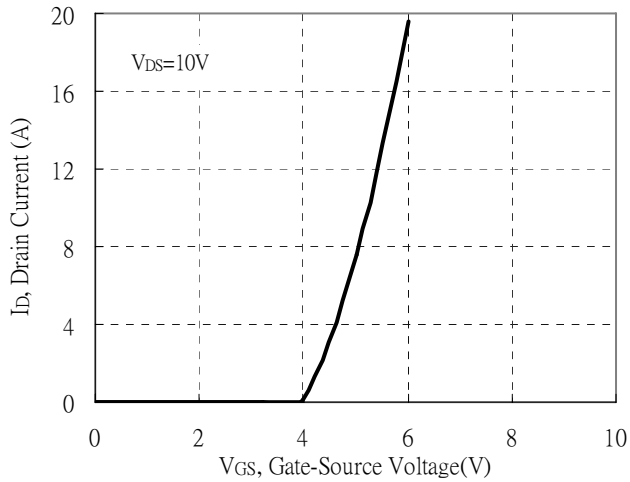


Maximum Drain Current vs Junction Temperature

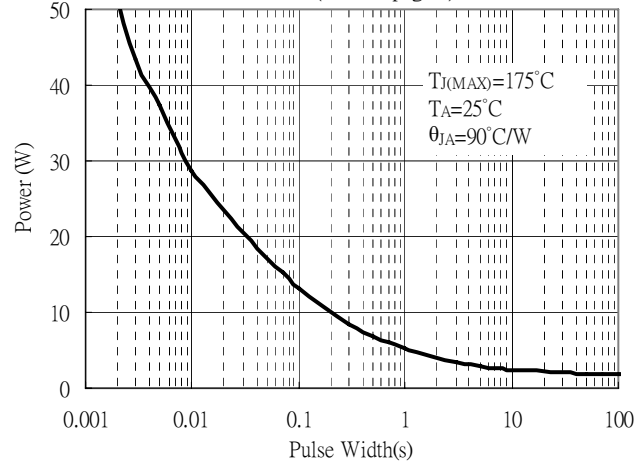


Q1, N-CH Typical Characteristics(Cont.)

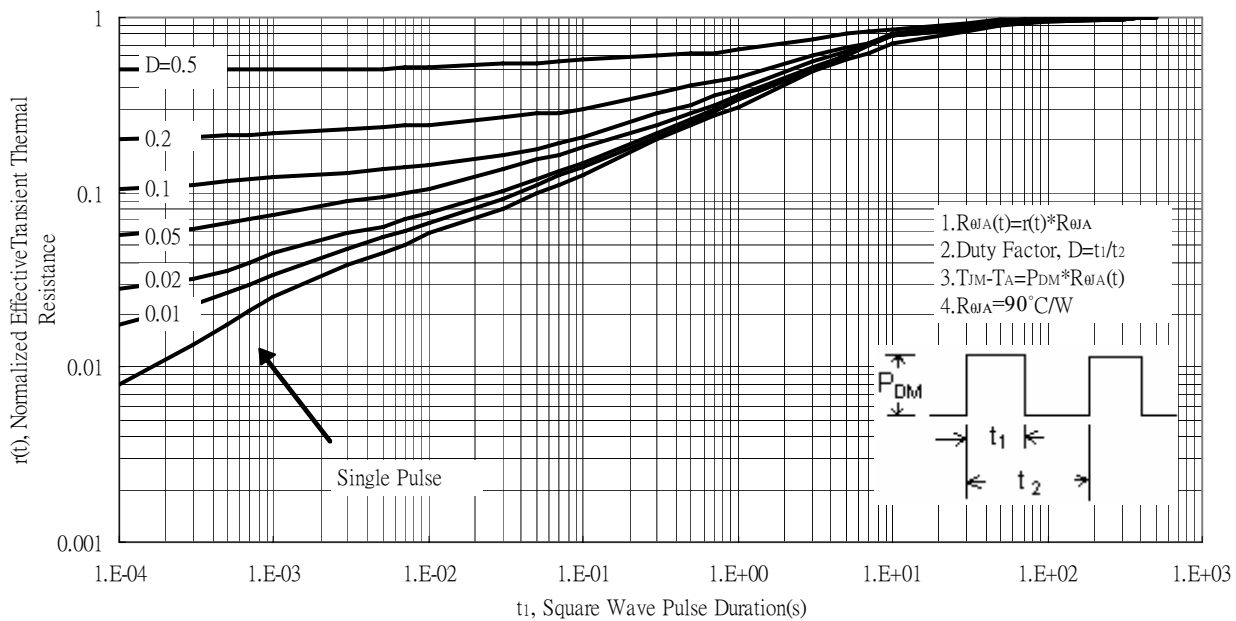
Typical Transfer Characteristics



Single Pulse Power Rating, Junction to Ambient
(Note on page 2)

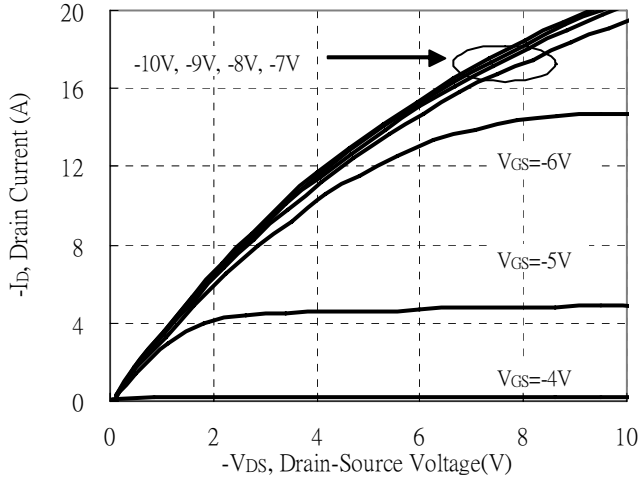


Transient Thermal Response Curves

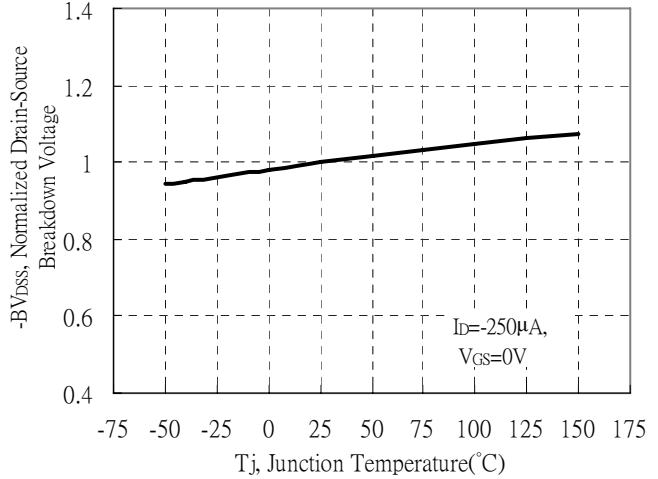


Q2, P-CH Typical Characteristics

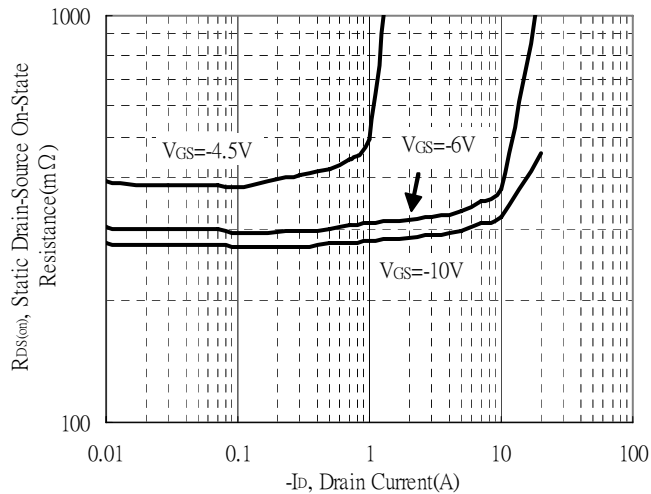
Typical Output Characteristics



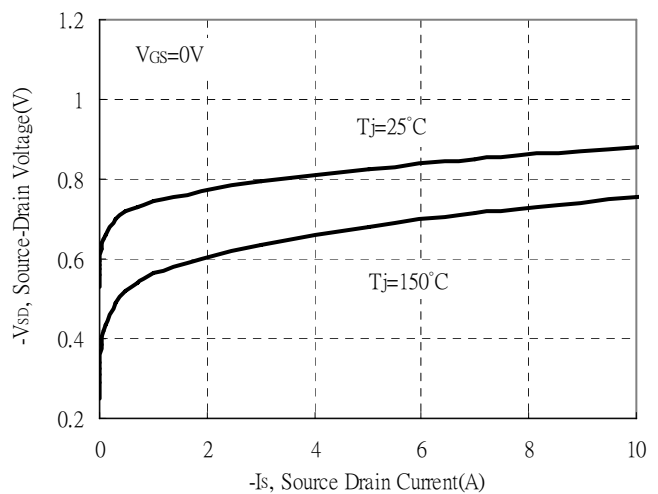
Breakdown Voltage vs Ambient Temperature



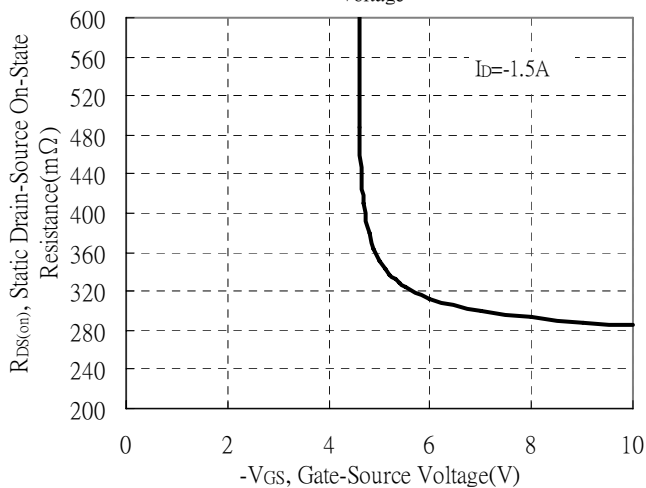
Static Drain-Source On-State resistance vs Drain Current



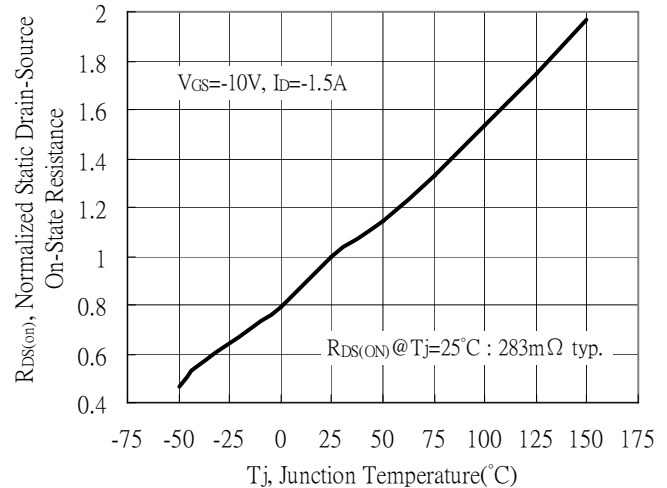
Source Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

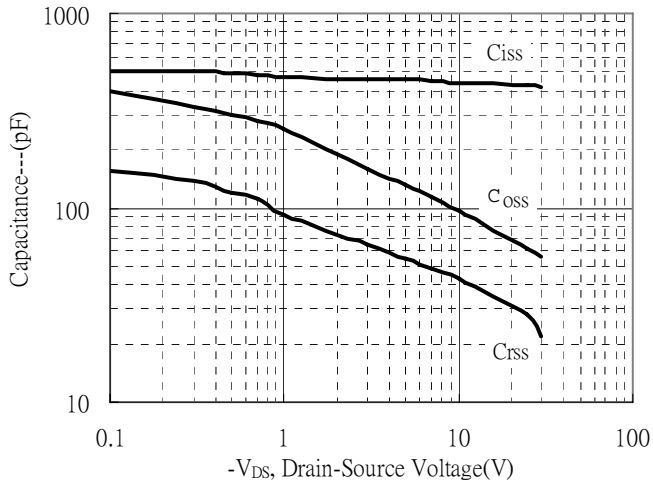


Drain-Source On-State Resistance vs Junction Temperature

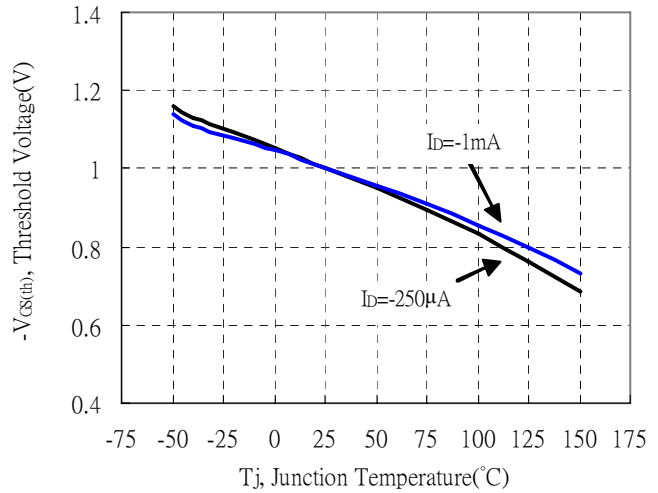


Q2, P-CH Typical Characteristics(Cont.)

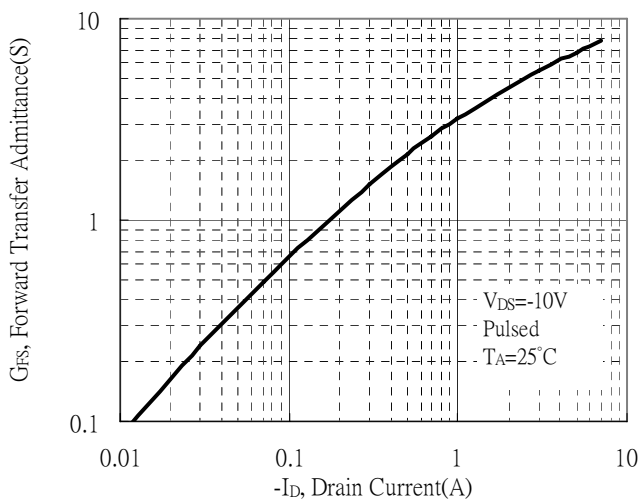
Capacitance vs Drain-to-Source Voltage



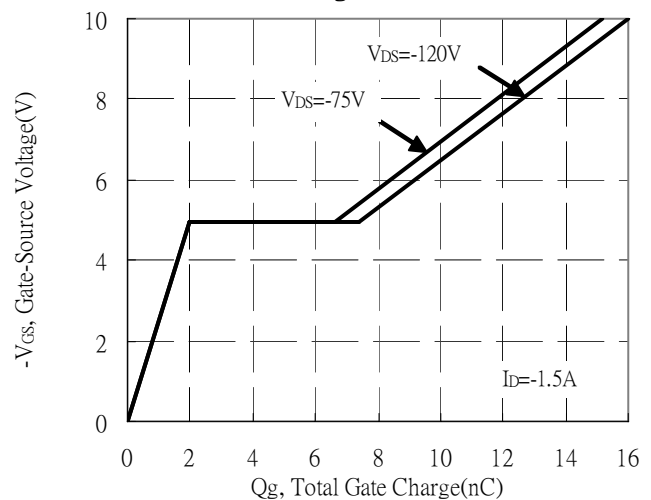
Threshold Voltage vs Junction Temperature



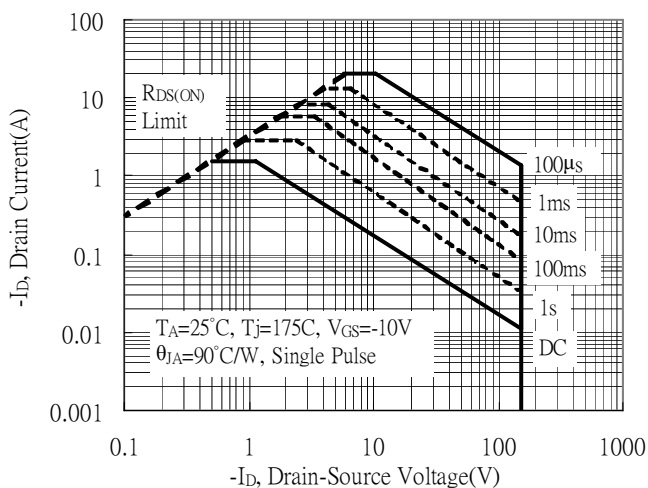
Forward Transfer Admittance vs Drain Current



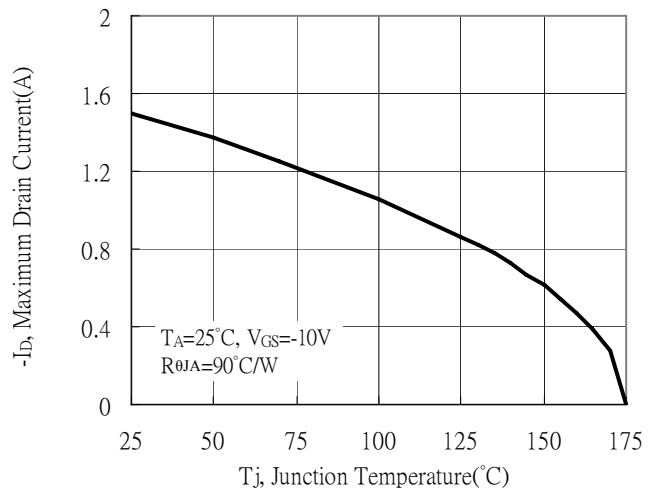
Gate Charge Characteristics



Maximum Safe Operating Area

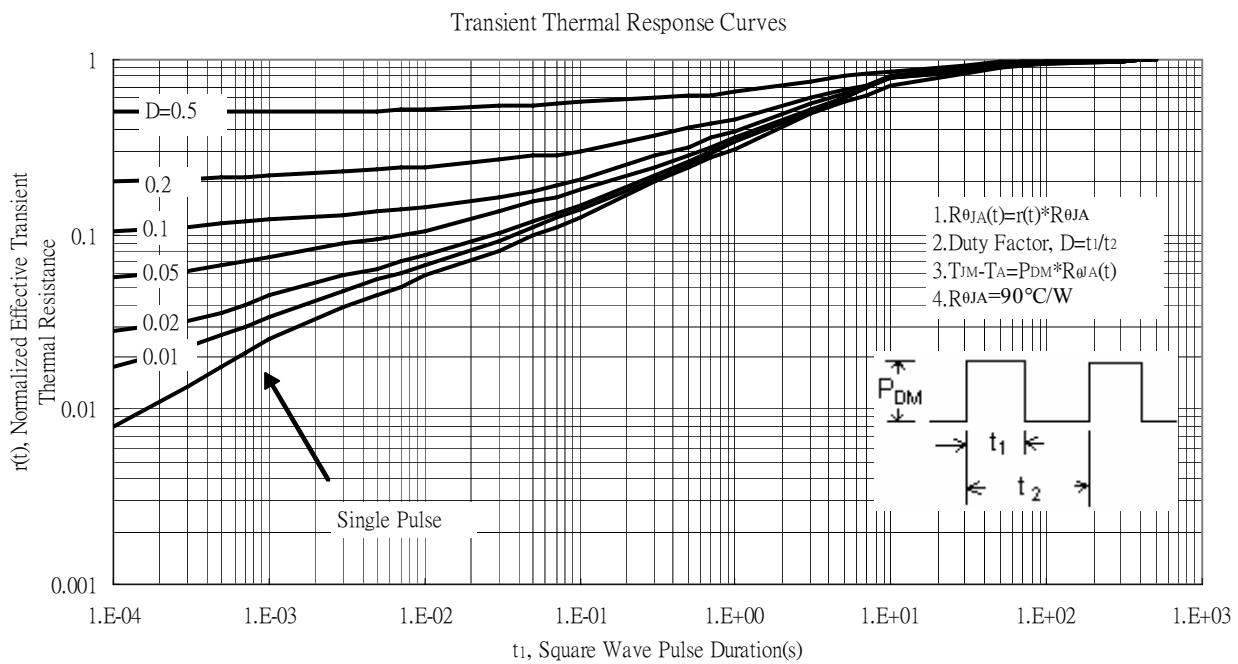
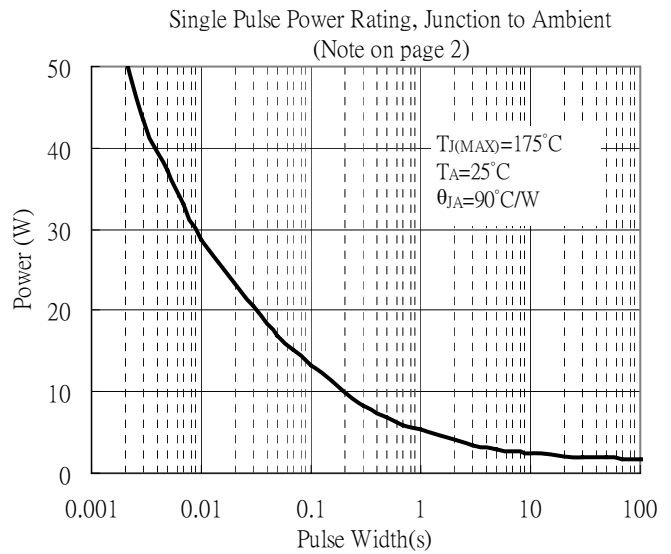
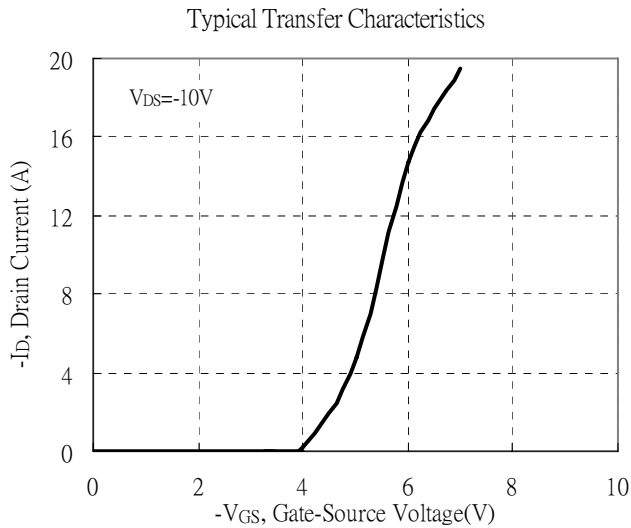


Maximum Drain Current vs Junction Temperature

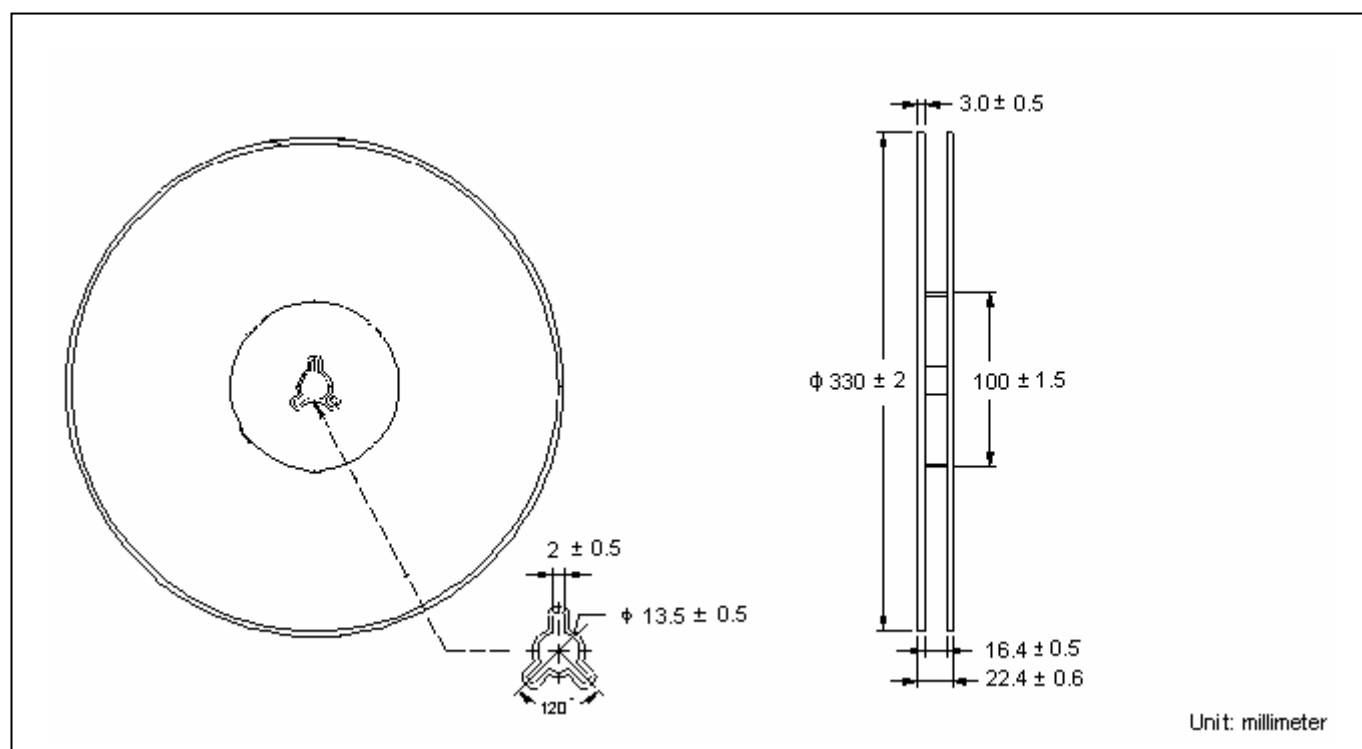




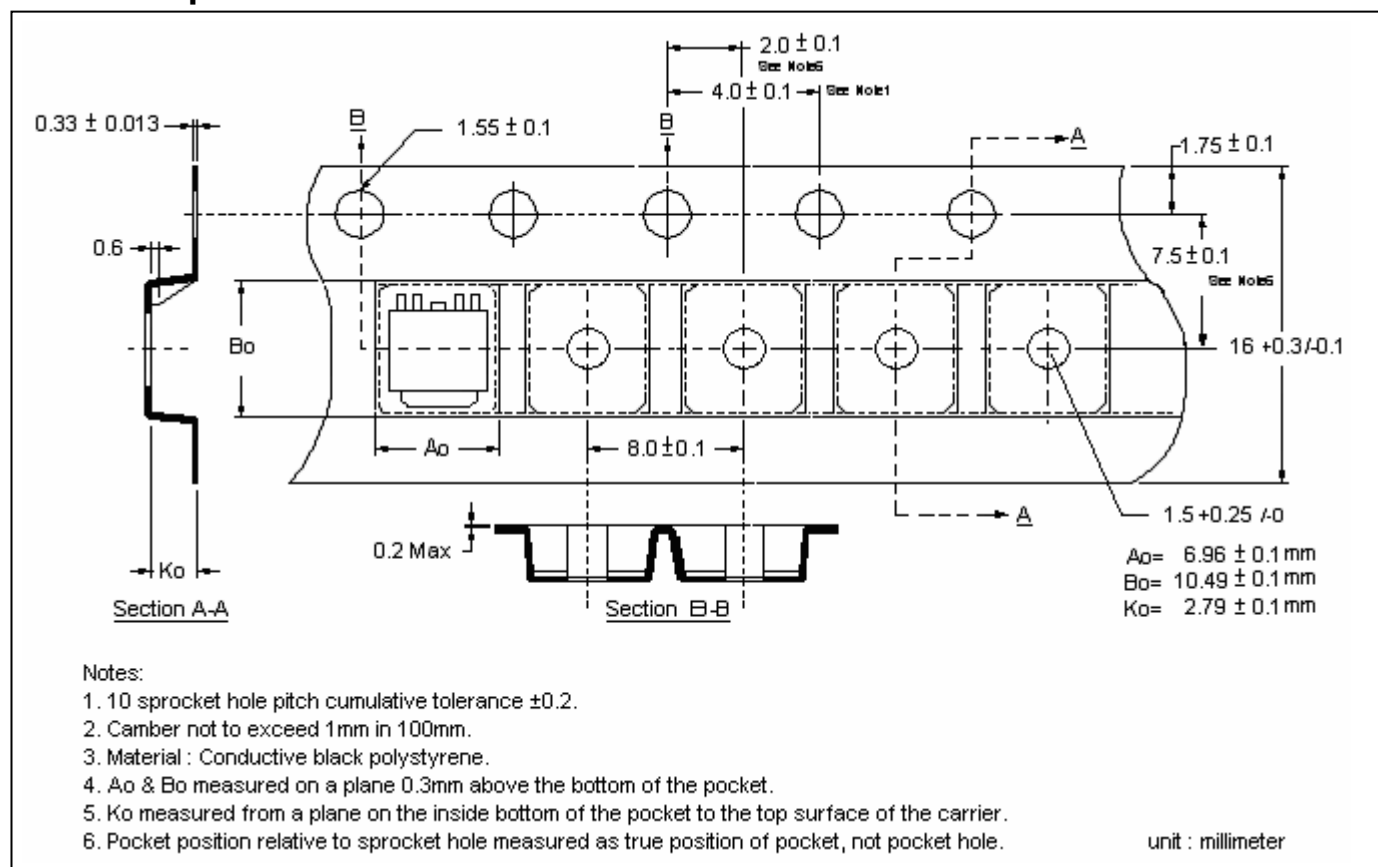
Q2, P-CH Typical Characteristics(Cont.)



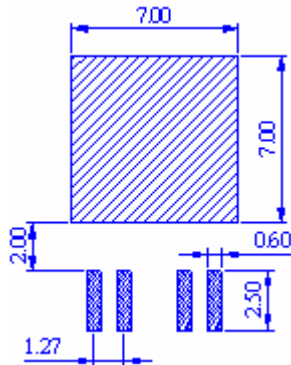
Reel Dimension



Carrier Tape Dimension



Recommended soldering footprint

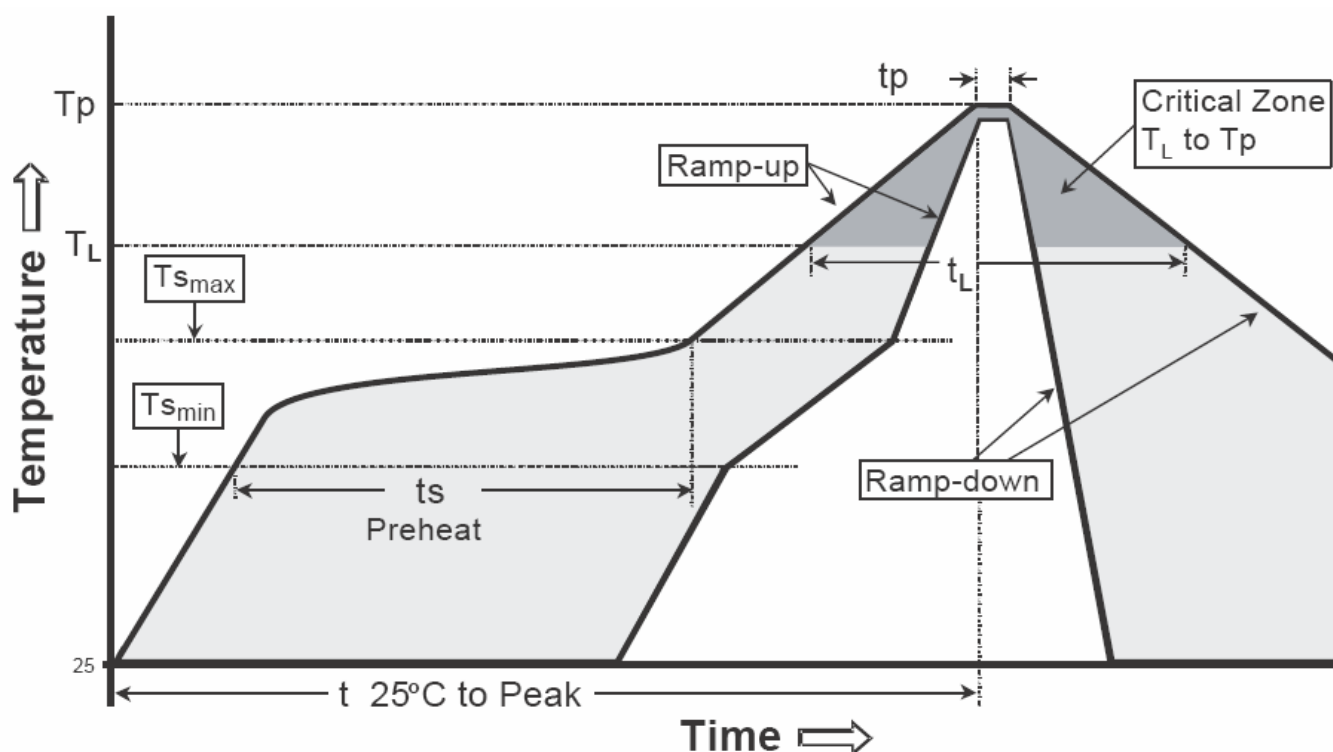


Unit : mm

Recommended wave soldering condition

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

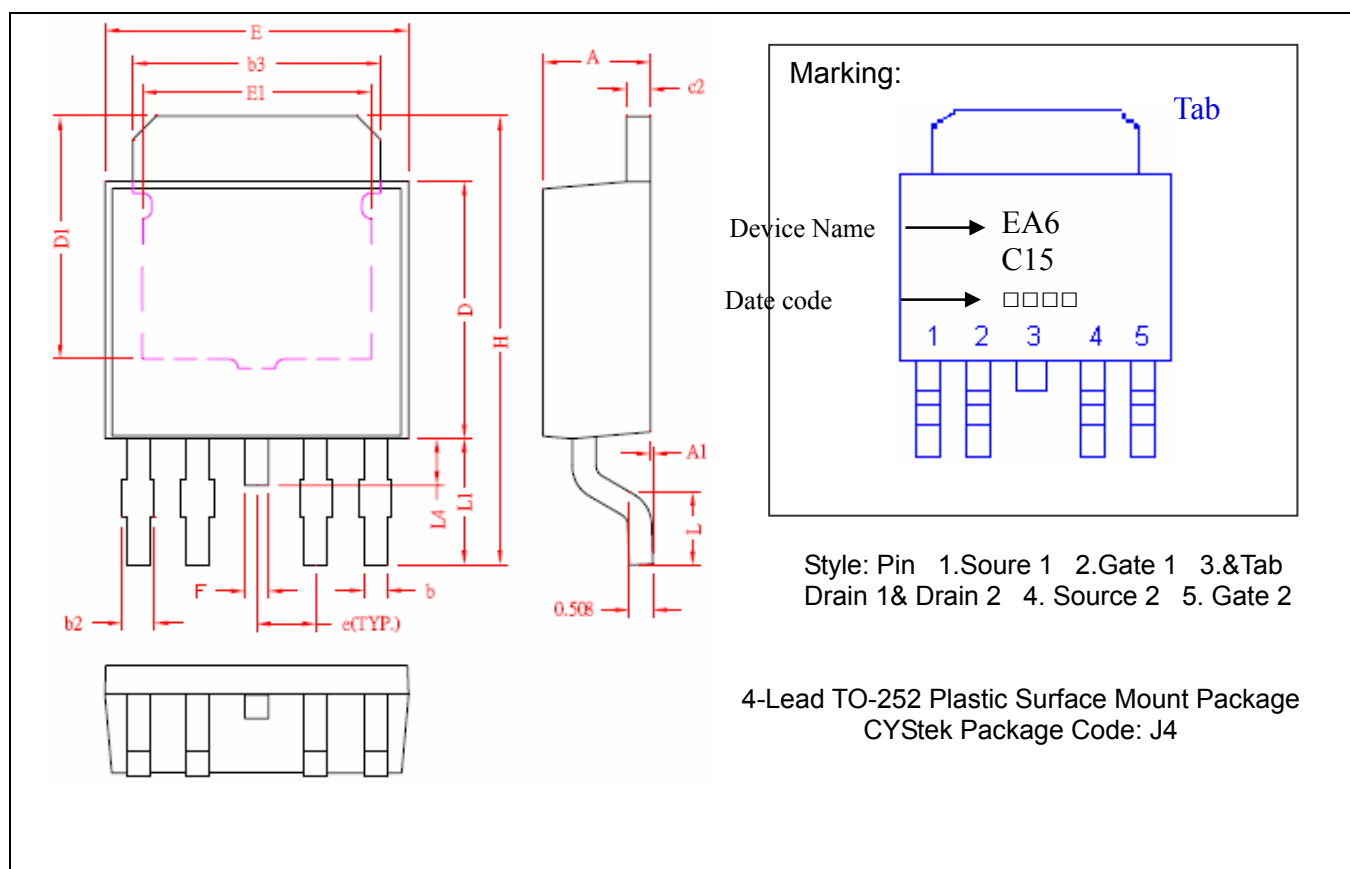
Recommended temperature profile for IR reflow



Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (Tsmax to Tp)	3°C/second max.	3°C/second max.
Preheat		
-Temperature Min(Ts min)	100°C	150°C
-Temperature Max(Ts max)	150°C	200°C
-Time(ts min to ts max)	60-120 seconds	60-180 seconds
Time maintained above:		
-Temperature (TL)	183°C	217°C
- Time (tL)	60-150 seconds	60-150 seconds
Peak Temperature(TP)	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.

TO-252 Dimension



DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.0866	0.0945	2.20	2.40	E	0.2520	0.2677	6.40	6.80
A1	0.0000	0.0059	0.00	0.15	E1	0.1500	-	3.81	-
b	0.0157	0.0236	0.40	0.60	e	0.0500	REF	1.27	REF
b2	0.0199	0.0315	0.50	0.80	F	0.0157	0.0236	0.40	0.60
b3	0.2047	0.2165	5.20	5.50	H	0.3701	0.4016	9.40	10.20
c2	0.0177	0.0217	0.45	0.55	L	0.0551	0.0697	1.40	1.77
D	0.2126	0.2283	5.40	5.80	L1	0.0945	0.1181	2.40	3.00
D1	0.1799	-	4.57	-	L4	0.0315	0.0472	0.80	1.20

Notes: 1. Controlling dimension: millimeters.

2. Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.

3. If there is any question with packing specification or packing method, please contact your local CYStek sales office.

Material:

- Lead : Pure tin plated.
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0.

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