



N-Channel Enhancement Mode Power MOSFET

# MTN10N65BE3

BV <sub>DSS</sub>	650V
I <sub>D</sub> @ V <sub>GS</sub> =10V, T <sub>C</sub> =25°C	10A
R <sub>DS(on)(TYP)</sub> @ V <sub>GS</sub> =10V, I <sub>D</sub> =6A	0.54Ω

## Description

The MTN10N65BE3 is a N-channel enhancement-mode MOSFET, providing the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost effectiveness. The TO-220 package is universally preferred for all commercial-industrial applications

## Features

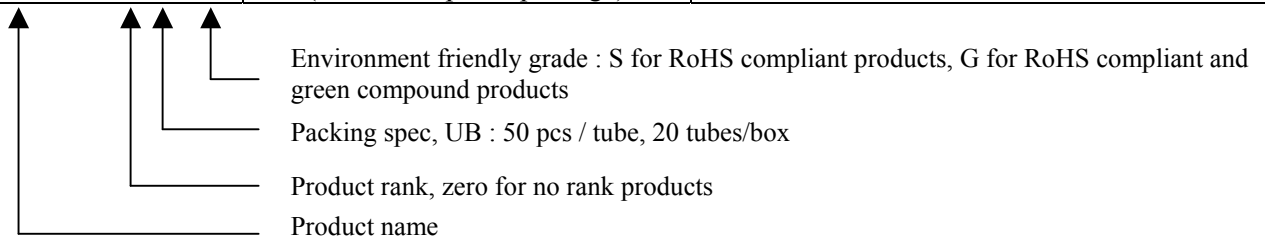
- Low On Resistance
- Simple Drive Requirement
- Low Gate Charge
- Fast Switching Characteristic
- RoHS compliant package

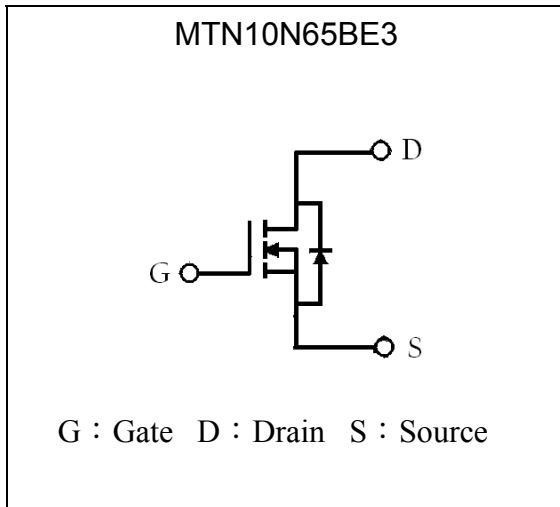
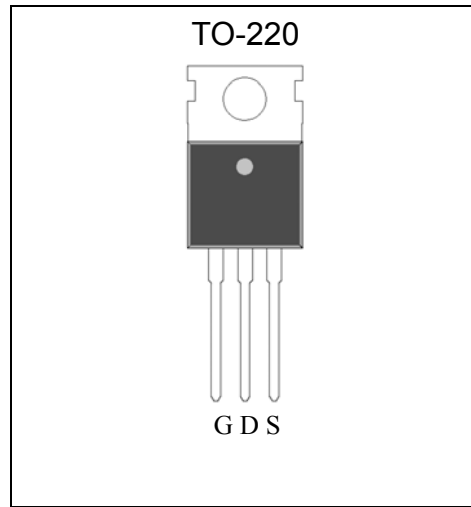
## Applications

- Open Framed Power Supply
- Adapter
- STB

## Ordering Information

Device	Package	Shipping
MTN10N65BE3-0-UB-X	TO-220 (RoHS compliant package)	50 pcs/tube, 20 tubes/box, 4 boxes / carton



**Symbol**

**Outline**

**Absolute Maximum Ratings** ( $T_C=25^\circ\text{C}$ )

Parameter	Symbol	Limits	Unit
Drain-Source Voltage	$V_{DS}$	650	V
Gate-Source Voltage	$V_{GS}$	$\pm 30$	
Continuous Drain Current @ $V_{GS}=10\text{V}$ , $T_C=25^\circ\text{C}$	$I_D$	10*	A
Continuous Drain Current @ $V_{GS}=10\text{V}$ , $T_C=100^\circ\text{C}$		6.3*	
Pulsed Drain Current @ $V_{GS}=10\text{V}$ (Note 1)		$I_{DM}$	
Avalanche Current (Note 1)	$I_{AS}$	10	mJ
Single Pulse Avalanche Energy (Note 2)	$E_{AS}$	215	
Repetitive Avalanche Energy (Note 1)	$E_{AR}$	6	V/ns
Peak Diode Recovery $dv/dt$ (Note 3)	$dv/dt$	4.5	
Maximum Temperature for Soldering @ Lead at 0.125 in(0.318mm) from case for 10 seconds	$T_L$	300	$^\circ\text{C}$
Total Power Dissipation ( $T_C=25^\circ\text{C}$ )	$P_D$	185	W
Linear Derating Factor		1.48	W/ $^\circ\text{C}$
Operating Junction and Storage Temperature	$T_j, T_{stg}$	-55~+150	$^\circ\text{C}$

\*Drain current limited by maximum junction temperature

\*100% UIS testing in condition of  $V_D=50\text{V}$ ,  $L=4.3\text{mH}$ ,  $V_G=10\text{V}$ ,  $I_L=10\text{A}$ , Rated  $V_{DS}=650\text{V}$

Note : 1.Repetitive rating; pulse width limited by maximum junction temperature.

2.  $I_{AS}=10\text{A}$ ,  $V_{DD}=50\text{V}$ ,  $L=4.3\text{mH}$ ,  $R_G=25\Omega$ , starting  $T_J=+25^\circ\text{C}$ .

3.  $I_{SD}\leq 10\text{A}$ ,  $dI/dt\leq 100\text{A}/\mu\text{s}$ ,  $V_{DD}\leq BV_{DSS}$ , starting  $T_J=+25^\circ\text{C}$ .



**Thermal Data**

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-case, max	R <sub>θJC</sub>	0.68	°C/W
Thermal Resistance, Junction-to-ambient, max	R <sub>θJA</sub>	62.5	

**Characteristics (T<sub>c</sub>=25°C, unless otherwise specified)**

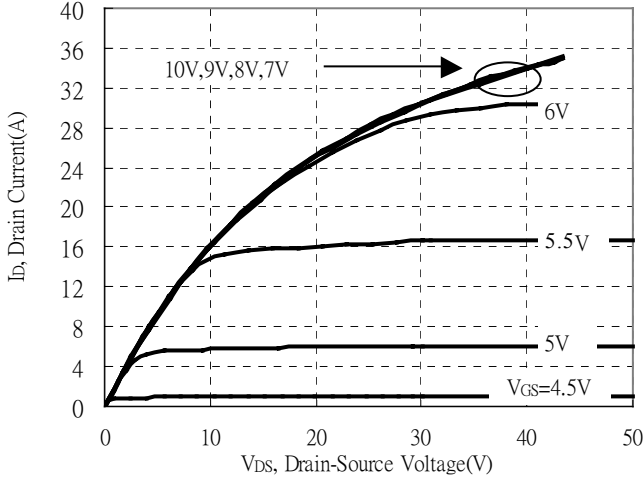
Symbol	Min.	Typ.	Max.	Unit	Test Conditions
<b>Static</b>					
BV <sub>DSS</sub>	650	-	-	V	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA, T <sub>j</sub> =25°C
ΔBV <sub>DSS</sub> /ΔT <sub>j</sub>	-	0.6	-	V/°C	Reference to 25°C, I <sub>D</sub> =250μA
V <sub>GS(th)</sub>	2.0	-	4.0	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> =250μA
*G <sub>FS</sub>	-	16	-	S	V <sub>DS</sub> =15V, I <sub>D</sub> =6A
I <sub>GSS</sub>	-	-	±100	nA	V <sub>GS</sub> =±30V
I <sub>DSS</sub>	-	-	1	μA	V <sub>DS</sub> =650V, V <sub>GS</sub> =0V
	-	-	10		V <sub>DS</sub> =520V, V <sub>GS</sub> =0V, T <sub>j</sub> =125°C
*R <sub>DS(ON)</sub>	-	0.54	0.75	Ω	V <sub>GS</sub> =10V, I <sub>D</sub> =6A
<b>Dynamic</b>					
*Q <sub>g</sub>	-	54.2	-	nC	I <sub>D</sub> =10A, V <sub>DD</sub> =520V, V <sub>GS</sub> =10V
*Q <sub>gs</sub>	-	9.6	-		
*Q <sub>gd</sub>	-	22.2	-		
*t <sub>d(ON)</sub>	-	21.2	-	ns	V <sub>DD</sub> =325V, I <sub>D</sub> =10A, V <sub>GS</sub> =10V, R <sub>G</sub> =9.1 Ω
*t <sub>r</sub>	-	18.2	-		
*t <sub>d(OFF)</sub>	-	81.8	-		
*t <sub>f</sub>	-	25.8	-		
C <sub>iss</sub>	-	1920	-	pF	V <sub>GS</sub> =0V, V <sub>DS</sub> =25V, f=1MHz
C <sub>oss</sub>	-	178	-		
C <sub>rss</sub>	-	24	-		
R <sub>g</sub>	-	2	-	Ω	f=1MHz
<b>Source-Drain Diode</b>					
*V <sub>SD</sub>	-	0.78	1.5	V	I <sub>S</sub> =6A, V <sub>GS</sub> =0V
*I <sub>S</sub>	-	-	10	A	
*I <sub>SM</sub>	-	-	40		
*t <sub>rr</sub>	-	411	-	ns	V <sub>GS</sub> =0V, I <sub>F</sub> =10A, dI <sub>F</sub> /dt=100A/μs
*Q <sub>rr</sub>	-	3.3	-	μC	

\*Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

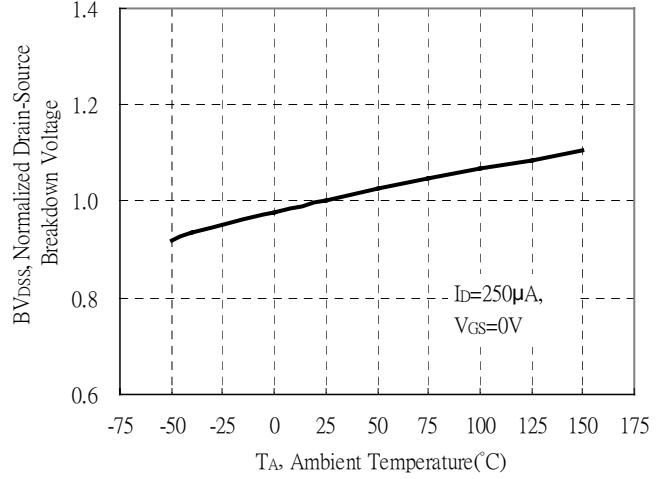


**Typical Characteristics**

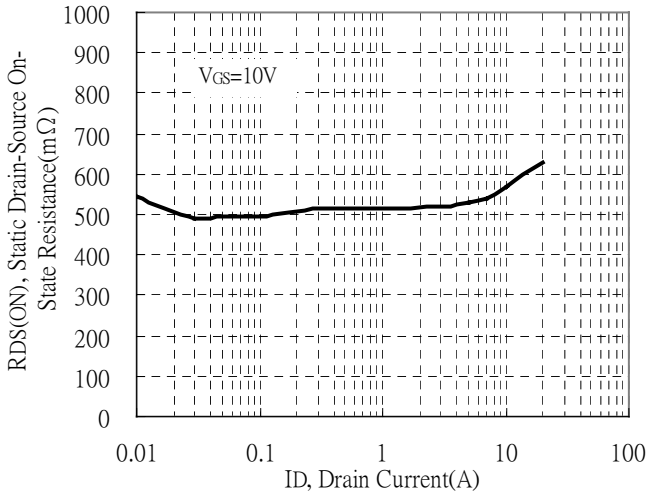
Typical Output Characteristics



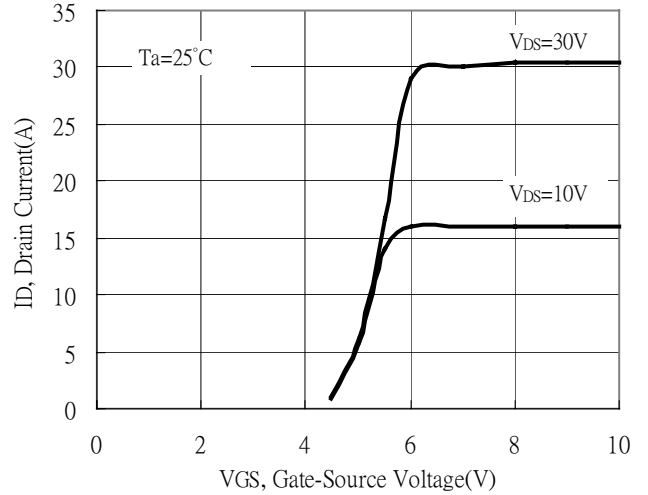
Brekdwn Voltage vs Ambient Temperature



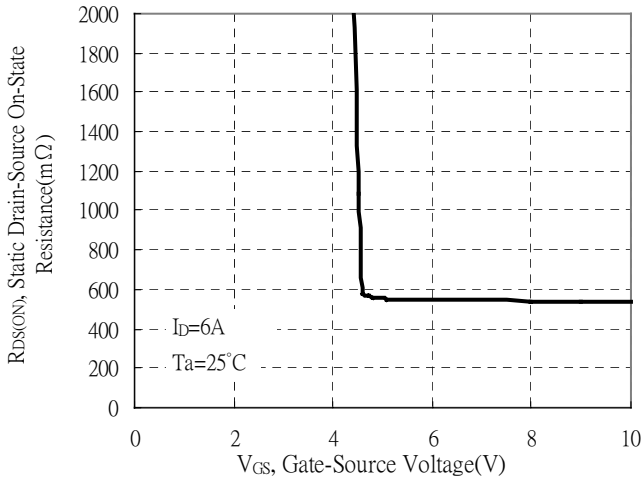
Static Drain-Source On-State resistance vs Drain Current



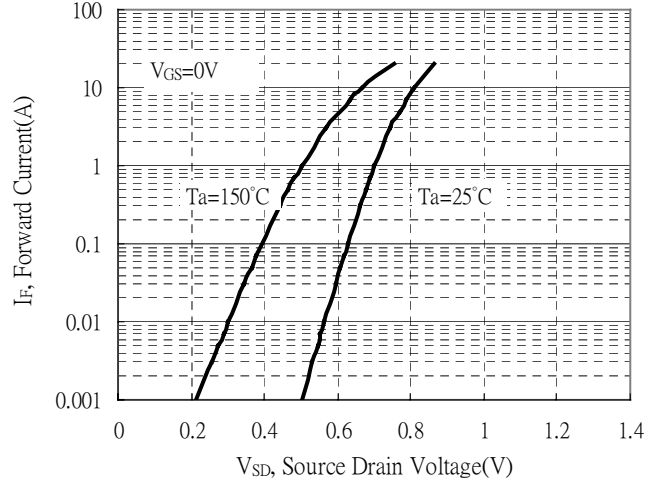
Drain Current vs Gate-Source Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

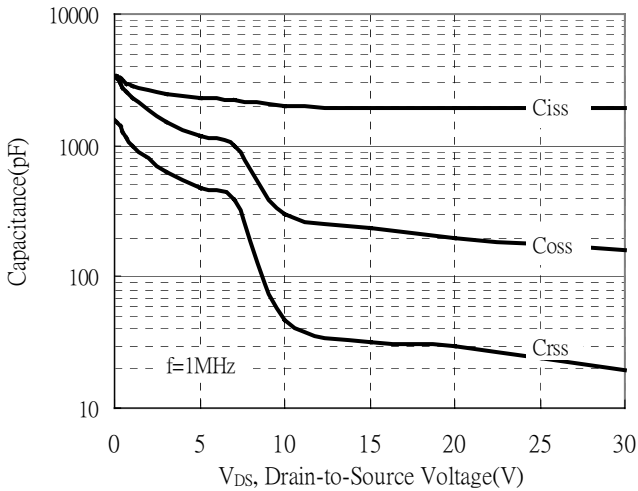


Forward Drain Current vs Source-Drain Voltage

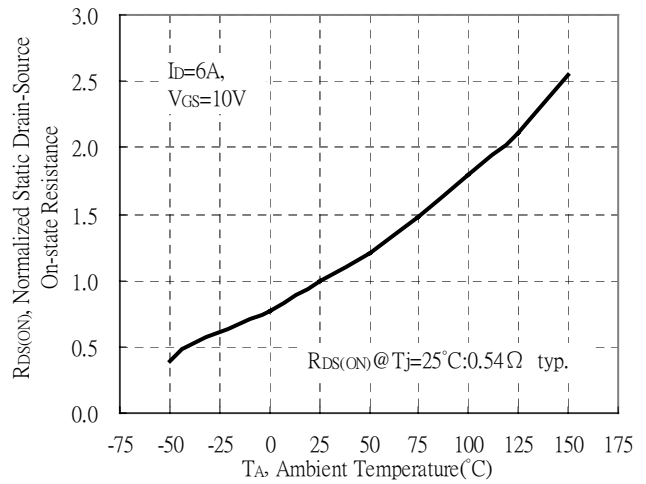


## Typical Characteristics(Cont.)

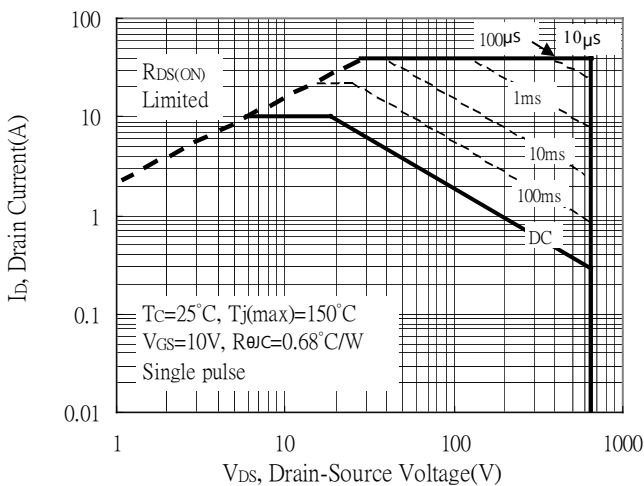
Capacitance vs Reverse Voltage



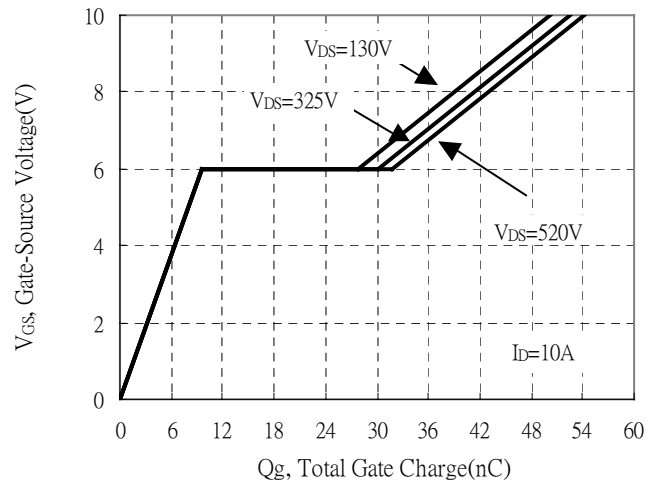
Static Drain-Source On-resistance vs Ambient Temperature



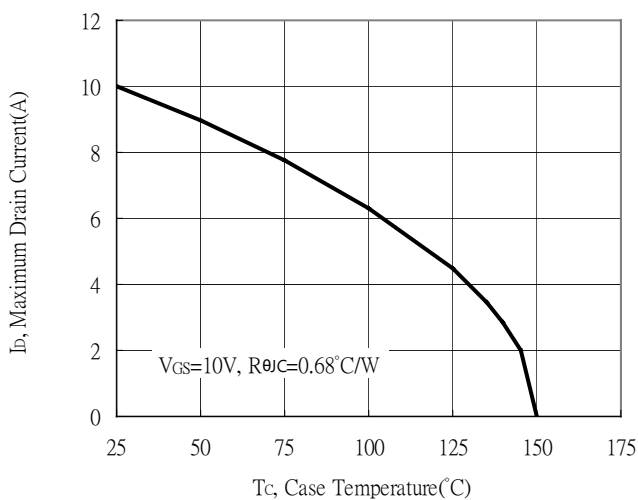
Maximum Safe Operating Area



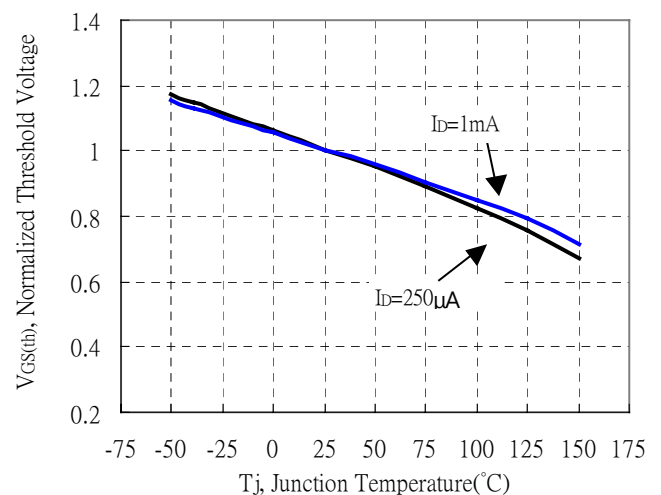
Gate Charge Characteristics



Maximum Drain Current vs Case Temperature



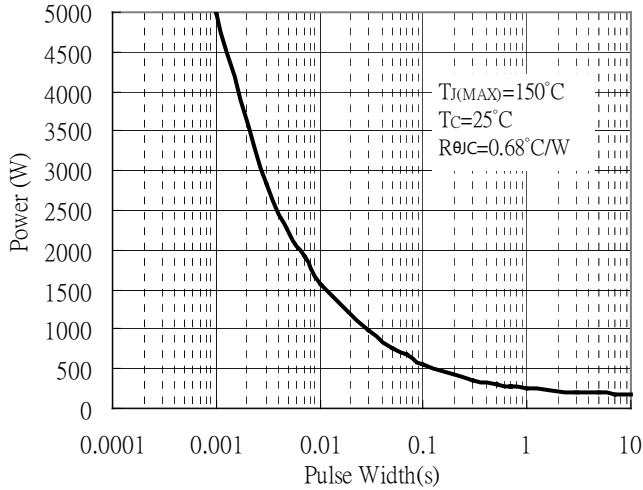
Threshold Voltage vs Junction Temperature



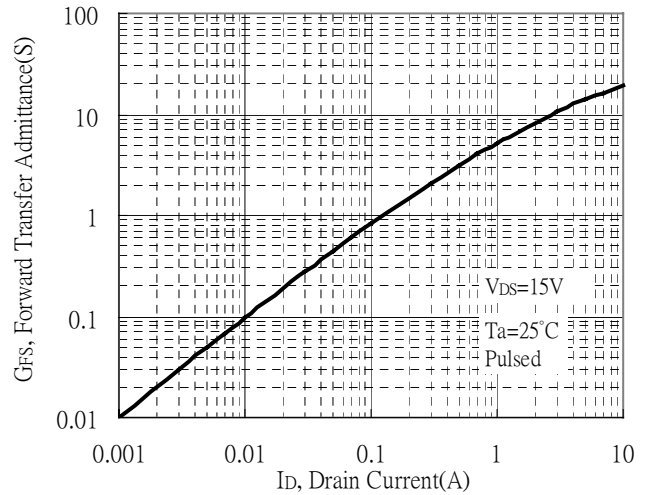


**Typical Characteristics(Cont.)**

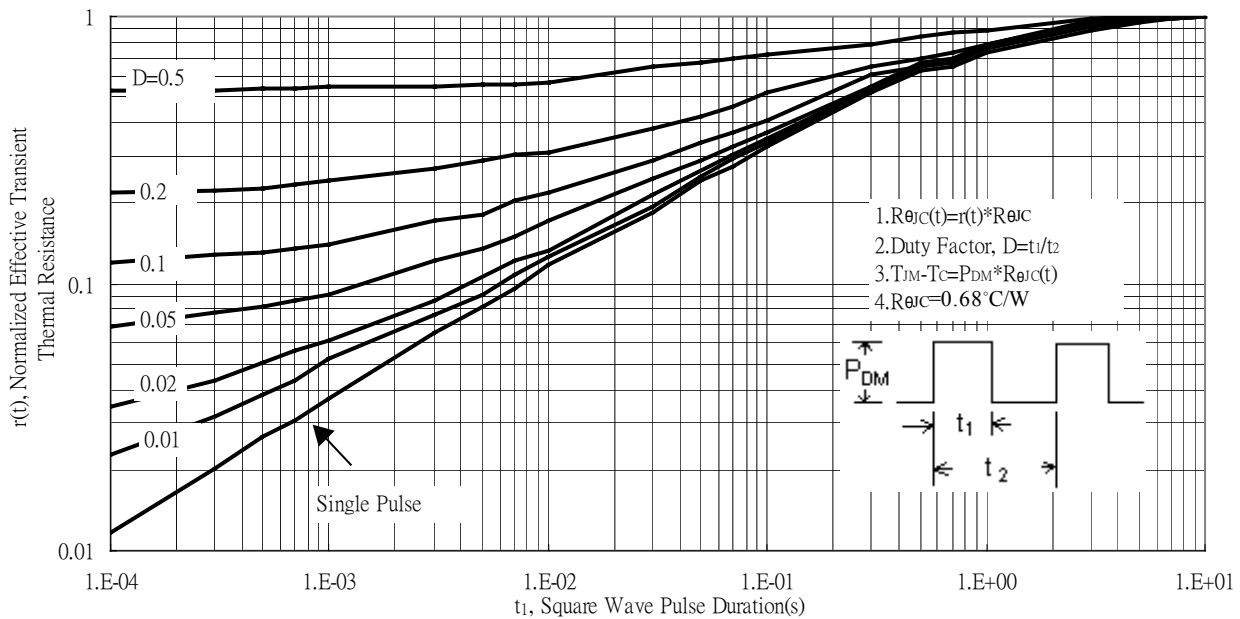
Single Pulse Power Rating, Junction to Case



Forward Transfer Admittance vs Drain Current



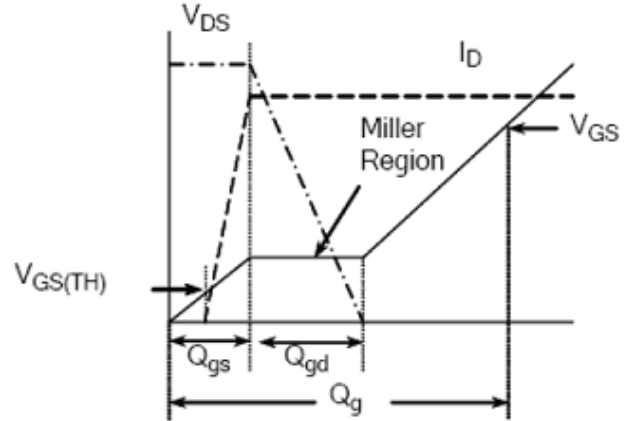
Transient Thermal Response Curves



**Test Circuits and Waveforms**



**Gate Charge Test Circuit**



**Gate Charge Waveform**



**Resistive Switching Test Circuit**

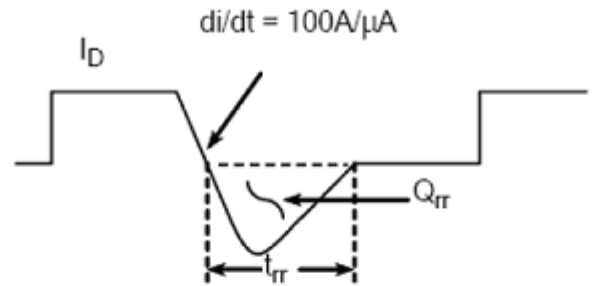


**Resistive Switching Waveforms**

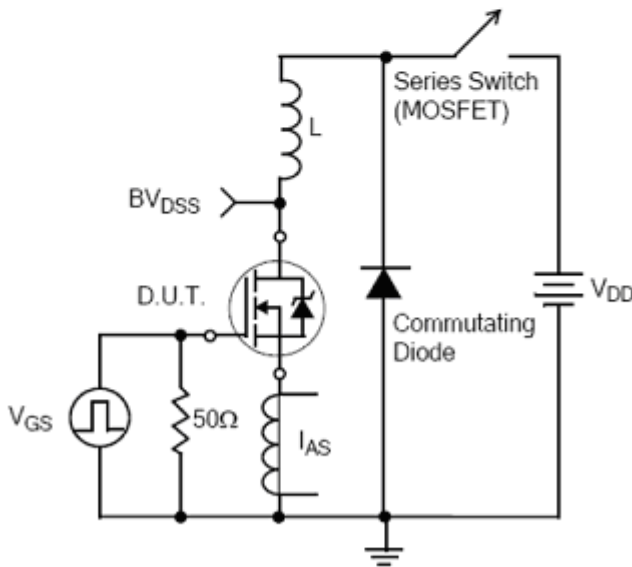
**Test Circuits and Waveforms(Cont.)**



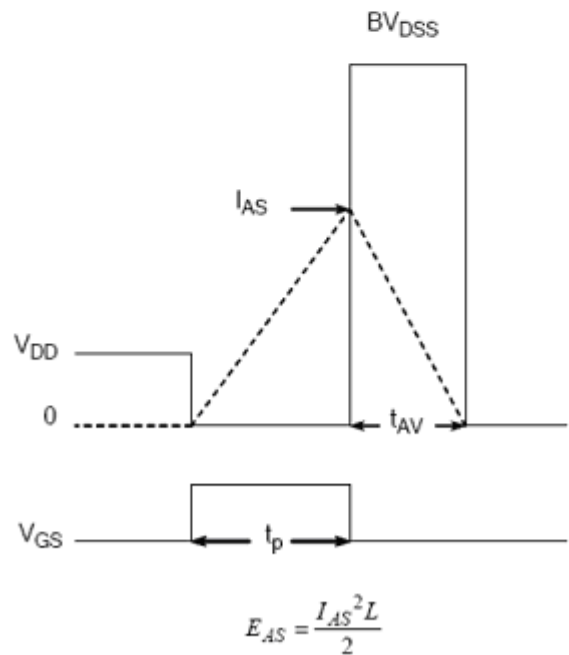
**Diode Reverse Recovery Test Circuit**



**Diode Reverse Recovery Waveform**



**Unclamped Inductive Switching Test Circuit**



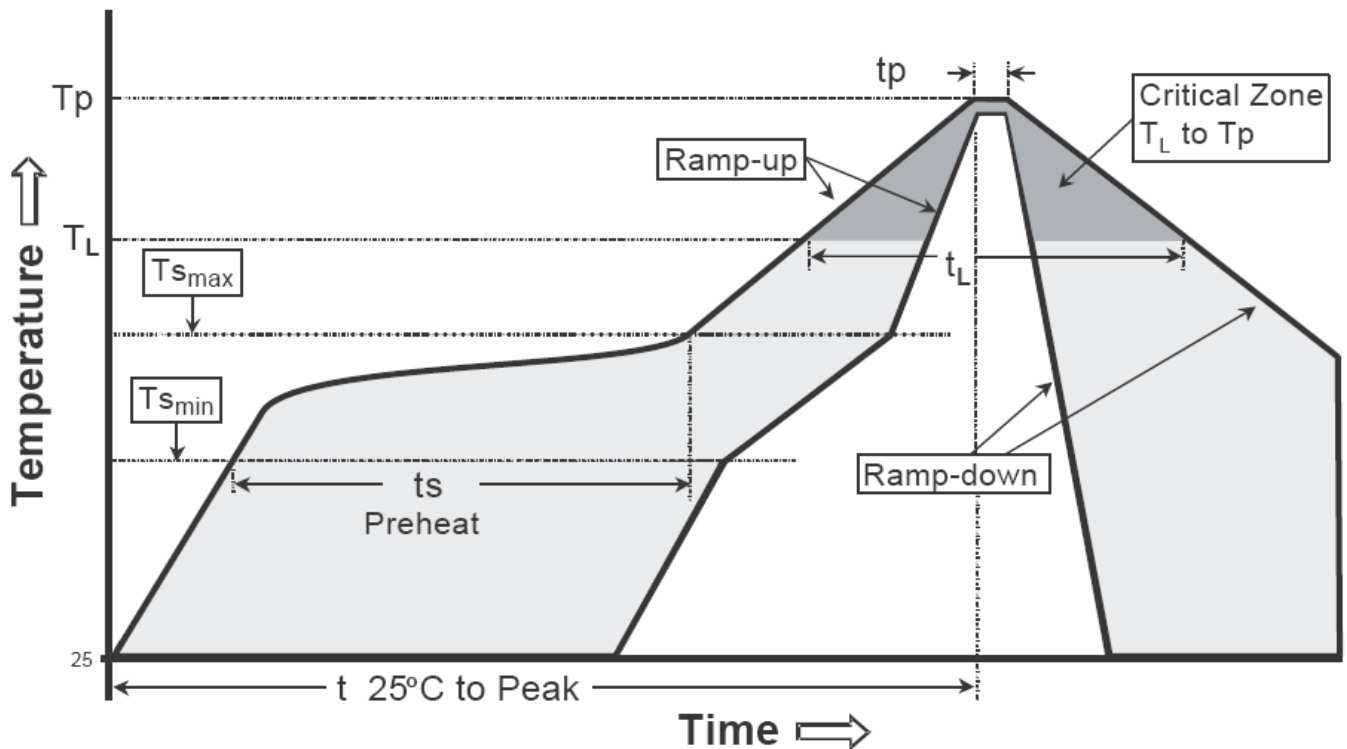
**Unclamped Inductive Switching Waveforms**



**Recommended wave soldering condition**

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

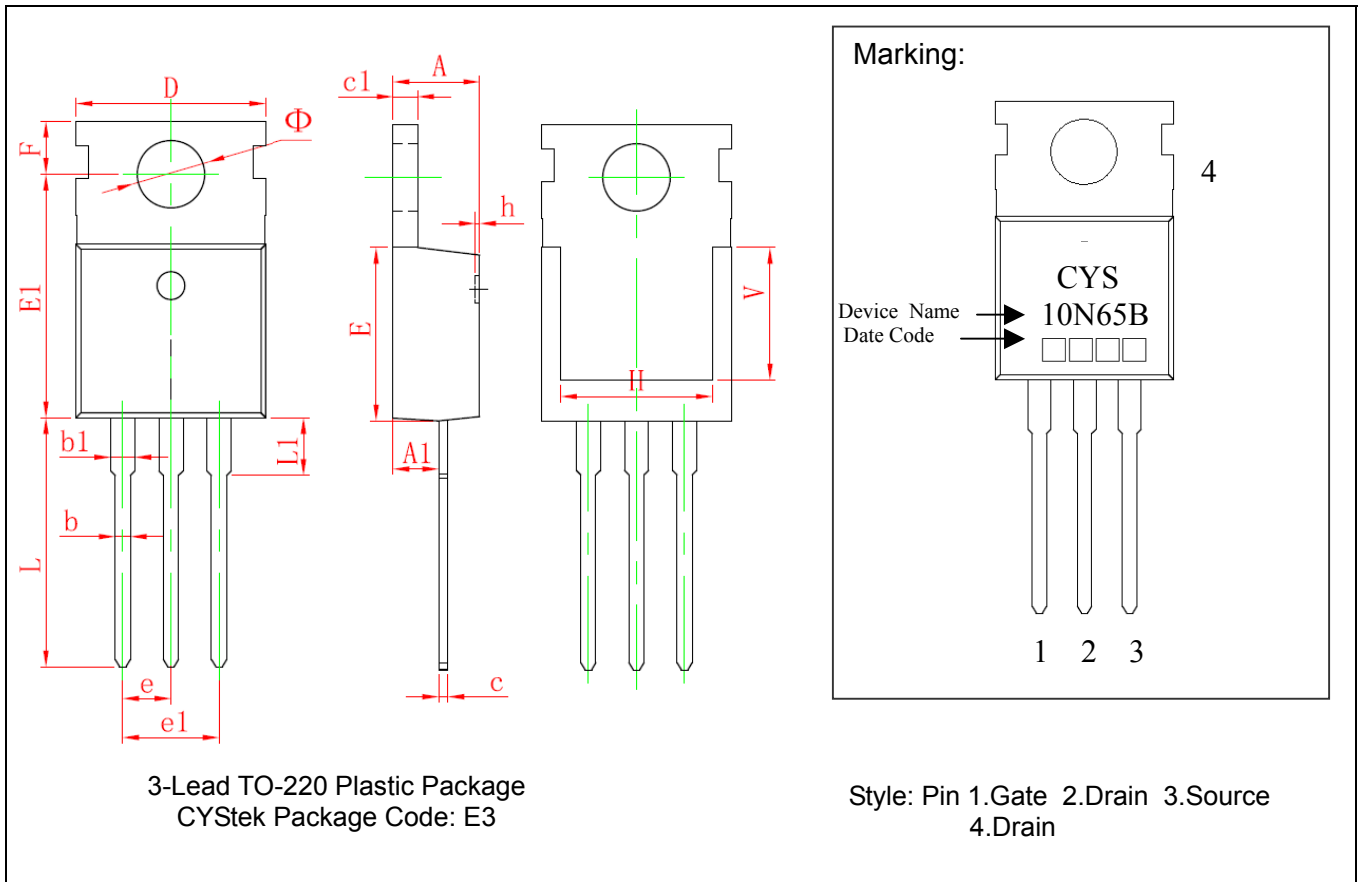
**Recommended temperature profile for IR reflow**



Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (Tsmax to Tp)	3°C/second max.	3°C/second max.
Preheat		
-Temperature Min(Ts min)	100°C	150°C
-Temperature Max(Ts max)	150°C	200°C
-Time(ts min to ts max)	60-120 seconds	60-180 seconds
Time maintained above:		
-Temperature (Tl)	183°C	217°C
- Time (tl)	60-150 seconds	60-150 seconds
Peak Temperature(Tp)	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.

**TO-220 Dimension**



\*: Typical

DIM	Millimeters		Inches		DIM	Millimeters		Inches	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	4.400	4.600	0.173	0.181	e	2.540*		0.100*	
A1	2.250	2.550	0.089	0.100	e1	4.980	5.180	0.196	0.204
b	0.710	0.910	0.028	0.036	F	2.650	2.950	0.104	0.116
b1	1.170	1.370	0.046	0.054	H	7.900	8.100	0.311	0.319
c	0.330	0.650	0.013	0.026	h	0.000	0.300	0.000	0.012
c1	1.200	1.400	0.047	0.055	L	12.900	13.400	0.508	0.528
D	9.910	10.250	0.390	0.404	L1	2.850	3.250	0.112	0.128
E	8.950	9.750	0.352	0.384	V	7/500	REF	0.295	REF
E1	12.650	12.950	0.498	0.510	Φ	3.400	3.800	0.134	0.150

Notes: 1.Controlling dimension: millimeters.  
 2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.  
 3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

**Material:**

- Lead: Pure tin plated.
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0.

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