

N -Channel Logic Level Enhancement Mode Power MOSFET

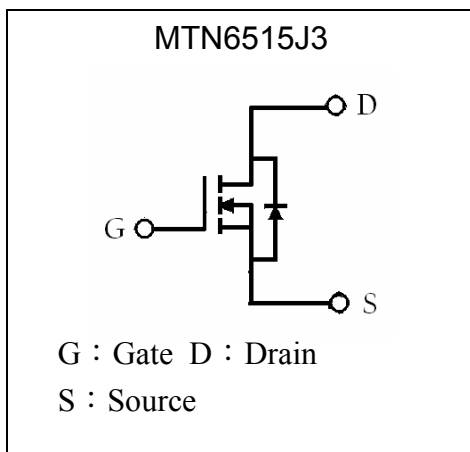
MTN6515J3

BV_{DSS}	150V
I_D	20A
R_{DS(ON)}@V_{GS}=10V, I_D=15A	60mΩ (typ)
R_{DS(ON)}@V_{GS}=5V, I_D=10A	59mΩ (typ)
R_{DS(ON)}@V_{GS}=3V, I_D=3A	60mΩ (typ)

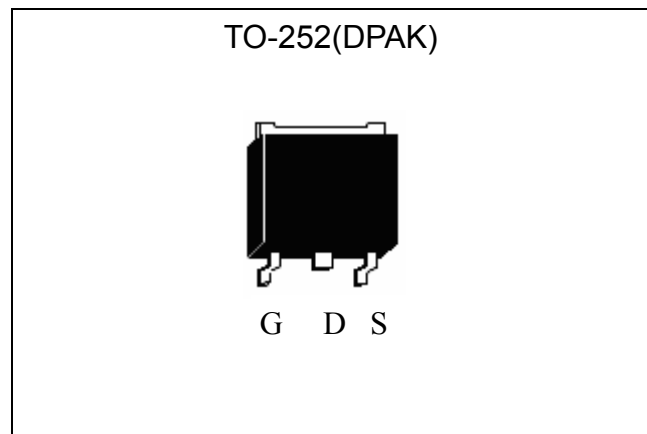
Features

- Low Gate Charge
- Simple Drive Requirement
- Pb-free lead plating and halogen-free package

Equivalent Circuit



Outline



Absolute Maximum Ratings (T_C=25°C, unless otherwise noted)

Parameter Sy	mbol	Limits	Unit
Drain-Source Voltage	V _{DS}	150	V
Gate-Source Voltage	V _{GS}	±16	
Continuous Drain Current @ T _C =25°C	I _D	20	A
Continuous Drain Current @ T _C =100°C	I _D	14	
Pulsed Drain Current *1	I _{DM}	60	
Avalanche Current	I _{AS}	20	
Avalanche Energy @ L=0.1mH, I _D =10A, R _G =25Ω	E _{AS}	5	mJ
Repetitive Avalanche Energy @ L=0.05mH *2	E _{AR}	2.5	
Total Power Dissipation @T _C =25°C	P _d	60	W
Total Power Dissipation @T _C =100°C		30	
Operating Junction and Storage Temperature Range	T _J , T _{stg}	-55~+175	°C

Note : *1. Pulse width limited by maximum junction temperature

*2. Duty cycle ≤ 1%

**Thermal Data**

Parameter Sym	bol	Value	Unit
Thermal Resistance, Junction-to-case, max	$R_{th,j-c}$ 2.5		$^{\circ}\text{C/W}$
Thermal Resistance, Junction-to-ambient, max	$R_{th,j-a}$ 100		$^{\circ}\text{C/W}$

Characteristics (Tc=25°C, unless otherwise specified)

Symbol	Min.		Typ.	Max.	Unit	Test Conditions
Static						
BV _{DSS} 150			-	-	V	V _{GS} =0V, I _D =250μA
V _{GS(th)}	0.45	0.8	1.20		V	V _{DS} =V _{GS} , I _D =250μA
I _{GSS} -			-	±100	nA	V _{GS} =±12, V _{DS} =0
I _{DSS}	-	-		1	μA	V _{DS} =120V, V _{GS} =0
	-		-	25		V _{DS} =100V, V _{GS} =0, T _J =125°C
R _{DS(ON)} *1	-	60		75	mΩ	V _{GS} =10V, I _D =15A
	-	59		75		V _{GS} =5V, I _D =10A
	-	60		75		V _{GS} =3V, I _D =3A
G _{FS} *1	-		48	-	S	V _{DS} =5V, I _D =10A
Dynamic						
Q _g *1, 2	-		30	-	nC I	I _D =10A, V _{DS} =80V, V _{GS} =5V
Q _{gs} *1, 2	-		4.8	-		
Q _{gd} *1, 2 -			16	-		
t _{d(ON)} *1, 2	-		23	-	ns	V _{DS} =75V, I _D =1A, V _{GS} =4.5V, R _G =6Ω
t _r *1, 2	-		22	-		
t _{d(OFF)} *1, 2	-		91	-		
t _f *1, 2	-		63	-		
C _{iss} -			2282	-	pF V	V _{GS} =0V, V _{DS} =25V, f=1MHz
C _{oss} -			120	-		
C _{rss} -			66	-		
Source-Drain Diode						
I _S *1 -			-	20	A	
I _{SM} *3 -			-	60		
V _{SD} *1 -			0.85	1.3	V	I _F =I _S , V _{GS} =0V
t _{rr}	-		50	-	ns	I _F =20A, dI _F /dt=100A/μs
Q _{rr} -			120	-	nC	

Note : *1.Pulse Test : Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$

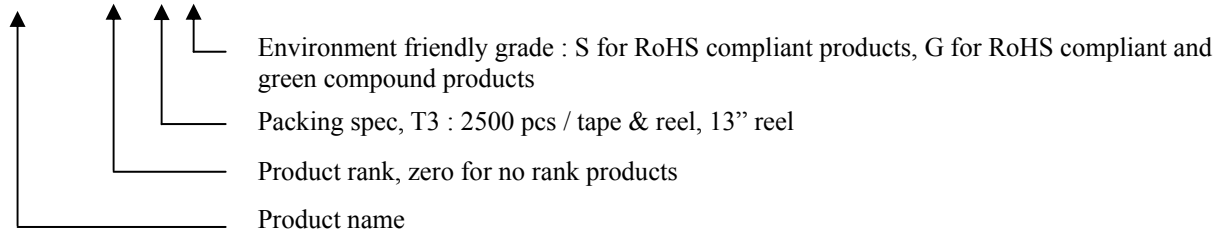
*2.Independent of operating temperature

*3.Pulse width limited by maximum junction temperature.

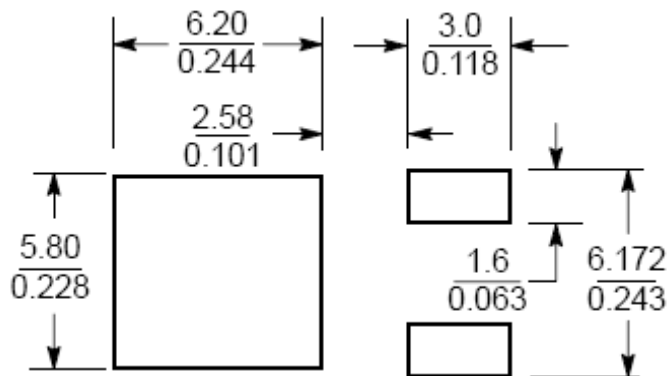


Ordering Information

Device Package		Shipping
MTN6515J3-0-T3-G	TO-252 (Pb-free lead plating and halogen-free package)	2500 pcs / Tape & Reel



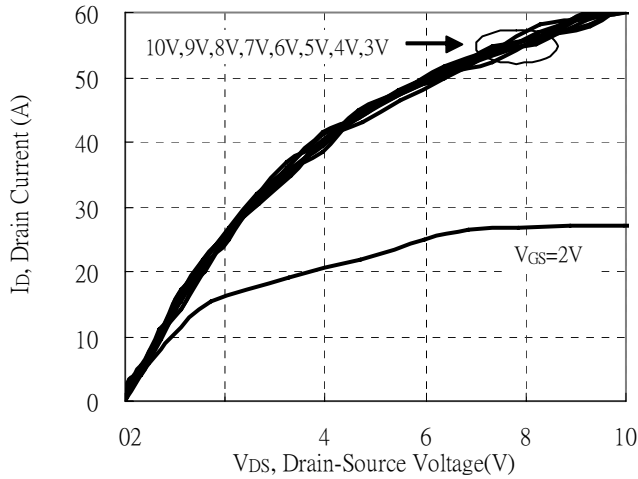
Recommended soldering footprint



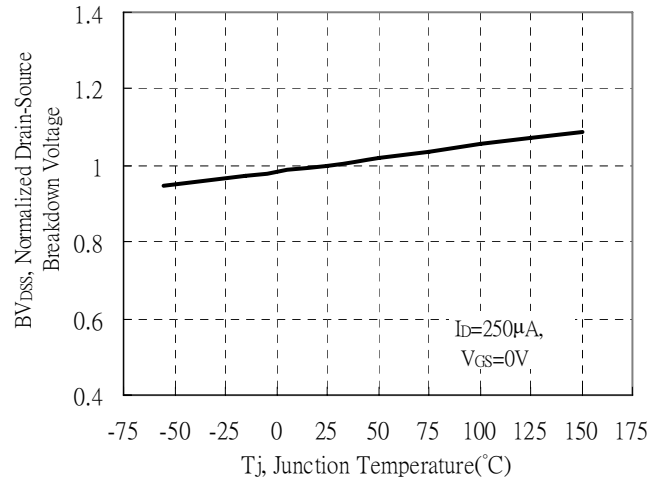
Unit ($\frac{\text{mm}}{\text{inch}}$)

Typical Characteristics

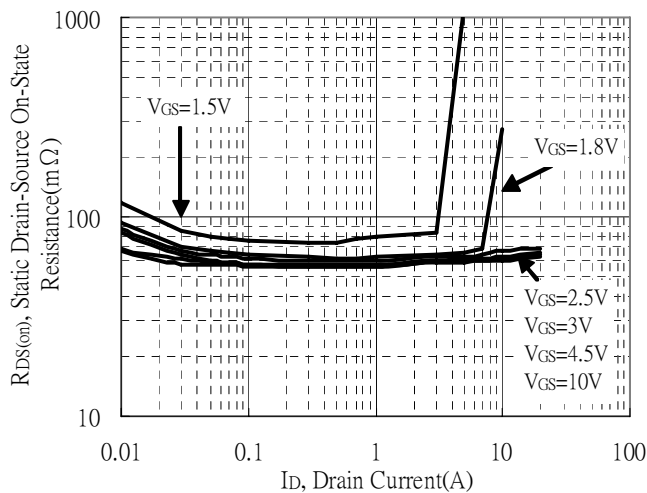
Typical Output Characteristics



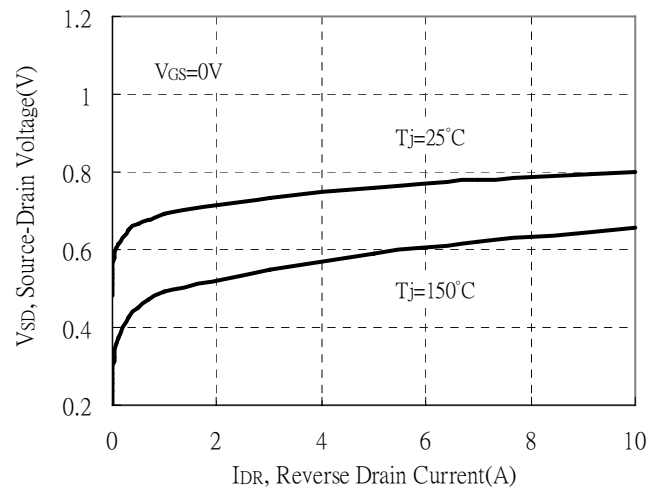
Breakdown Voltage vs Ambient Temperature



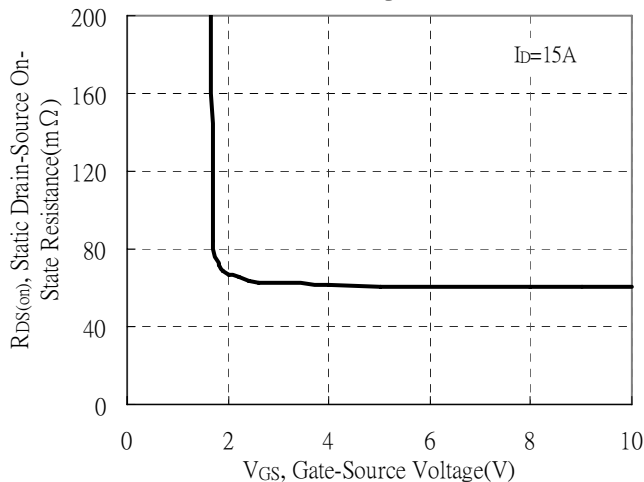
Static Drain-Source On-State resistance vs Drain Current



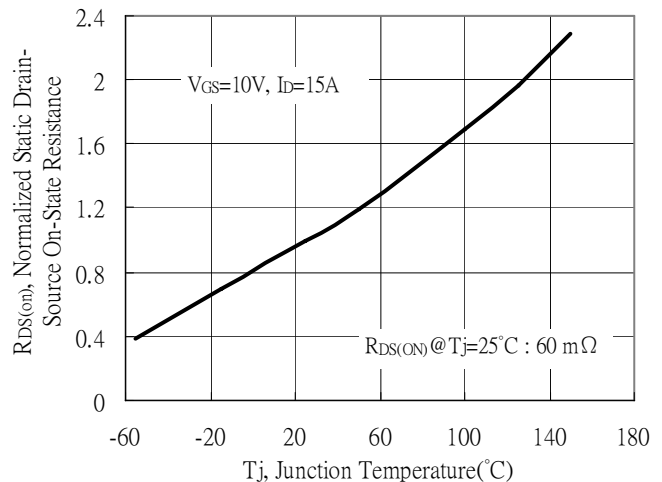
Reverse Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

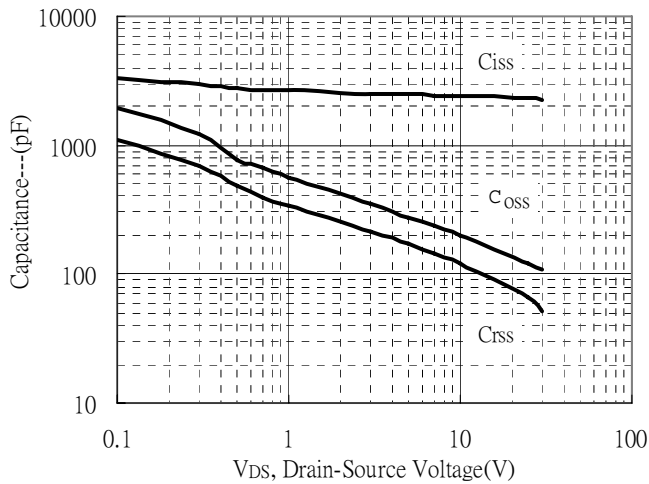


Drain-Source On-State Resistance vs Junction Temperature

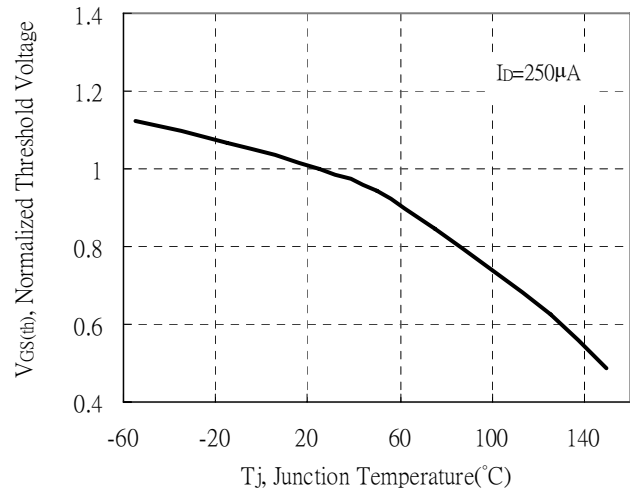


Typical Characteristics (Cont.)

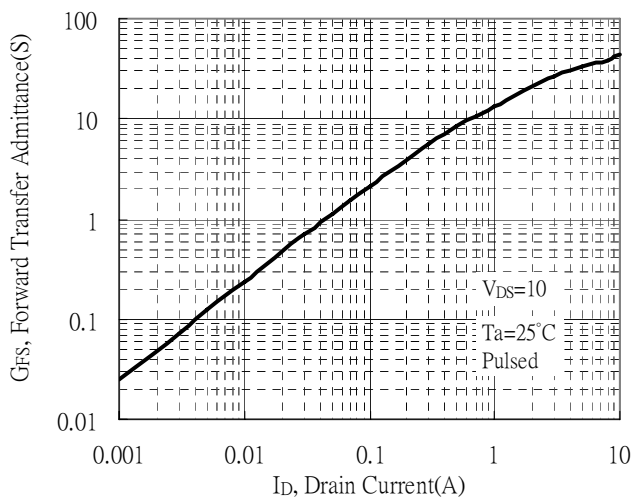
Capacitance vs Drain-to-Source Voltage



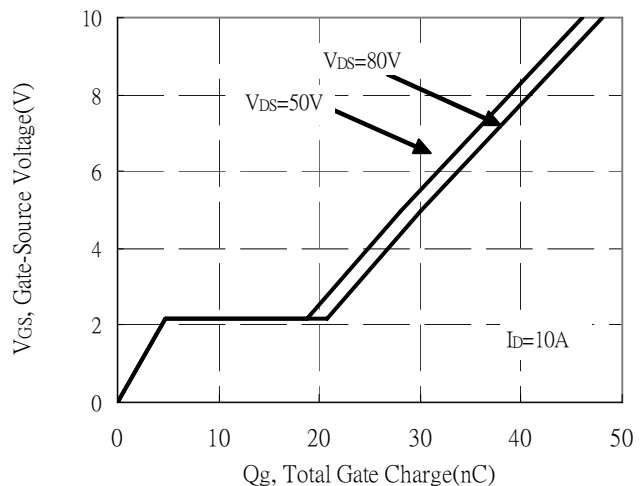
Threshold Voltage vs Junction Temperature



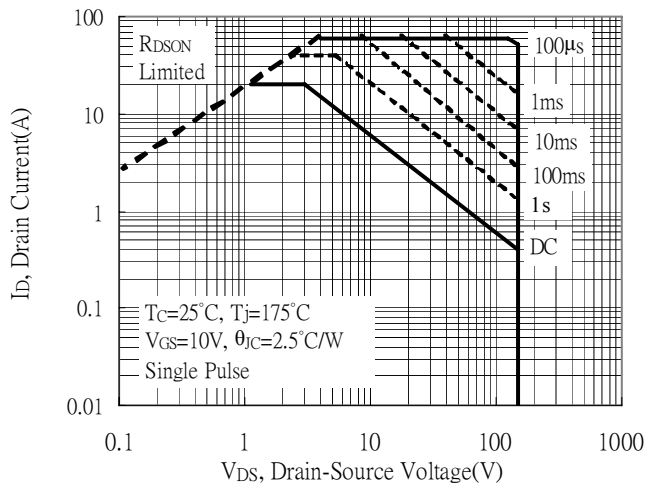
Forward Transfer Admittance vs Drain Current



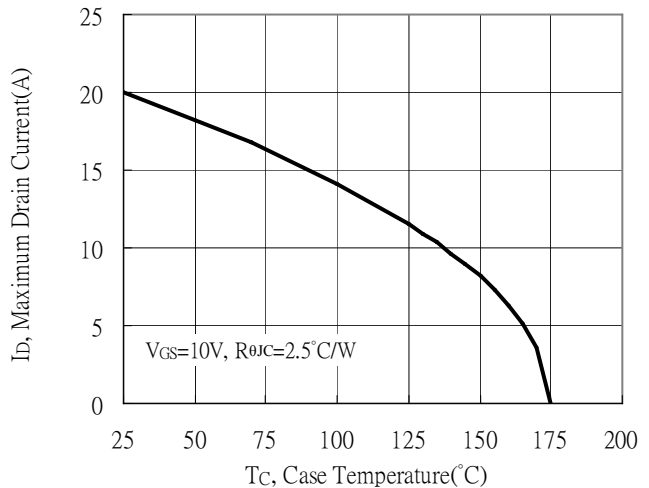
Gate Charge Characteristics



Maximum Safe Operating Area

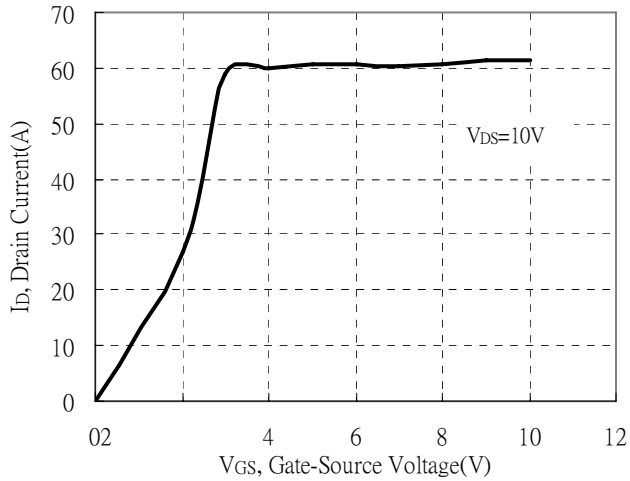


Maximum Drain Current vs Case Temperature

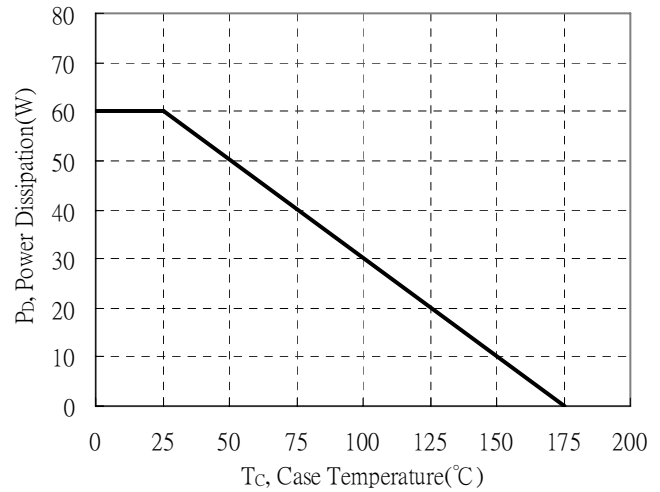


Typical Characteristics (Cont.)

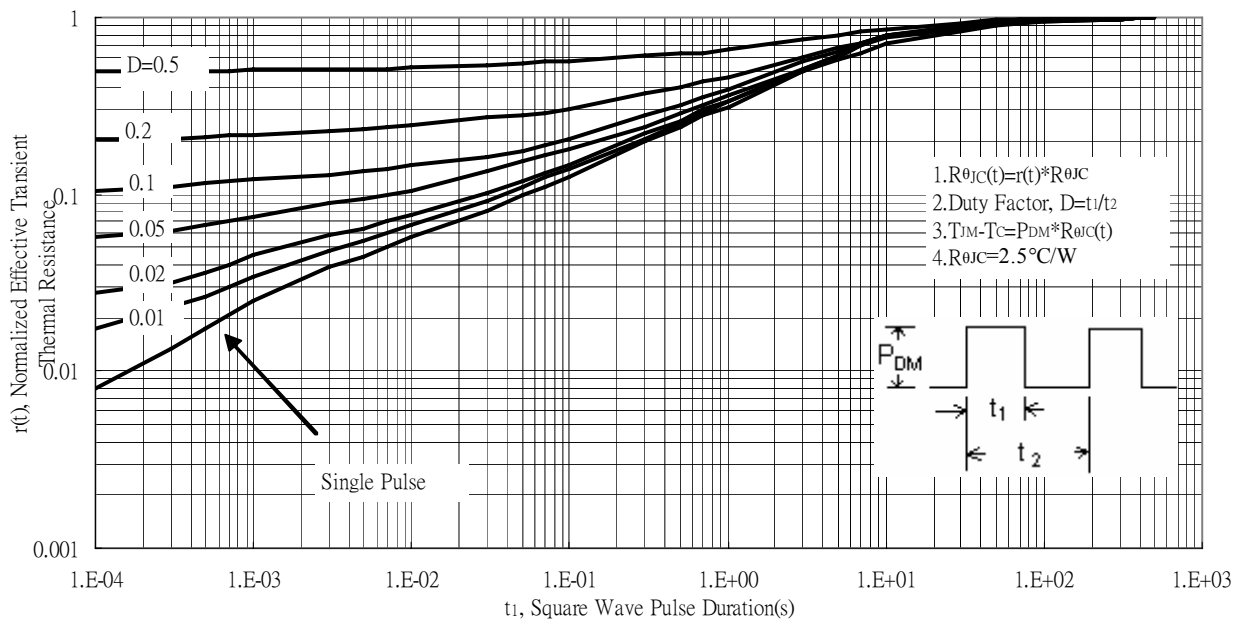
Typical Transfer Characteristics



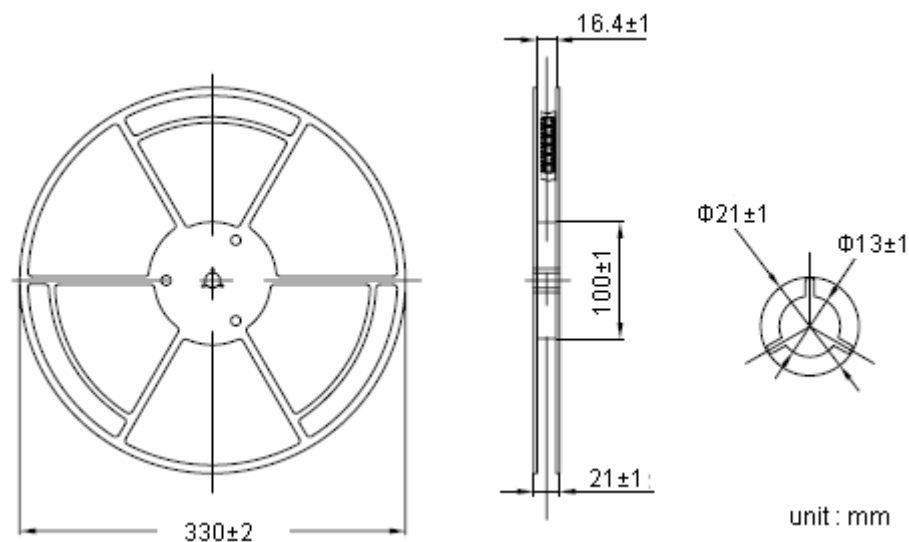
Power Derating Curve



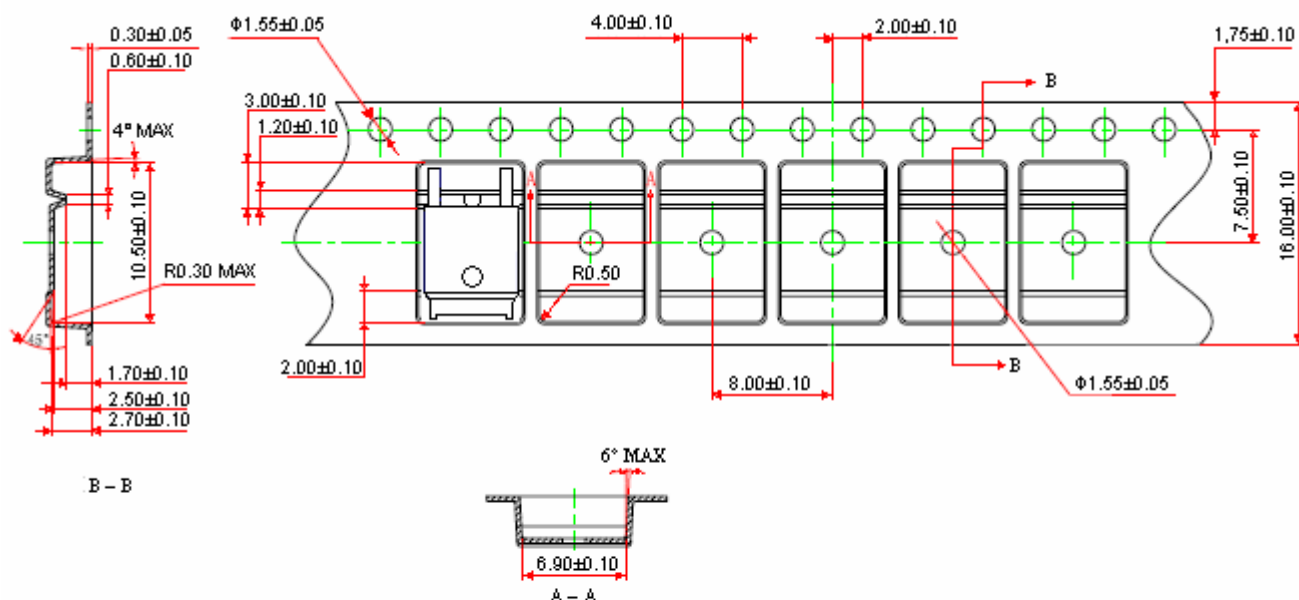
Transient Thermal Response Curves



Reel Dimension



Carrier Tape Dimension



Notes:

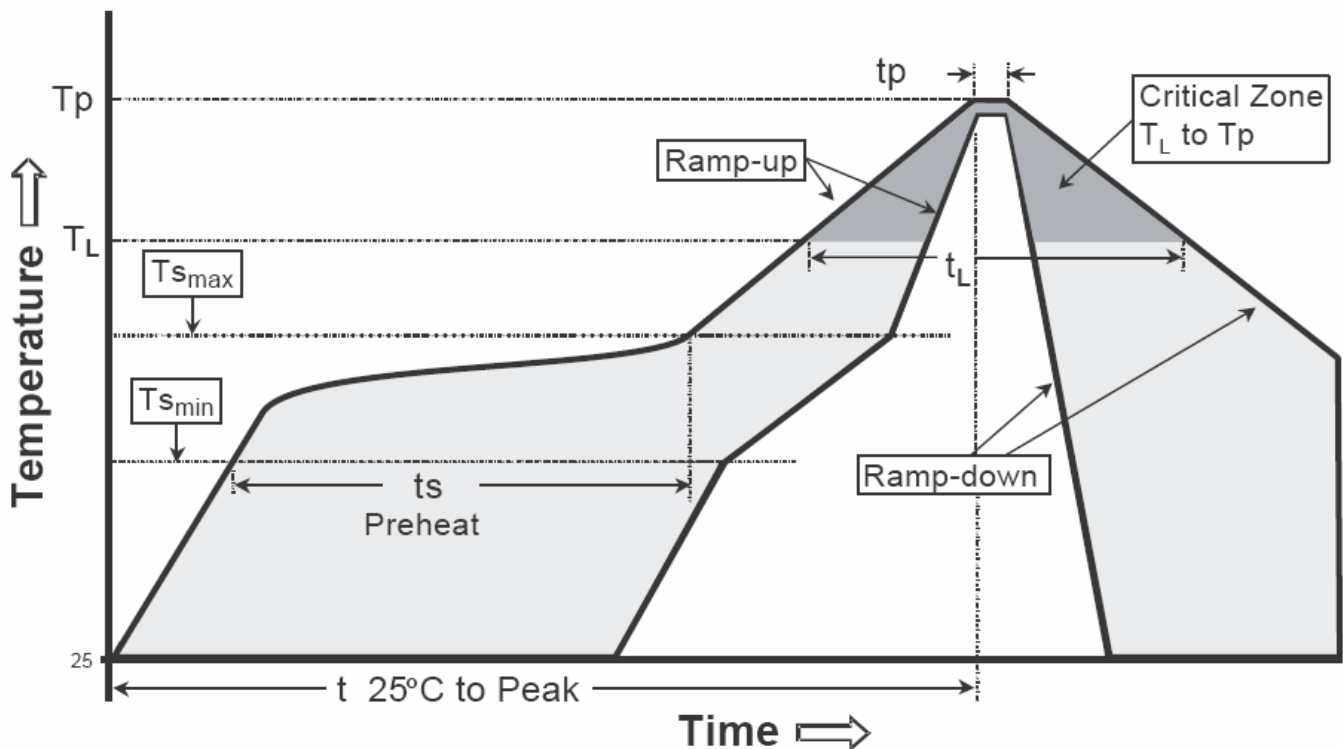
1. 10 sprocket hole pitch cumulative tolerance ± 0.2 .
2. Camber not to exceed 1mm in 100mm.
3. Material: conductive black polystyrene, antistatic coated : $10^5 \Omega/\square \sim 10^{11} \Omega/\square$

unit : mm

Recommended wave soldering condition

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

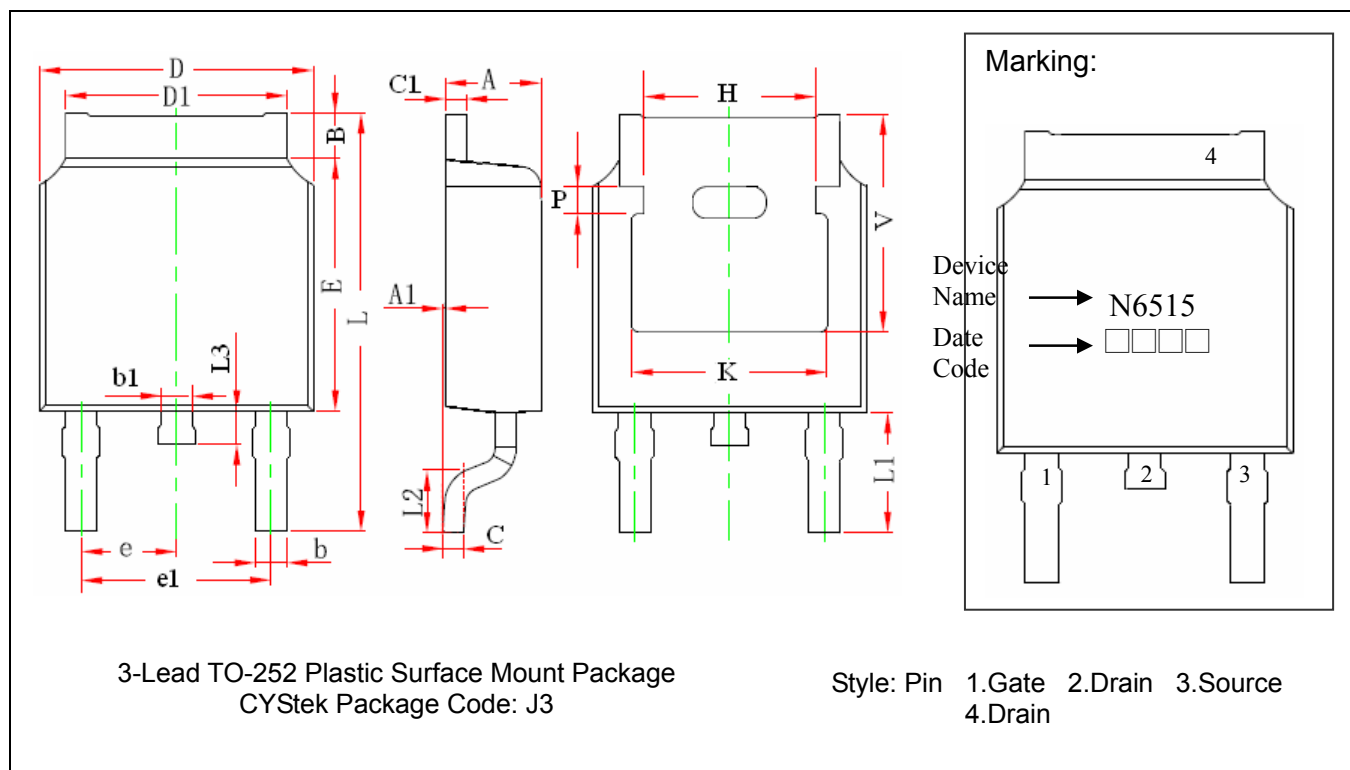
Recommended temperature profile for IR reflow



Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (Tsmax to Tp)	3°C/second max.	3°C/second max.
Preheat		
-Temperature Min(Ts min)	100°C	150°C
-Temperature Max(Ts max)	150°C	200°C
-Time(ts min to ts max)	60-120 seconds	60-180 seconds
Time maintained above:		
-Temperature (TL)	183°C	217°C
- Time (tL)	60-150 seconds	60-150 seconds
Peak Temperature(TP)	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.

TO-252 Dimension



DIM	Inches Millimeters				DIM	Inches Millimeters			
	Min.	Max	Min.	Max		Min.	Max.	Min.	Max.
A	0.087	0.094	2.200	2.400	e	0.086	0.094	2.186	2.386
A1	0.000	0.005	0.000	0.127	e1	0.172	0.188	4.372	4.772
B	0.039	0.048	0.990	1.210	H	0.163	REF	4.140	REF
b	0.026	0.034	0.660	0.860	K	0.190	REF	4.830	REF
b1	0.026	0.034	0.660	0.860	L	0.386	0.409	9.800	10.400
C	0.018	0.023	0.460	0.580	L1	0.114	REF	2.900	REF
C1	0.018	0.023	0.460	0.580	L2	0.055	0.067	1.400	1.700
D	0.256	0.264	6.500	6.700	L3	0.024	0.039	0.600	1.000
D1	0.201	0.215	5.100	5.460	P	0.026	REF	0.650	REF
E	0.236	0.244	6.000	6.200	V	0.211	REF	5.350	REF

Notes: 1.Controlling dimension: millimeters.

2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.

3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

Material:

- Lead : Pure tin plated.
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0.

Important Notice:

- All rights are reserved. Reproduction in whole or in part is prohibited without the prior written approval of CYStek.
- CYStek reserves the right to make changes to its products without notice.
- CYStek **semiconductor products are not warranted to be suitable for use in Life-Support Applications, or systems.**
- CYStek assumes no liability for any consequence of customer product design, infringement of patents, or application assistance.