



## DESCRIPTION

This new series of digital transistors is designed to replace a single device and its external resistor bias network. The BRT (Bias Resistor Transistor) contains a single transistor with a monolithic bias network resistor. The BRT eliminates these individual components by integrating them into a single device. The use of a BRT can reduce both system cost and board space. The device is housed in the SOT-23 package which is designed for low power surface mount applications.

The MUN2211~MUN2241 are available in SOT-23 Package.

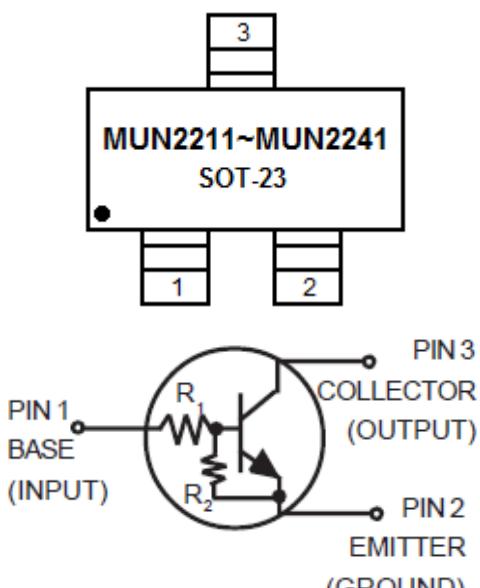
## ORDERING INFORMATION

Package Type	Part Number
SOT-23	MUN2211
	MUN2212
	MUN2213
	MUN2214
	MUN2215
	MUN2216
	MUN2230
	MUN2231
	MUN2232
	MUN2233
	MUN2234
	MUN2235
	MUN2238
	MUN2241
Note	3,000pcs/Reel
AiT provides all RoHS Compliant Products	

## FEATURES

- Simplifies Circuit Design
- Reduces Board Space and Component Count
- The SOT-23 package can be soldered using wave or reflow. The modified gull-winged leads absorb thermal stress during soldering eliminating the possibility of damage to the die.
- Available in SOT-23 Package

## PIN DESCRIPTION





## ABSOLUTE MAXIMUM RATINGS

$T_A = 25^\circ\text{C}$  unless otherwise noted.

$V_{CBO}$ , Collector-Base Voltage	50Vdc
$V_{CEO}$ , Collector-Emitter Voltage	50Vdc
$I_C$ , Collector Current	100mAdc
$P_D$ , Total Power Dissipation @ $T_A = 25^\circ\text{C}$ NOTE1	246mW
Derate above $25^\circ\text{C}$	1.5°C/W

NOTE1: Device mounted on a FR-4 glass epoxy printed circuit board using the minimum recommended footprint.

## THERMAL CHARACTERISTICS

Parameter	Symbol	Value	Unit
Thermal Resistance-Junction-to-Ambient NOTE1	$R_{\theta JA}$	508	°C/W
Operating and Storage Temperature Range	$T_J, T_{STG}$	-55 ~ +150	°C
Maximum Temperature for Soldering Purposes, Time in Solder Bath	$T_L$	260	°C
		10	Sec

NOTE1: Device mounted on a FR-4 glass epoxy printed circuit board using the minimum recommended footprint.



## ELECTRICAL CHARACTERISTICS

$T_A = 25^\circ\text{C}$ , unless otherwise noted.

Parameter	Symbol	Characteristic	Min	Typ	Max	Unit
<b>OFF CHARACTERISTICS</b>						
Collector-Base Cutoff Current	$I_{CBO}$	$V_{CB} = 50\text{V}, I_E = 0$	-	-	100	nAdc
Collector-Emitter Cutoff Current	$I_{CEO}$	$V_{CE} = 50\text{V}, I_B = 0$	-	-	500	nAdc
Emitter-Base Cutoff Current	$I_{EBO}$	$V_{EB} = 6.0\text{V}, I_C = 0$	-	-	mAdc	0.5
						0.2
						0.1
						0.2
						0.9
						1.9
						4.3
						2.3
						1.5
						0.18
						0.13
						0.2
						4.0
						0.1
Collector-Base Breakdown Voltage	$V_{(BR)CBO}$	$I_C = 10\mu\text{A}, I_E = 0$	50	-	-	Vdc
Collector-Emitter Breakdown Voltage <sup>NOTE2</sup>	$V_{(BR)CEO}$	$I_C = 2.0\text{mA}, I_B = 0$	50	-	-	Vdc

NOTE2: Pulse Test: Pulse Width < 300μs, Duty Cycle < 2.0%.



T<sub>A</sub> = 25°C, unless otherwise noted.

Parameter	Symbol	Characteristic	Min	Typ	Max	Unit
<b>ON CHARACTERISTICS</b> <small>NOTE2</small>						
DC Current Gain	$h_{FE}$	V <sub>CE</sub> = 10V, $I_C$ = 5.0mA	MUN2211	35	60	
			MUN2212	60	100	
			MUN2213	80	140	
			MUN2214	80	140	
			MUN2215	160	350	
			MUN2216	160	350	
			MUN2230	3.0	5.0	
			MUN2231	8.0	15	
			MUN2232	15	30	
			MUN2233	80	200	
			MUN2234	80	150	
			MUN2235	80	140	
			MUN2238	160	350	
			MUN2241	160	350	
Collector-Emitter Saturation Voltage	V <sub>CE(sat)</sub>	$I_C$ = 10mA, $I_B$ = 0.3mA $I_C$ = 10mA, $I_B$ = 5mA $I_C$ = 10mA, $I_B$ = 1mA	MUN2230	-	-	0.25 Vdc
			MUN2231	-	-	
			MUN2215	-	-	
			MUN2216	-	-	
			MUN2232	-	-	
			MUN2233	-	-	
			MUN2234	-	-	
			MUN2235	-	-	
			MUN2238	-	-	

NOTE2: Pulse Test: Pulse Width < 300μs, Duty Cycle < 2.0%.



T<sub>A</sub> = 25°C, unless otherwise noted.

Parameter	Symbol	Characteristic	Min	Typ	Max	Unit
<b>ON CHARACTERISTICS</b> <small>NOTE2</small>						
Output Voltage (on)	V <sub>OL</sub>	V <sub>CC</sub> = 5.0V, V <sub>B</sub> = 2.5V, R <sub>L</sub> = 1.0kΩ	MUN2211	-	0.2	Vdc
			MUN2212			
			MUN2214			
			MUN2215			
			MUN2216			
			MUN2230			
			MUN2231			
			MUN2232			
			MUN2233			
			MUN2234			
Output Voltage (off)	V <sub>OH</sub>	V <sub>CC</sub> = 5.0V, V <sub>B</sub> = 3.5V, R <sub>L</sub> = 1.0kΩ	MUN2213	4.9	-	Vdc
		V <sub>CC</sub> = 5.0V, V <sub>B</sub> = 5.0V, R <sub>L</sub> = 1.0kΩ	MUN2241			
		V <sub>CC</sub> = 5.0V, V <sub>B</sub> = 0.5V, R <sub>L</sub> = 1.0kΩ				
		V <sub>CC</sub> = 5.0V, V <sub>B</sub> = 0.050V, R <sub>L</sub> = 1.0kΩ	MUN2230			
		V <sub>CC</sub> = 5.0V, V <sub>B</sub> = 0.25V, R <sub>L</sub> = 1.0kΩ	MUN2215 MUN2216 MUN2233 MUN2238			

NOTE2: Pulse Test: Pulse Width < 300μs, Duty Cycle < 2.0%.



T<sub>A</sub> = 25°C unless otherwise noted.

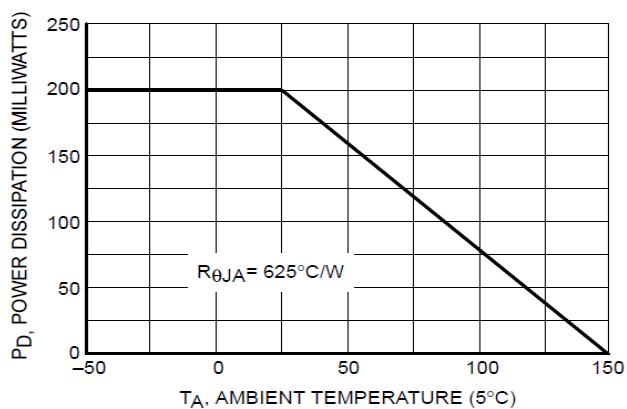
Parameter	Symbol	Characteristic	Min	Typ	Max	Unit	
ON CHARACTERISTICS <small>NOTE2</small>							
Input Resistor	R <sub>1</sub>	MUN2211	7.0	10	13	kΩ	
		MUN2212	15.4	22	28.6		
		MUN2213	32.9	47	61.1		
		MUN2214	7.0	10	13		
		MUN2215	7.0	10	13		
		MUN2216	3.3	4.7	6.1		
		MUN2230	0.7	1.0	1.3		
		MUN2231	1.5	2.2	2.9		
		MUN2232	3.3	4.7	6.1		
		MUN2233	3.3	4.7	6.1		
		MUN2234	15.4	22	28.6		
		MUN2235	1.54	2.2	2.86		
		MUN2238	1.54	2.2	2.88		
		MUN2241	70	100	130		
Resistor Ratio	R <sub>1</sub> /R <sub>2</sub>	MUN2211	0.8	1.0	1.2	kΩ	
		MUN2212					
		MUN2213					
		MUN2214	0.17	0.21	0.25		
		MUN2215	-	-	-		
		MUN2216					
		MUN2238					
		MUN2241	-	-	-		
		MUN2230	0.8	1.0	1.2		
		MUN2231					
		MUN2232					
		MUN2233	0.055	0.1	0.185		
		MUN2234	0.38	0.47	0.56		
		MUN2235	0.038	0.047	0.056		

NOTE3: Pulse Test: Pulse Width < 300μs, Duty Cycle < 2.0%.

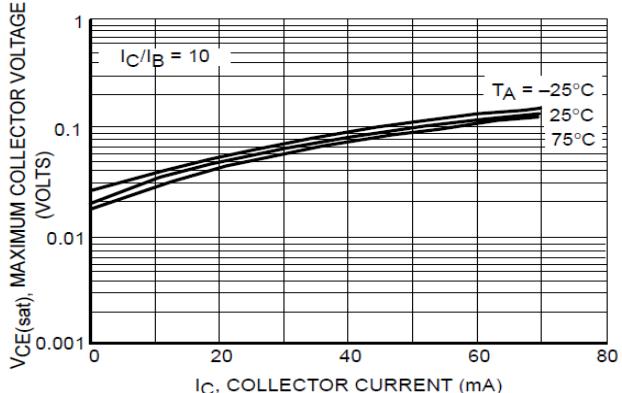


## TYPICAL PERFORMANCE CHARACTERISTICS

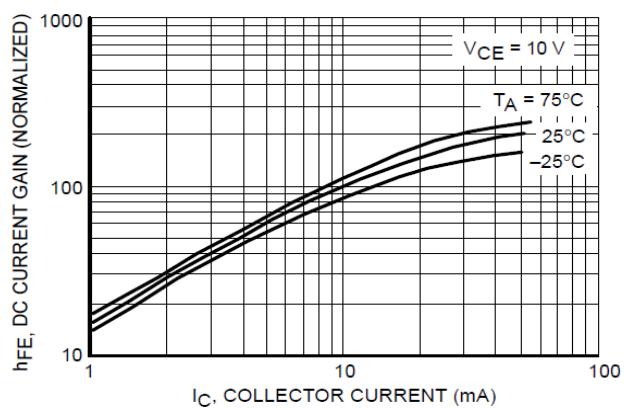
### 1. Derating Curve



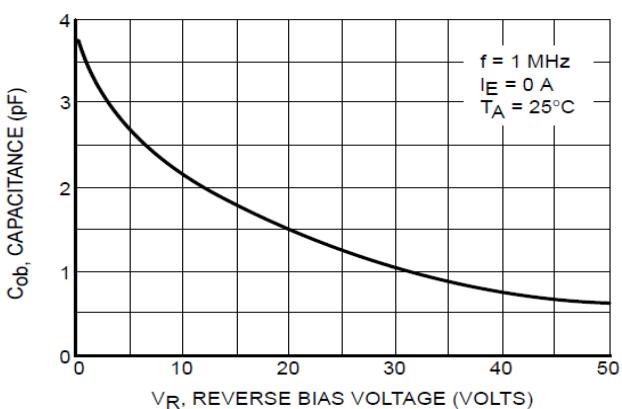
### 2. $V_{CE(\text{sat})}$ vs. $I_C$



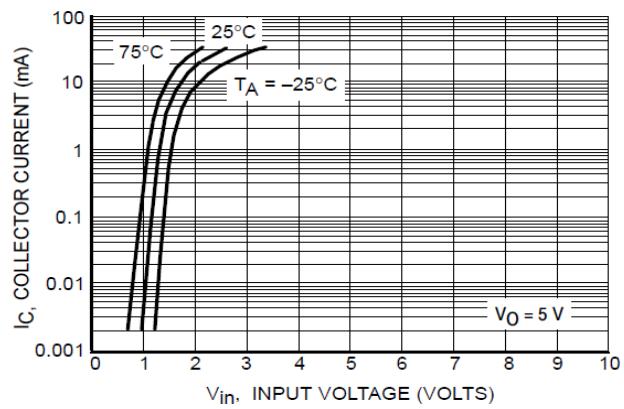
### 3. DC Current Gain



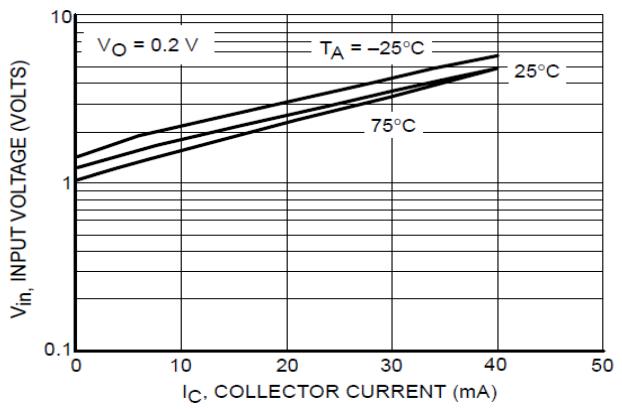
### 4. Output Capacitance



### 5. Output Current vs. Input Voltage



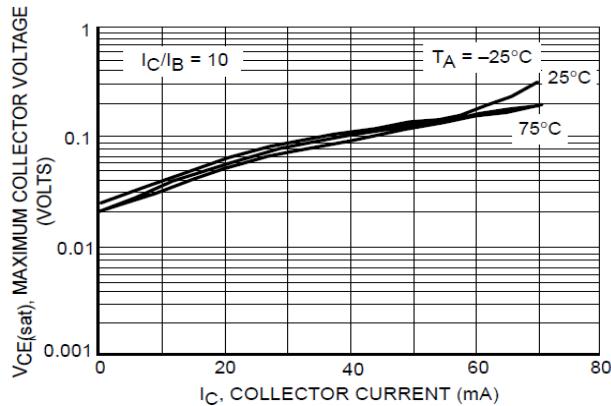
### 6. Input Voltage vs. Output Current



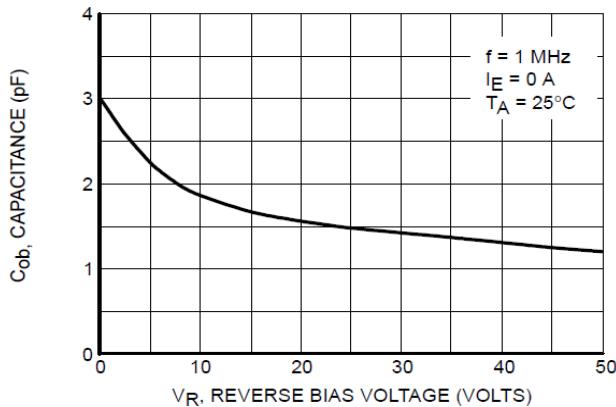


## MUN2212

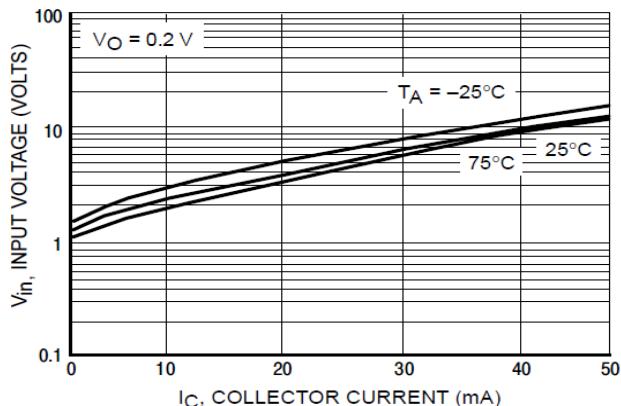
### 7. $V_{CE(sat)}$ vs. $I_C$



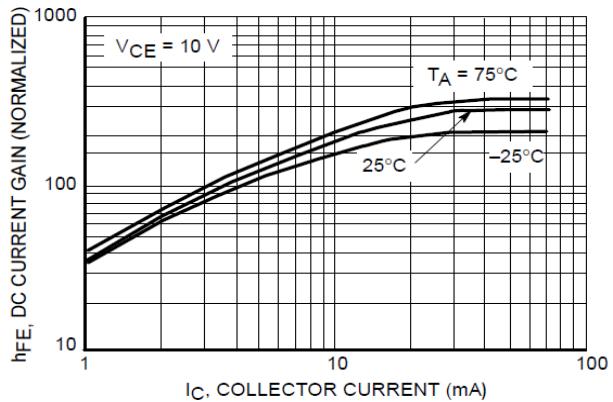
### 9. Output Capacitance



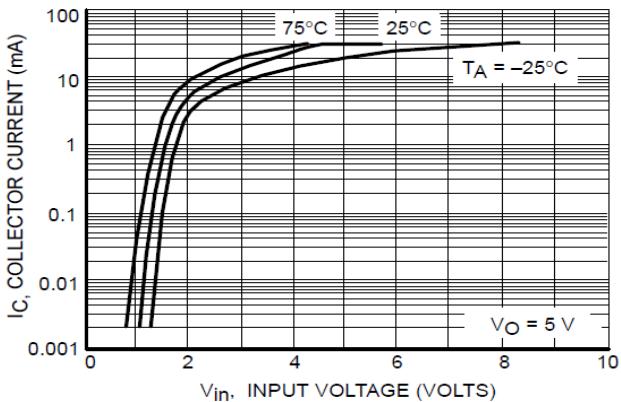
### 11. Input Voltage vs. Output Current



### 8. DC Current Gain



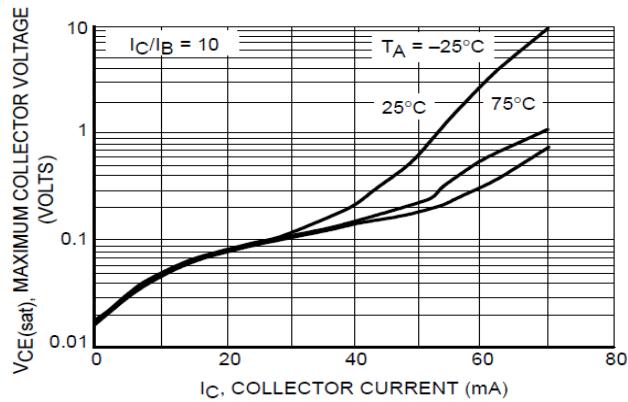
### 10. Output Current vs. Input Voltage



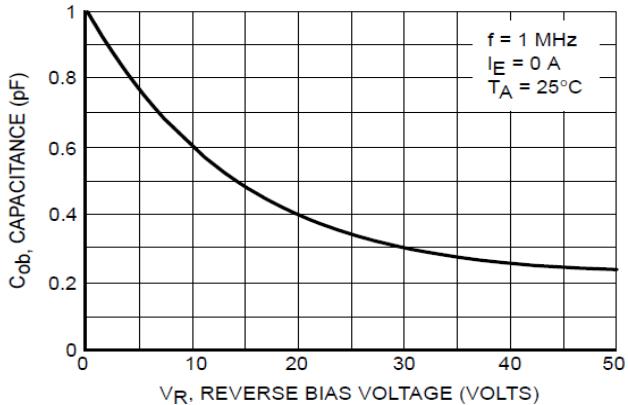


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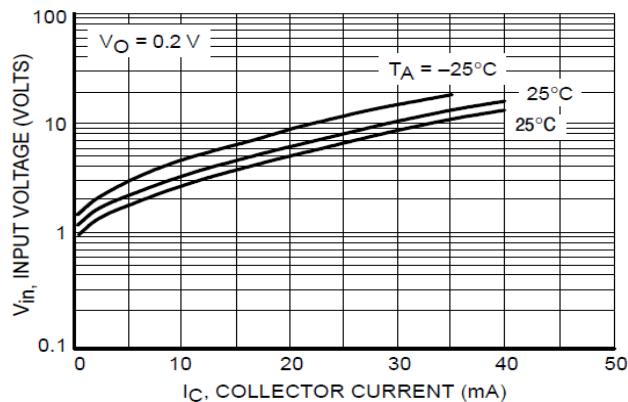
12.  $V_{CE(sat)}$  vs.  $I_C$



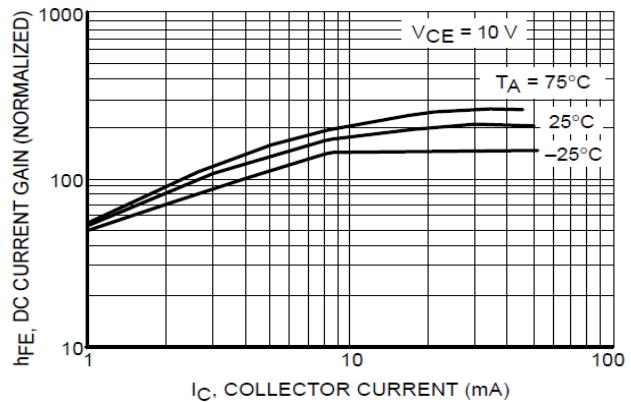
14. Output Capacitance



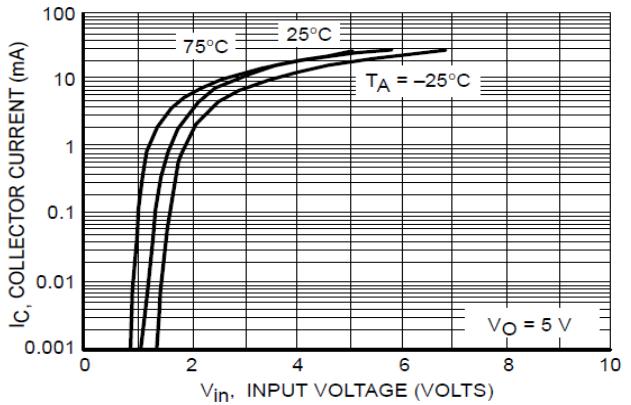
16. Input Voltage vs. Output Current



13. DC Current Gain



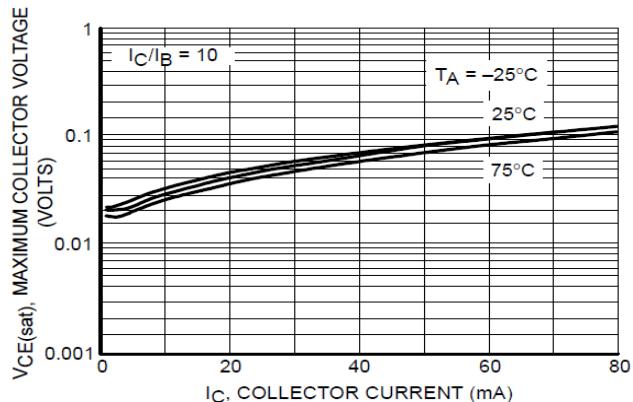
15. Output Current vs. Input Voltage



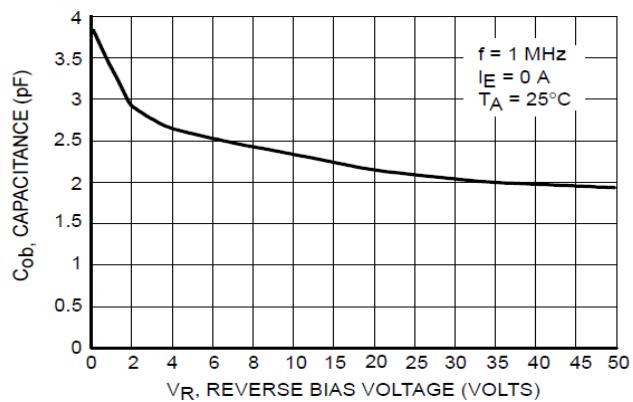


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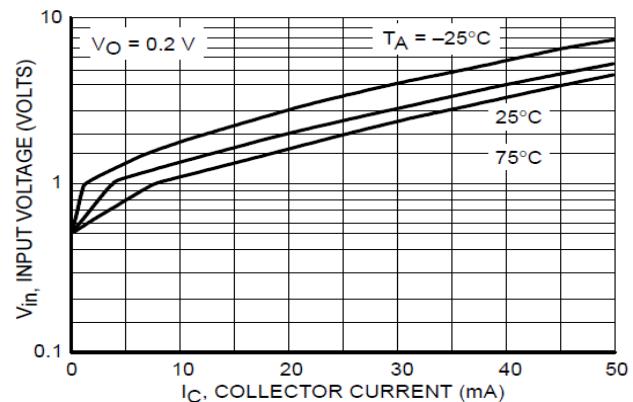
17.  $V_{CE(sat)}$  vs.  $I_C$



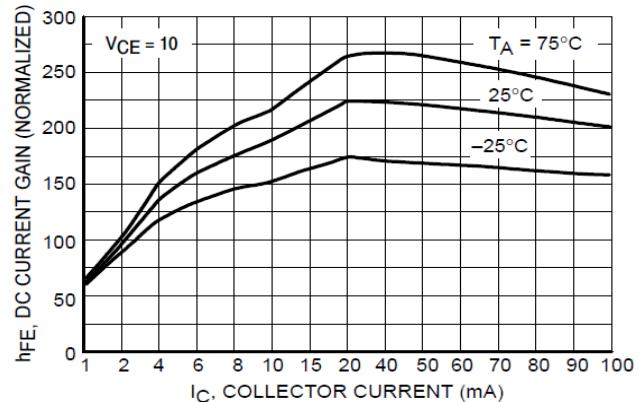
19. Output Capacitance



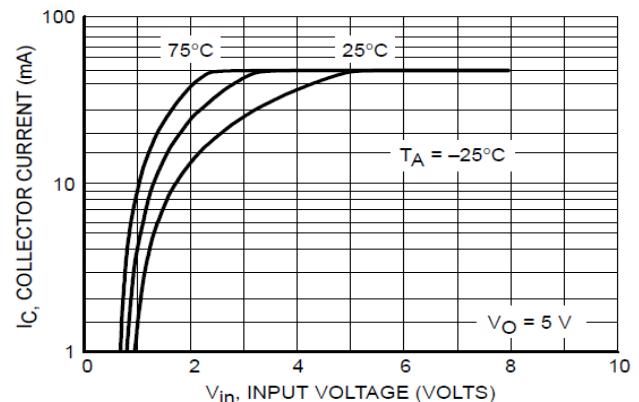
21. Input Voltage vs. Output Current



18. DC Current Gain



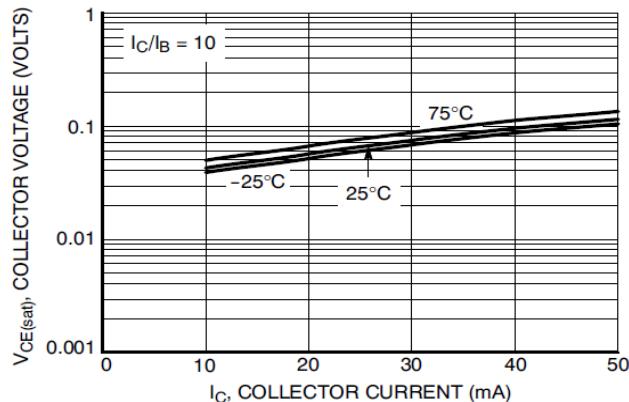
20. Output Current vs. Input Voltage



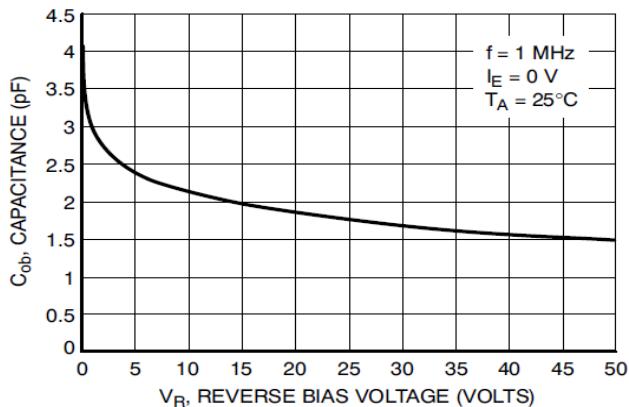


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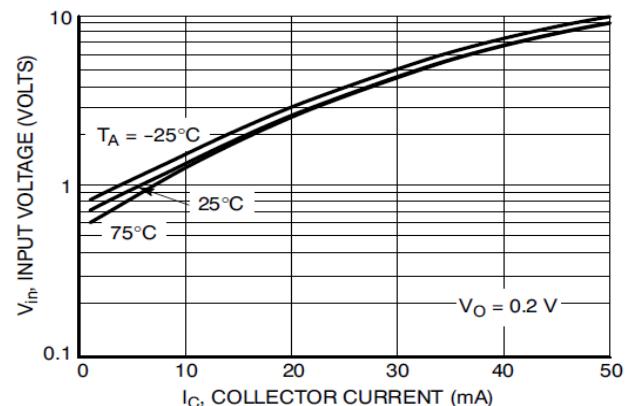
22.  $V_{CE(sat)}$  versus  $I_C$



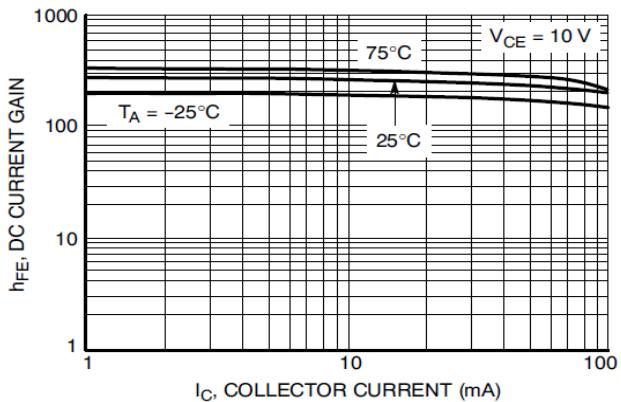
24. Output Capacitance



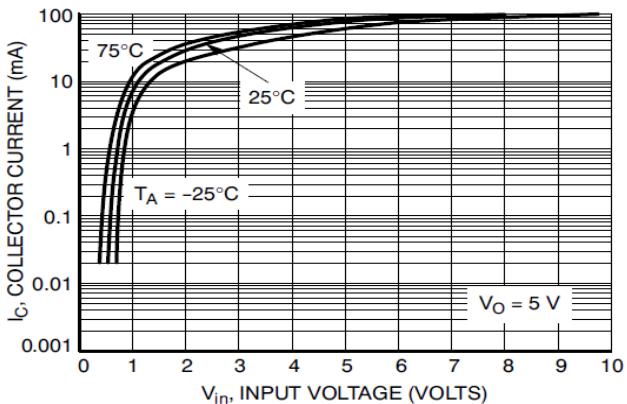
26. Input Voltage versus Output Current



23. DC Current Gain



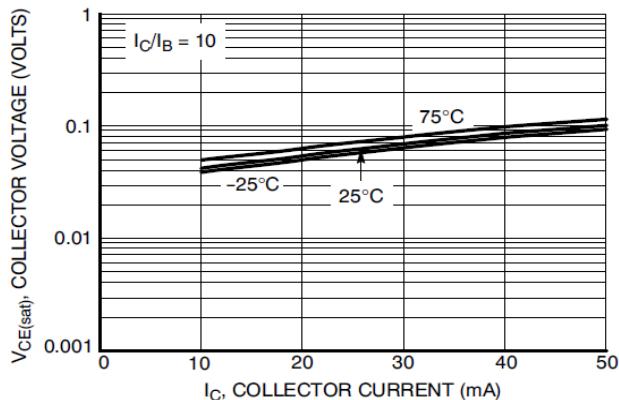
25. Output Current versus Input Voltage



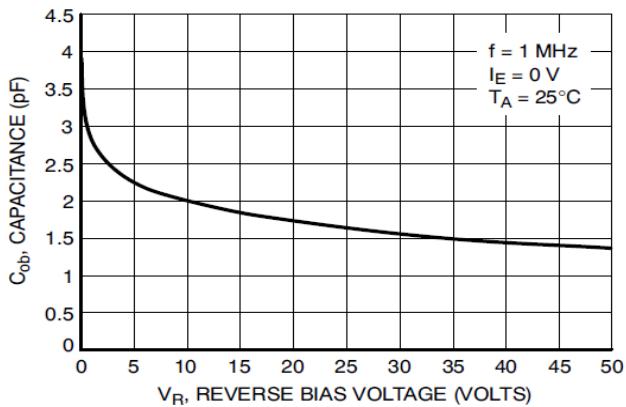


## MUN2216

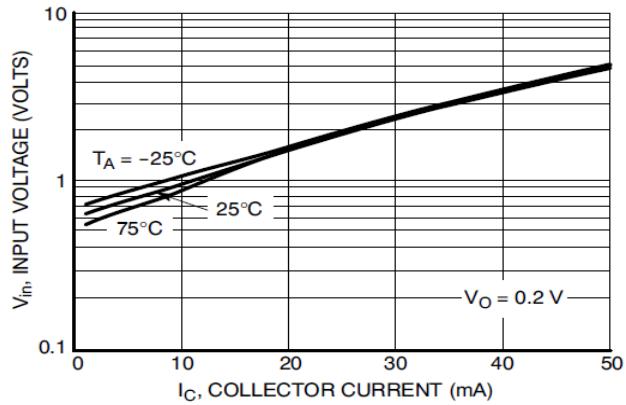
27.  $V_{CE(sat)}$  versus  $I_C$



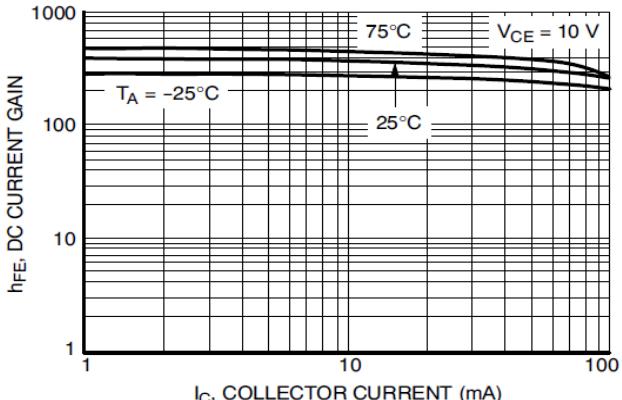
29. Output Capacitance



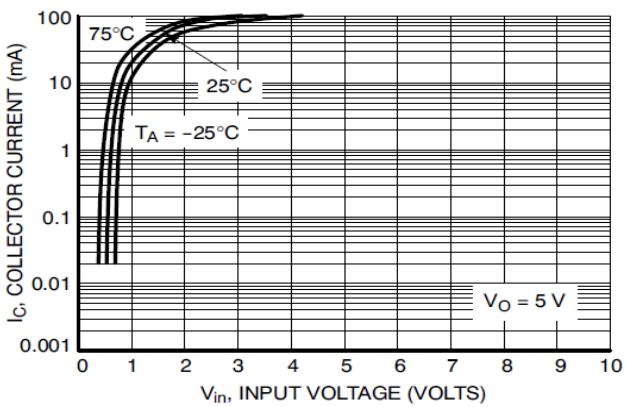
31. Input Voltage versus Output Current



28. DC Current Gain



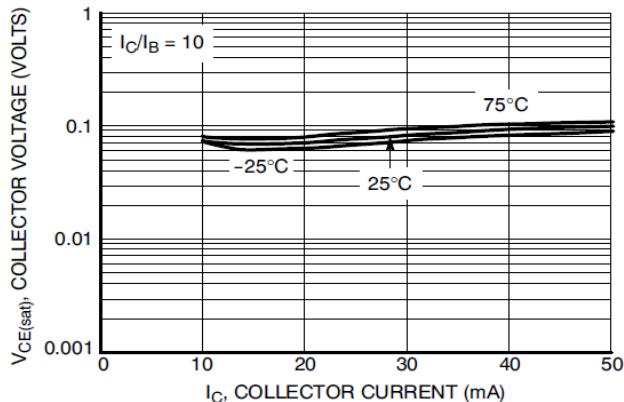
30. Output Current versus Input Voltage



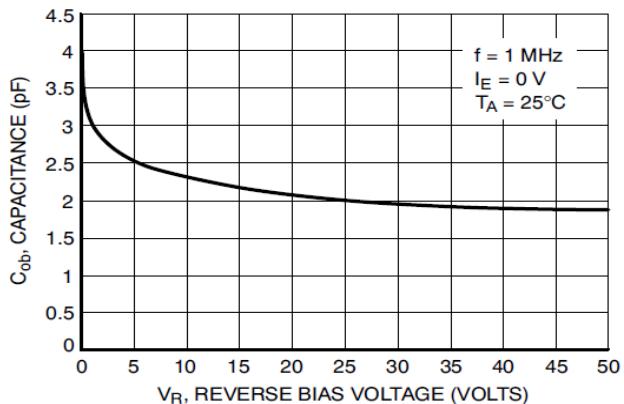


## MUN2230

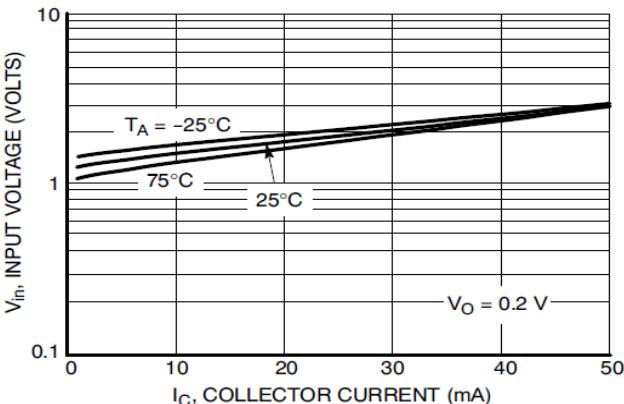
32.  $V_{CE(sat)}$  versus  $I_C$



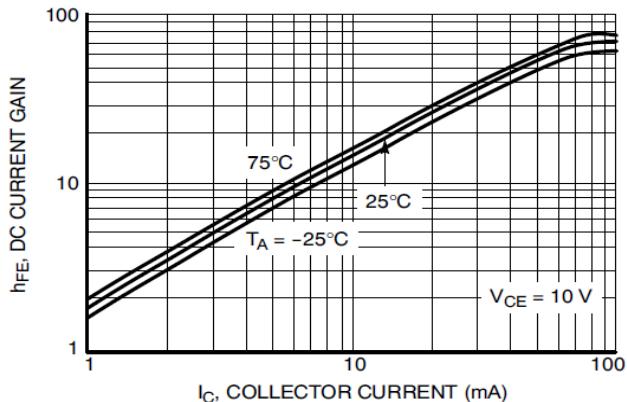
34. Output Capacitance



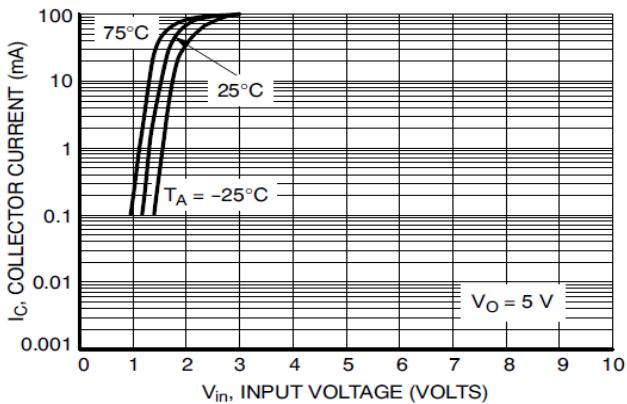
36. Input Voltage versus Output Current



33. DC Current Gain



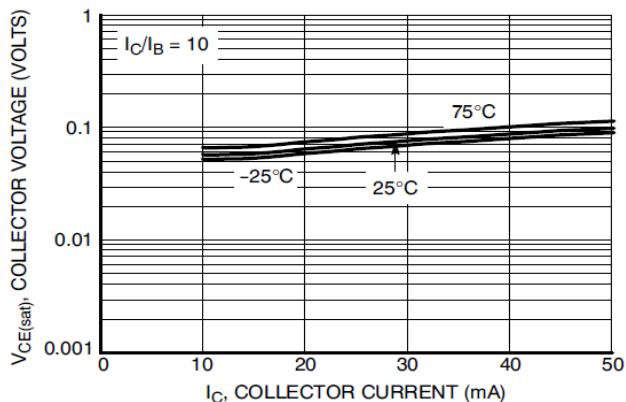
35. Output Current versus Input Voltage



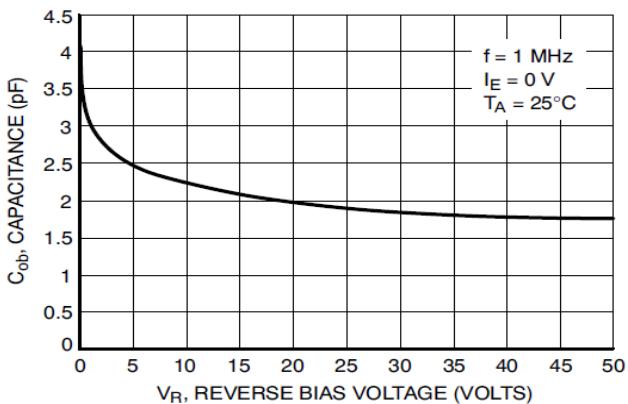


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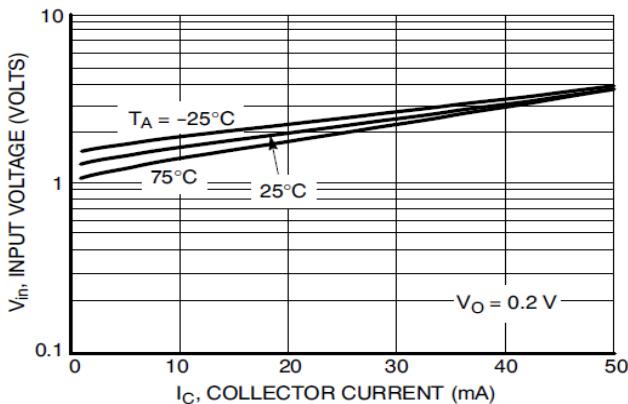
37.  $V_{CE(sat)}$  versus  $I_C$



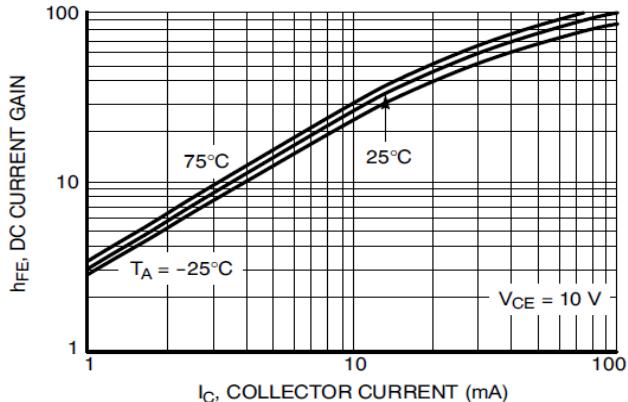
39. Output Capacitance



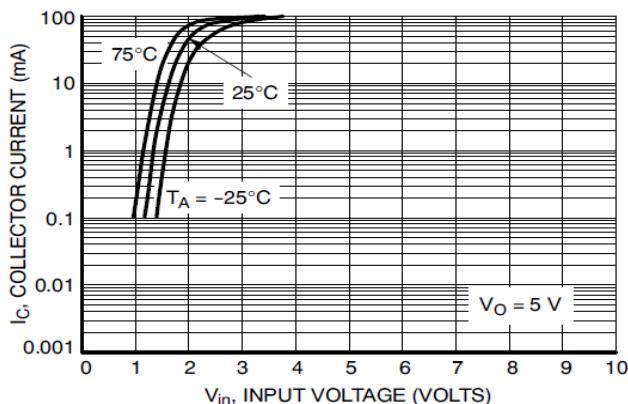
41. Input Voltage versus Output Current



38. DC Current Gain



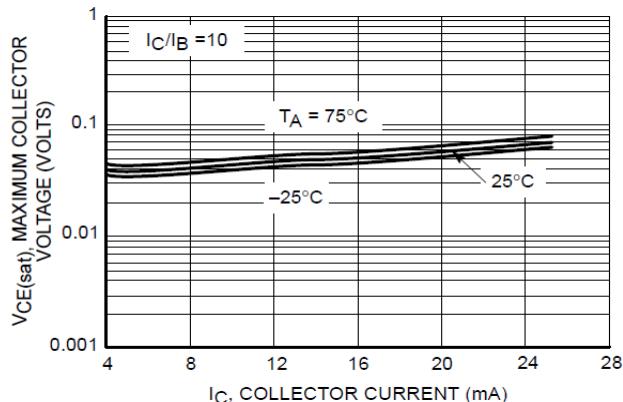
40. Output Current versus Input Voltage



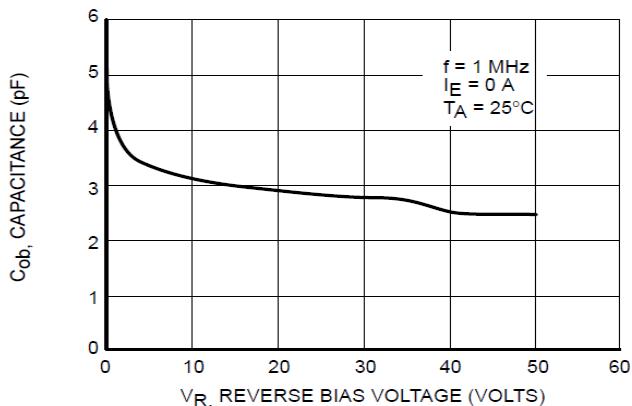


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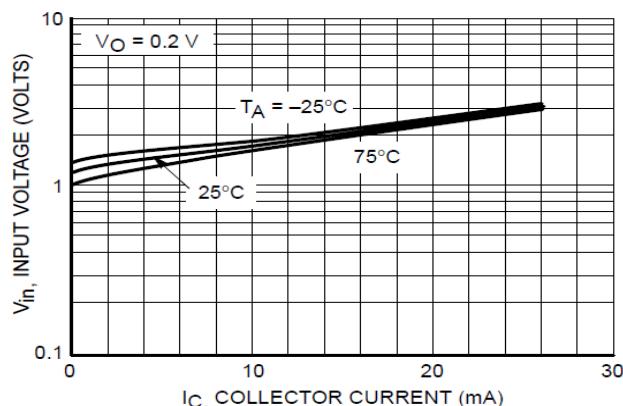
42.  $V_{CE(sat)}$  vs.  $I_C$



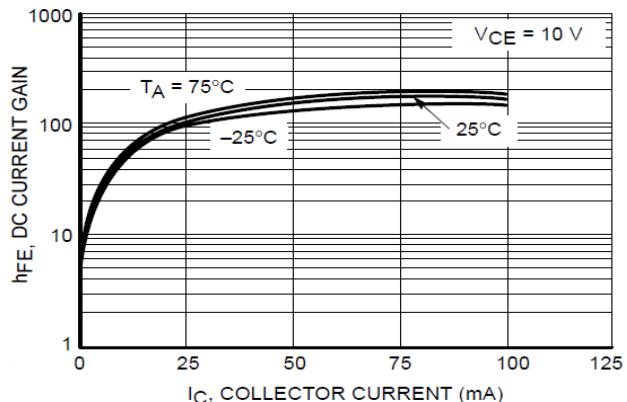
44. Output Capacitance



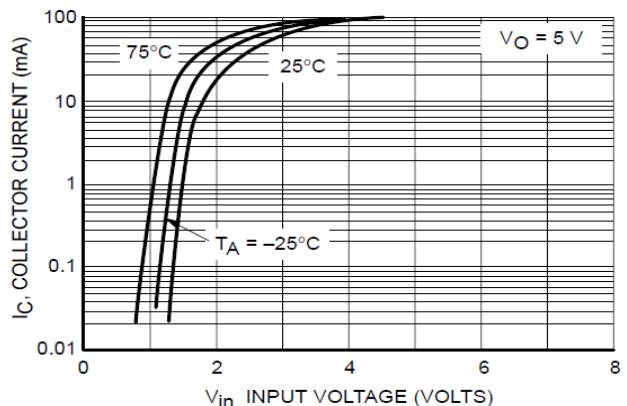
46. Output Voltage vs. Input Current



43. DC Current Gain



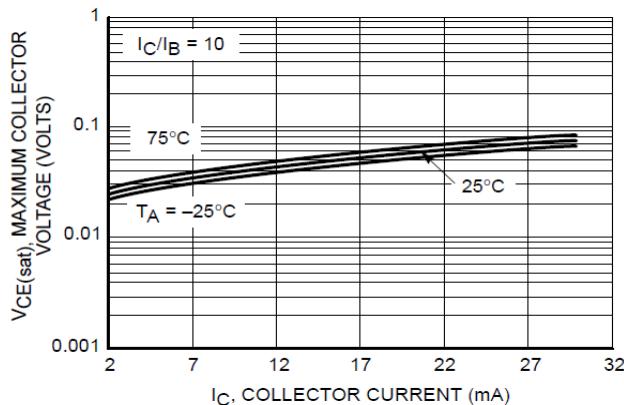
45. Output Current vs. Input Voltage



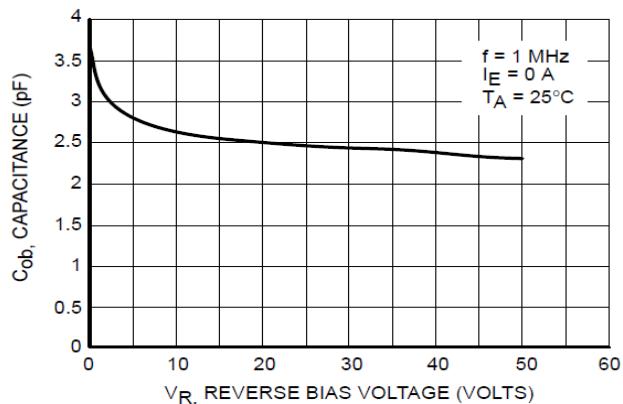


### MUN2233

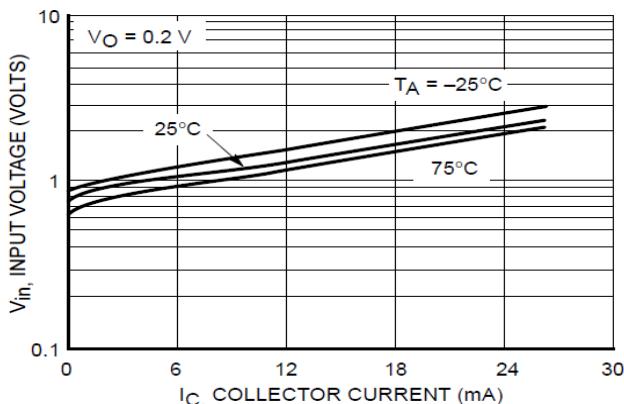
47.  $V_{CE(sat)}$  vs.  $I_C$



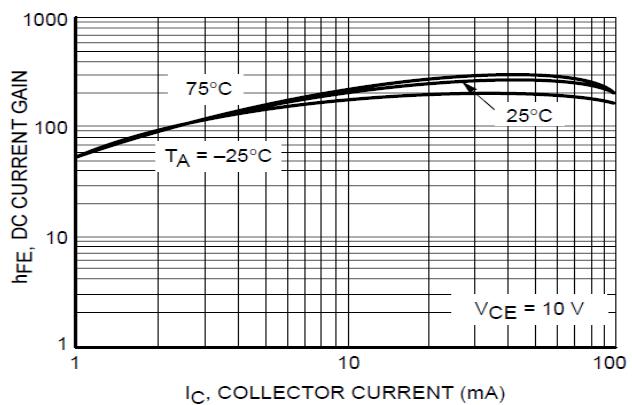
49. Figure 49. Output Capacitance



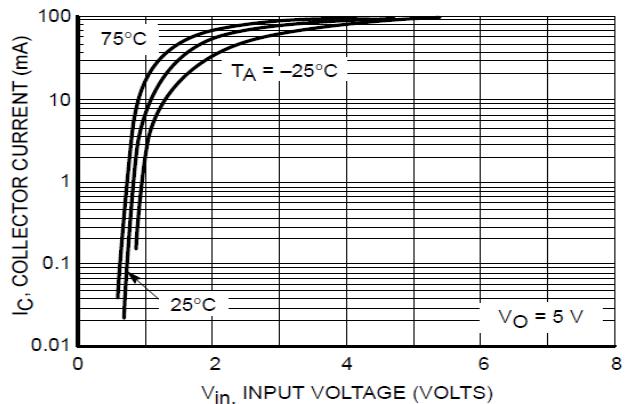
51. Input Voltage vs. Output Current



48. DC Current Gain



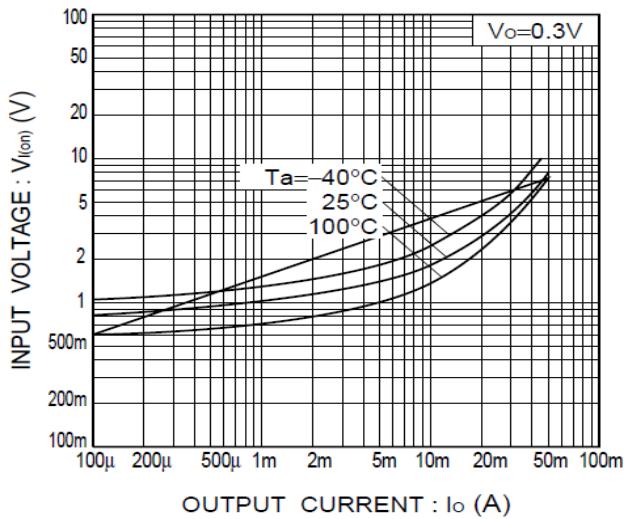
50. Figure 50. Output Current vs. Input Voltage



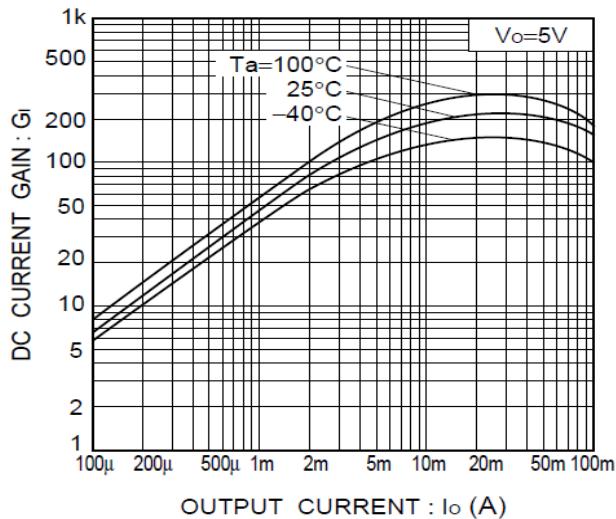


**MUN2234**

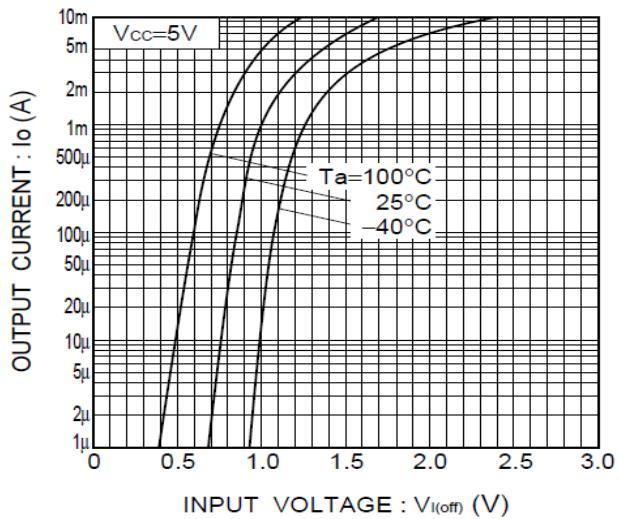
52. Input voltage vs. output current  
(ON characteristics)



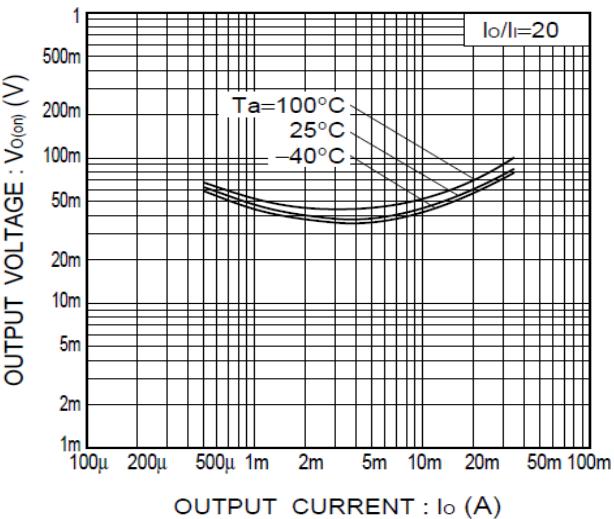
54. DC current gain vs. output current



53. Output current vs. input voltage  
(OFF characteristics)



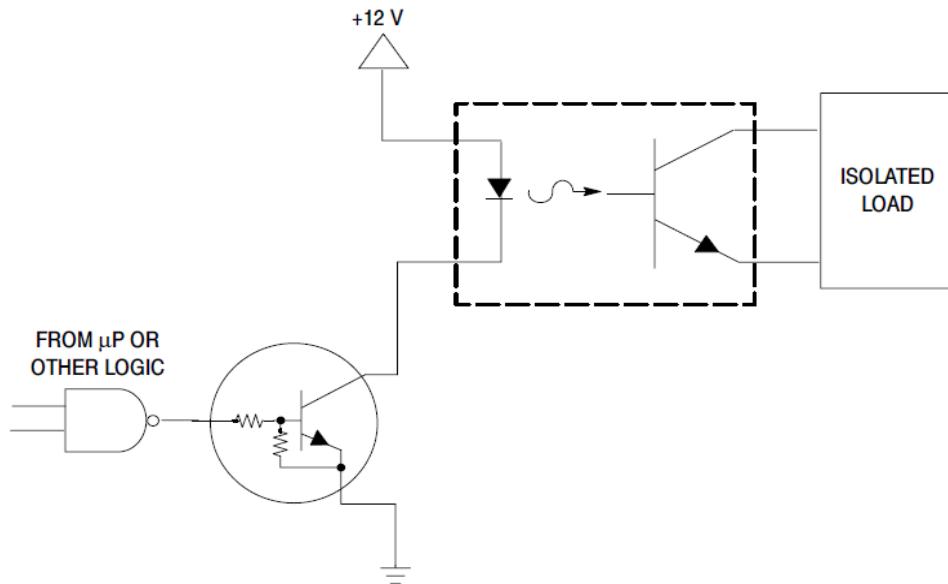
55. Output voltage vs. output current



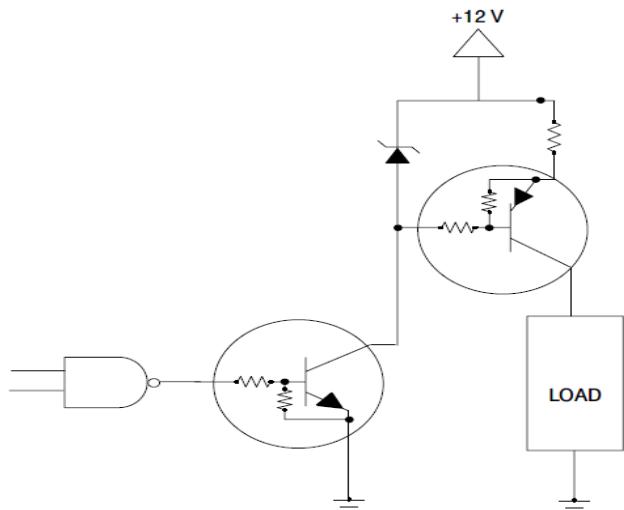
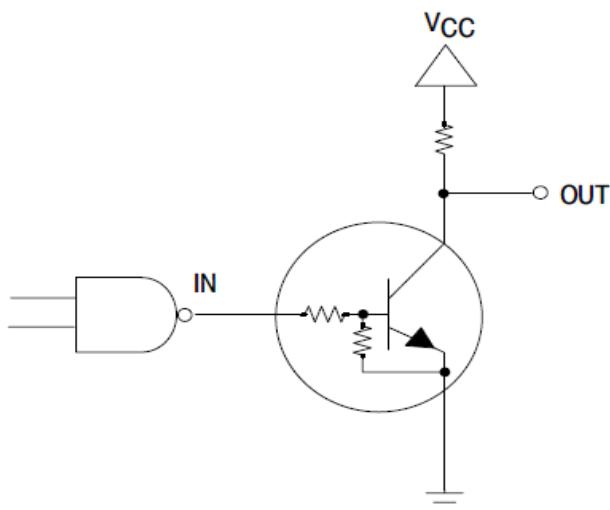


### NPN BRTs

56. Level Shifter: Connects 12 or 24 Volt Circuits to Logic



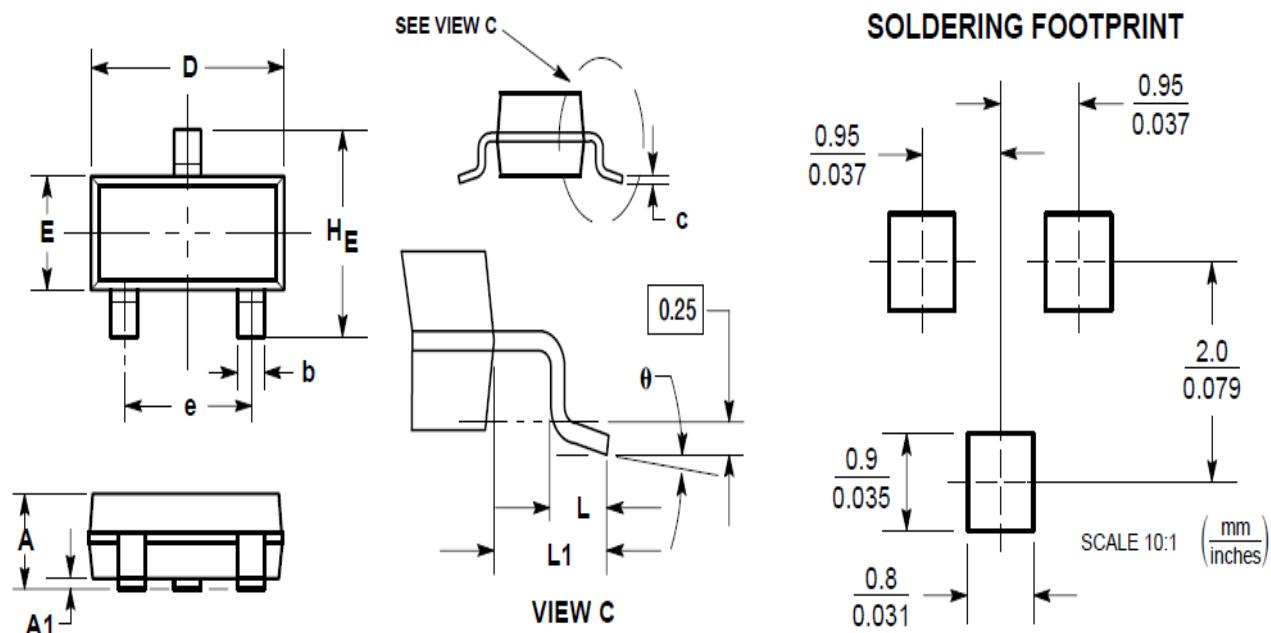
57. Open Collector Inverter: Inverts the Input Signal    58. Inexpensive, Unregulated Current Source





## PACKAGE INFORMATION

Dimension in SOT-23 Package (Unit: mm)



DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.035	0.044	0.89	1.11
A1	0.001	0.004	0.01	0.10
b	0.015	0.020	0.37	0.50
c	0.003	0.007	0.09	0.18
D	0.110	0.120	2.80	3.04
E	0.047	0.055	2.30	1.40
e	0.070	0.081	1.78	2.04
L	0.004	0.012	0.10	0.30
L1	0.014	0.029	0.35	0.69
H <sub>E</sub>	0.083	0.104	2.10	2.64



## IMPORTANT NOTICE

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