SWITCHMODE Power Rectifiers

MUR105, MUR110, MUR115, MUR120, MUR130, MUR140, MUR160

The MUR120 series of SWITCHMODE power rectifiers are designed for use in switching power supplies, inverters and as free wheeling diodes.

Features

- Ultrafast 25, 50 and 75 Nanosecond Recovery Times
- 175°C Operating Junction Temperature
- Low Forward Voltage
- Low Leakage Current
- High Temperature Glass Passivated Junction
- Reverse Voltage to 600 V
- Shipped in Plastic Bags; 1,000 per Bag
- Available Tape and Reel; 5,000 per Reel, by adding a "RL" Suffix to the Part Number
- These are Pb-Free Devices*

Mechanical Characteristics:

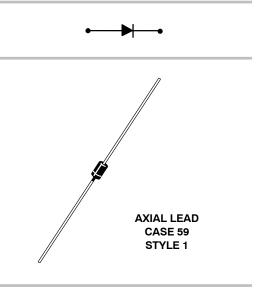
- Case: Epoxy, Molded
- Weight: 0.4 Gram (Approximately)
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead Temperature for Soldering Purposes: 260°C Max. for 10 Seconds
- Polarity: Cathode Indicated by Polarity Band



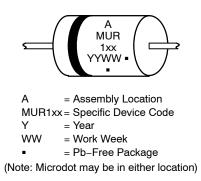
ON Semiconductor®

http://onsemi.com

ULTRAFAST RECTIFIERS 1.0 AMPERE, 50 – 600 VOLTS



MARKING DIAGRAM



ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 6 of this data sheet.

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

MAXIMUM RATINGS

		MUR							
Rating	Symbol	105	110	115	120	130	140	160	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _R	50	100	150	200	300	400	600	V
Average Rectified Forward Current (Square Wave Mounting Method #3 Per Note 2)	I _{F(AV)}	1.0 @ $T_A = 130^{\circ}C$ 1.0 @ $T_A = 120^{\circ}C$			20°C	A			
Nonrepetitive Peak Surge Current (Surge applied at rated load conditions, halfwave, single phase, 60 Hz)	I _{FSM}	35			A				
Operating Junction Temperature and Storage Temperature	T _J , T _{stg}	- 65 to +175				°C			

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

THERMAL CHARACTERISTICS

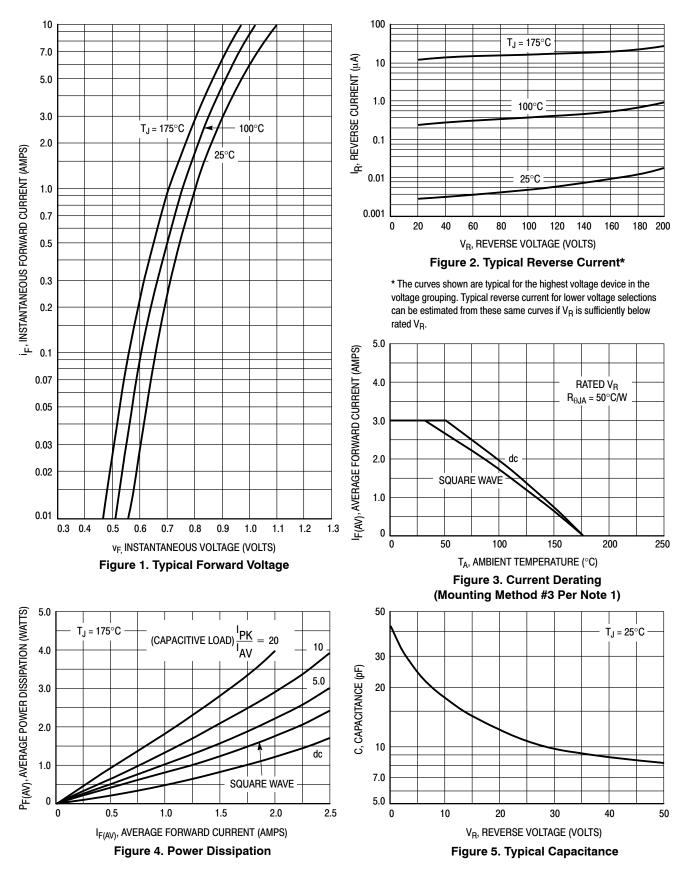
Characteristic	Symbol	Мах	Unit
Maximum Thermal Resistance, Junction-to-Ambient		Note 2	°C/W

ELECTRICAL CHARACTERISTICS

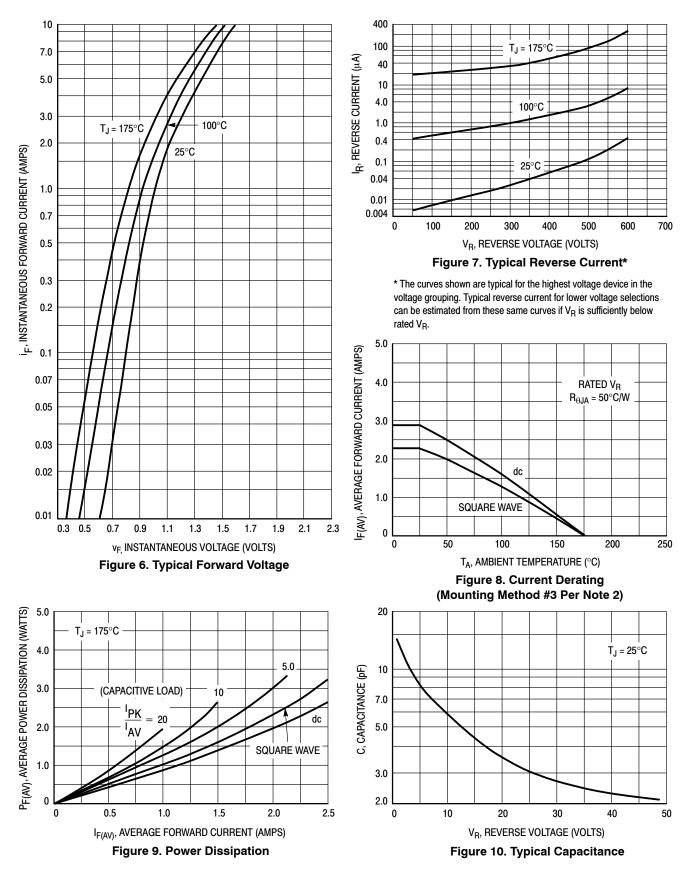
Characteristic	Symbol	Va	Unit	
Maximum Instantaneous Forward Voltage (Note 1) ($i_F = 1.0 \text{ Amp}, T_J = 150^{\circ}\text{C}$) ($i_F = 1.0 \text{ Amp}, T_J = 25^{\circ}\text{C}$)	VF	0.710 0.875	1.05 1.25	V
Maximum Instantaneous Reverse Current (Note 1) (Rated DC Voltage, $T_J = 150^{\circ}C$) (Rated DC Voltage, $T_J = 25^{\circ}C$)	İR	50 2.0	150 5.0	μΑ
Maximum Reverse Recovery Time (I _F = 1.0 A, di/dt = 50 A/ μ s) (I _F = 0.5 A, i _R = 1.0 A, I _{REC} = 0.25 A)	t _{rr}	35 25	75 50	ns
Maximum Forward Recovery Time ($I_F = 1.0 \text{ A}$, di/dt = 100 A/µs, I_{REC} to 1.0 V)	t _{fr}	25	50	ns
Typical Peak Reverse Recovery Current $(I_F = 1.0 \text{ A}, \text{ di/dt} = 50 \text{ A/}\mu\text{s})$	I _{RM}	0.85		A

1. Pulse Test: Pulse Width = 300 μ s, Duty Cycle \leq 2.0%.

MUR105, MUR110, MUR115, MUR120



MUR130, MUR140, MUR160



NOTE 2. — AMBIENT MOUNTING DATA

Data shown for thermal resistance, junction-to-ambient $(R_{\theta JA})$ for the mountings shown is to be used as typical guideline values for preliminary engineering or in case the tie point temperature cannot be measured.

TYPICAL VALUES FOR $\textbf{R}_{\theta \textbf{J} \textbf{A}}$ IN STILL AIR Lead Length, L (in.) Mounting Method Units 1/8 1/4 1/2 65 °C/W 1 52 72 2 67 80 87 °C/W $R_{\theta JA}$ 3 50 °C/W **MOUNTING METHOD 1** MOUNTING METHOD 2 STA VIIIIIIIIIII VIR **Vector Pin Mounting MOUNTING METHOD 3** L = 3/8" **Board Ground Plane** P.C. Board with 1-1/2" X 1-1/2" Copper Surface

ORDERING INFORMATION

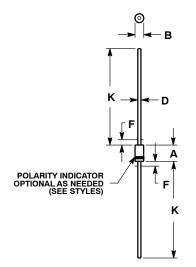
Device	Marking	Package	Shipping [†]
MUR105	MUR105	Axial Lead*	1000 Units / Bag
MUR105G	MUR105	Axial Lead*	1000 Units / Bag
MUR105RL	MUR105	Axial Lead*	5000 Units / Tape & Reel
MUR105RLG	MUR105	Axial Lead*	5000 Units / Tape & Reel
MUR110	MUR110	Axial Lead*	1000 Units / Bag
MUR110G	MUR110	Axial Lead*	1000 Units / Bag
MUR110RL	MUR110	Axial Lead*	5000 Units / Tape & Reel
MUR110RLG	MUR110	Axial Lead*	5000 Units / Tape & Reel
MUR115	MUR115	Axial Lead*	1000 Units / Bag
MUR115G	MUR115	Axial Lead*	1000 Units / Bag
MUR115RL	MUR115	Axial Lead*	5000 Units / Tape & Reel
MUR115RLG	MUR115	Axial Lead*	5000 Units / Tape & Reel
MUR120	MUR120	Axial Lead*	1000 Units / Bag
MUR120G	MUR120	Axial Lead*	1000 Units / Bag
MUR120RL	MUR120	Axial Lead*	5000 Units / Tape & Reel
MUR120RLG	MUR120	Axial Lead*	5000 Units / Tape & Reel
MUR130	MUR130	Axial Lead*	1000 Units / Bag
MUR130G	MUR130	Axial Lead*	1000 Units / Bag
MUR130RL	MUR130	Axial Lead*	5000 Units / Tape & Reel
MUR130RLG	MUR130	Axial Lead*	5000 Units / Tape & Reel
MUR140	MUR140	Axial Lead*	1000 Units / Bag
MUR140G	MUR140	Axial Lead*	1000 Units / Bag
MUR140RL	MUR140	Axial Lead*	5000 Units / Tape & Reel
MUR140RLG	MUR140	Axial Lead*	5000 Units / Tape & Reel
MUR160	MUR160	Axial Lead* 1000 Units / Ba	
MUR160G	MUR160	Axial Lead*	1000 Units / Bag
MUR160RL	MUR160	Axial Lead*	5000 Units / Tape & Reel
MUR160RLG	MUR160	Axial Lead*	5000 Units / Tape & Reel

This package is inherently Pb-Free.

PACKAGE DIMENSIONS

AXIAL LEAD CASE 59-10

ISSUE U



- NOTES: 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. CONTROLLING DIMENSION: INCH.
- 2. ALL RULES AND NOTES ASSOCIATED WITH JEDEC DO-41 OUTLINE SHALL APPLY З.
- POLARITY DENOTED BY CATHODE BAND. LEAD DIAMETER NOT CONTROLLED WITHIN F 5 DIMENSION.

	INCHES		MILLIM	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.161	0.205	4.10	5.20
В	0.079	0.106	2.00	2.70
D	0.028	0.034	0.71	0.86
F		0.050		1.27
ĸ	1.000		25.40	

STYLE 1: PIN 1. CATHODE (POLARITY BAND)

ON Semiconductor and 💷 are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC owns the rights to a number of patents, trademarks, ON Semiconductor and up are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of SCILLC's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any paticular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typical" must be validated for each customer application by customer's technical experts. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use sciented enders application in which the failure of the socient application berging and disclikutes however personal injury or death may occur. Should Buyer purchase concerver and the disclicute however parameters are application in which the failure of the product or used disclicutes however personal injury or death may occur. Should Buyer purchase or use scient of each owaver application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors hamless against all claims, costs, damages, and exponses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employeer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free USA/Canada Europe, Middle East and Africa Technical Support:

Phone: 421 33 790 2910 Japan Customer Focus Center Phone: 81-3-5817-1050

ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative