
MY84SPK02M2-V1.0
Bluetooth 3.0+EDR 立体声音频模组

一、 产品简介

MY84SPK02M02 V1.0蓝牙模组主芯片采用创杰的IS1684S设计。具有集成度高，体积小，低功耗，传输速度快等特点，只需在模组外围加上少许的元件，就可以实现高品质立体声音频流的无线接收，并能对音源播放器实现远程控制。

MY84SPK02M02 V1.0蓝牙立体声模组向用户提供远程控制按键接口、工作状态指示接口、立体声音频输出接口、AUX 输入接口。

二、 产品特点

- 1、遵循 Bluetooth 3.0+EDR 规范
- 2、Typical +2dBm Class 2 输出功率
- 3、Audio DAC: 94dB SNR
- 4、内置 Max. 350mAH 锂电池充电电路
- 5、支持 HSP, HFP, A2DP, AVRCP profile
- 6、SBC decode for Bluetooth audio streaming
- 7、有 Line in 功能
- 8、具有回音消除功能模组
- 9、EQ 调整
- 10、 尺寸: 15mm x29mm

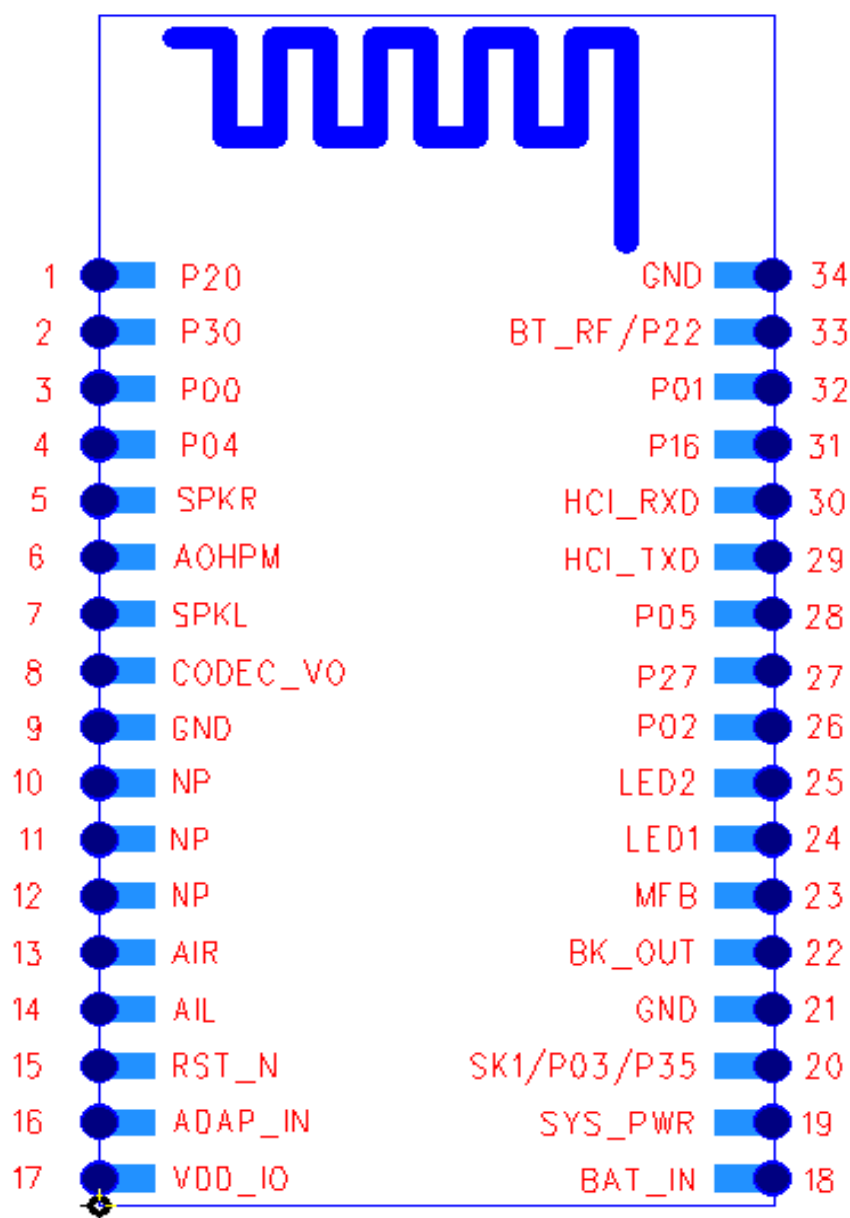
三、 产品应用领域

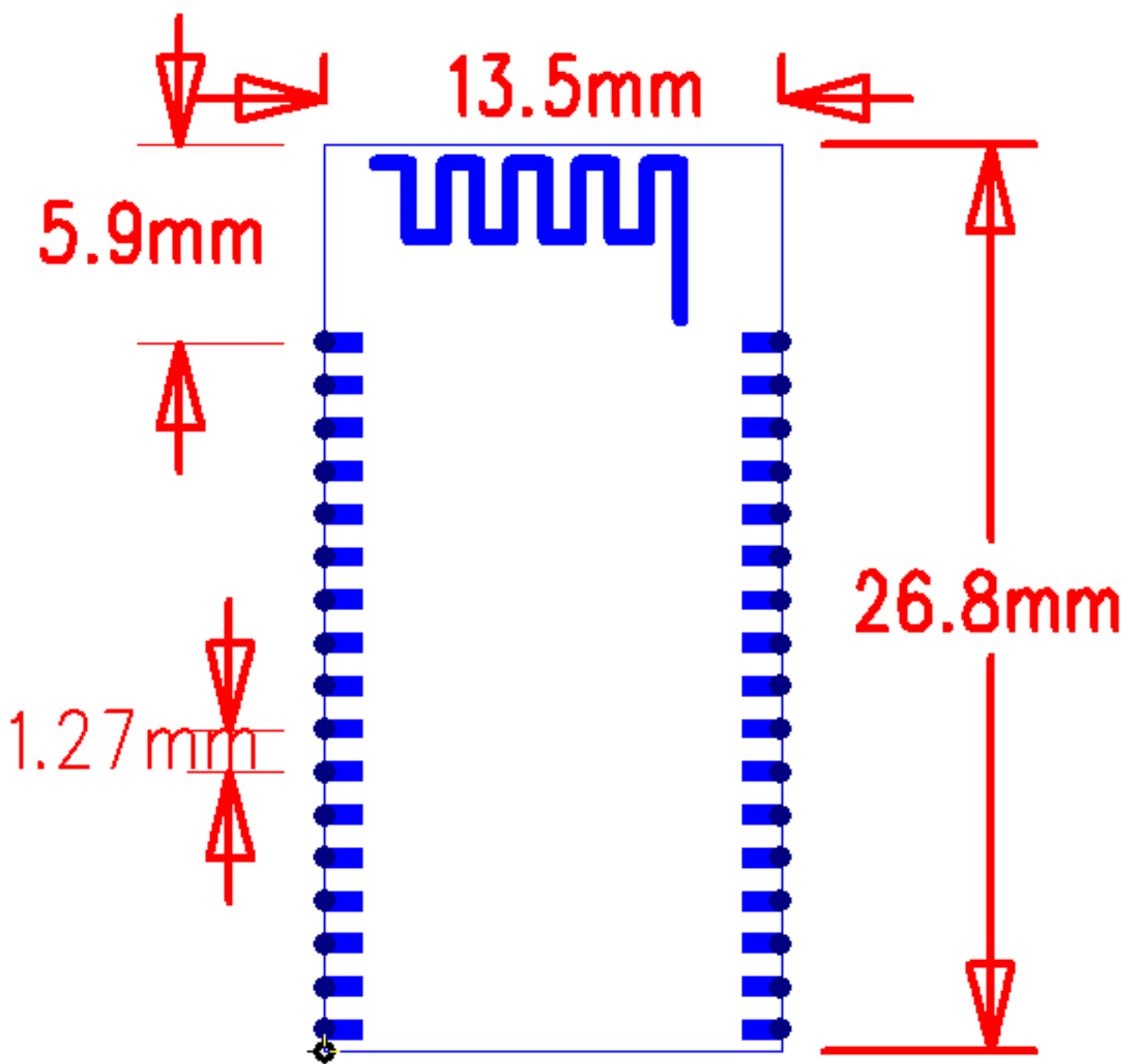
各类高品质蓝牙立体声耳机

各类高品质蓝牙立体声音箱、音响。

各类高品质蓝牙无线立体声音频接收设备。

四、 引脚定义及封装尺寸





Pin No.	I/O	Name	Descriptio
1	I/O	P20	GPIO, default pull-high input System Configuration, H: Application L: Baseband(IBDK Mode)
2	P	P30	GPIO, default pull-high input Line-in detection, 1: no line-in detected; 0: line-in detected
3	I/O	P00	GPIO, default pull-low input. Slide Switch Detector
4	I/O	P04	GPIO, default pull-high input Audio AMP Enable/NFC detect
5	AO	SPKR	R-channel analog headphone output, single-ended application only
6	AP	AOHPM	NC for single-end application, Comm for differential application
7	AO	SPKL	L-channel analog headphone output, single-ended or differential set by software
8	P	CODEC_VO	3.1V LDO output for CODEC power Reserve for externa cap to fine tune audio frequency response
9	P	GND	Digital ground
10		NP	
11		NP	
12		NP	
13	AI	AIR	Stereo analog line in, R-channel
14	AI	AIL	S in, L-channel
15	I/O	RST_N	System Reset Pin

16	P	ADAP_IN	Power adaptor input
17		VDD_IO	VDD_IO pin, for calibration only Do not add external power to this pin
18	P	BAT_IN	Battery input
19	P	SYS_PW	System Power Output
20	I/O	SK1/P03/P35	Default SAR input for battery detection This pin can be re-defined as GPIO P03 This pin can be as GPIO P35 Through the resistor on the module configuration
21	P	GND	Digital ground
22	P	BK_OUT	Buck feedback sense pin
23	P	MFB	Multi-Function Push Button key Combined Play/Pause key when A2DP enabled.
24	P	LED1	LED Driver 1
25	P	LED2	LED Driver 2
26	I/O	P02	GPIO, default pull-high input PLAY/PAUSE button
27	I/O	P27	GPIO, default pull-high input Foward button
28	I/O	P05	GPIO, default pull-high input REW button
29	0	HCI_TXD	HCI TX data
30	I	HXI_RXD	HCI RX data
31	I/O	P16	GPIO, default pull-high input Volumn down button
32	I/O	P01	GPIO, default pull-high input Volumn up button

33		BT_RF/	NC for on board PCB antenna Antenna matching if an external antenna is used GPIO, default pull-low input. External LDO enable Through the resistor on the module configuration
34	P	GND	Digital ground