## Phase Control Thyristor Stud Types N0416S#020 to N0416S#080

The data sheet on the subsequent pages of this document is a scanned copy of existing data for this product. (Rating Report 93NR1 Issue 1)

This data reflects the old part number for this product which is: N275PH02-08. This part number must <u>NOT</u> be used for ordering purposes – please use the ordering particulars detailed below.

> The limitations of this data are as follows: Only SC outline drawing (W18) in datasheet No reverse recovery information available

The following links will direct you to the appropriate outline drawings <u>Outline W18</u> – ¾" Ceramic stud <u>Outline W25</u> – ¾" Ceramic stud removed

Where any information on the product matrix page differs from that in the following data, the product matrix must be considered correct

An electronic data sheet for this product is presently in preparation.

For further information on this product, please contact your local ASM or distributor.

Alternatively, please contact Westcode as detailed below.

Ordering Particulars			
N0416	S#	<b>*</b>	0
Fixed Type Code	SC – $\frac{3}{4}$ " Ceramic stud SD – $\frac{3}{4}$ " Ceramic stud removed	Voltage code V <sub>RRM</sub> /100 02-08	Fixed Code

Typical Order Code: N0416SC080, 3/4" Ceramic stud, 800V V<sub>RRM</sub>/V<sub>DRM</sub>

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FSTCODE

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In the interest of product improvement, Westcode reserves the right to change specifications at any time without prior notice.

Devices with a suffix code (2-letter, 3-letter or letter/digit/letter combination) added to their generic code are not necessarily subject to the conditions and limits contained in this report.

QUALITY AND EVALUATION LA Rating Report: 03701	Page Issue 1
Rating Report: 93TR1	DUKATURY
Origin: Q.E.L.	Date: 28th June, 1993
F	Pages: 12
M. Baker M. Baker	8
Written by: M. Baker Checked: BK	Approved:
RATINGS	4~
Voltage Grades	
V <sub>DSM</sub>	: 02-08
v <sub>RSM</sub>	: 200-800V
	: 300-900V
V <sub>DRM</sub> , V <sub>RRM</sub>	: 200-800V
$I_{T(AV)}$ : Single phase; 50 Hz, 180° sinewave	
$T_{CASE} = 94^{\circ}C$	: 226A
I <sub>T(rms)</sub> Max.	
I <sub>T d.c.</sub> Max.	: 355A
$I_{TSM}$ : t = 10 ms half sinewave; $T_{J(initial)} = 125^{\circ}C;$ $V_{DM} = 0.6 V$ (Markov	: 355A
	: 6000A
$I_{TSM}$ : t = 10 ms half sinewave; $T_{J(initial)}$ = 125°C, $V_{RM} \leq 10V$	: 6600A
$I^{2}t$ : t = 10 ms; $T_{J(initial)} = 125^{\circ}C$ ; $V_{RM} = 0.6V_{RRM}$ (MAX) $I^{2}t$ : t = 10 mg, m	() : $180 \times 10^3 A^2 secs$
$T_{J(\text{initial})} = 125^{\circ}\text{C}; V_{\text{RM}} \leq 10V$	: 218 x 10 <sup>3</sup> A <sup>2</sup> SECS
$I^2t$ : t = 3 ms; $T_{J(initial)} = 125^{\circ}C$ ; $V_{RM} \leq 10V$	: 165 x $10^3 A^2 SECS$
di/dt : (Repetitive) T <sub>J</sub> = 125°C; Gate: 20V, 20ohms	
Rise time $1\mu$	S : 500A/µs
FGM · Anode positive with respect to cathode	: 19A
V <sub>FGM</sub> : Anode positive with respect to cathode <sup>V</sup> RGM :	: 18V
P <sub>G</sub> (AV):	≎ 5V
	: 2W
P <sub>GM</sub> :	: 100w
V <sub>GD</sub> :	
T <sub>C</sub> operating range	: 0.25v
T <sub>stg</sub> Non operating	: -40°C to 125°C
	: -40°C to 150°C

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CHARACTERISTICS (maximum values unless stated of	Page Issue	1
$I_{GT}$ : $T_J = 25^{\circ}C$ )	otherwise)	
$I_{H}$ : $T_{J} = 25^{\circ}C$ ) $V_{A} = 6V; I_{A} = 1A$	: 1	50mA
$v_{GT}$ : $T_J = 25^{\circ}C$	: 40	DOmA
	: 31	7
$V_{\rm O}$ : $T_{\rm J} = 125^{\circ}{\rm C}$	: 0.	857
		001

 $V_{\rm O}$  :  $T_{\rm J} = 125^{\circ}{\rm C}$  $r_{T}$ :  $T_{J} = 125^{\circ}C$ : 0.535mΩ А \$  $T_J = 25^{\circ}C$ : в :  $T_{J} = 25^{\circ}C$ : С :  $T_I = 25^{\circ}C$ : D :  $T_J = 25^{\circ}C$ : : (Constant)  $T_J = 125^{\circ}C$ Α : : (B x ln (i))  $T_J = 125^{\circ}C$ В : : (C x i)  $T_J = 125^{\circ}C$ С : : (D x  $\sqrt{1}$ ) T<sub>J</sub> = 125°C D :  $V_{TM}$  :  $I_{TM}$  = 690A.  $T_J$  = 125°C R<sub>th(J-C)</sub> : 1.22V : 0.12 K/W Rth(C-HS) : 0.04 K/W dv/dt : Linear ramp to  $0.8V_{DRM}(max)$ ;  $T_J = 125^{\circ}C$ ; Gate O/C repetitive : 200V/µs\*  $I_{DRM}$  :  $T_J = 125^{\circ}C$  ;  $V_{DM} = V_{DRM}(max)$ : 20mA  $I_{RRM}$  :  $T_J = 125^{\circ}C$  ;  $V_{RM} = V_{RRM}(max)$ : 20mA  $Q_{RA}$ : I<sub>TM</sub> = : dI/dt = A/ $\mu$ S, 50% chord value  $V_{RM}^{TM} = 50V; T_J = 125^{\circ}C$ :  $t_q$  :  $I_{TM} =$ :dI/dt = $A/\mu S; T_J = 125^{\circ}C; V_{RM} = 50V$  $dv/dt = 200V/\mu s$  to 0.8 V<sub>DRM</sub>  $dv/dt = 20V/\mu s$  to 0.8 VDRM : : Mounting Torque : 2.50 - 2.77 Kgm Outline Drawing : 101A225 Outline (JEDEC No.) : Extension of Turn-off Time

This Report is applicable to other tq/reapplied dv/dt combinations when supply has been agreed by Sales/Production. \*<u>Repetitive\_dv/dt</u>

Higher dv/dt selections are available up to 1000V/ $\mu$ S on request.

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## Voltage Ratings

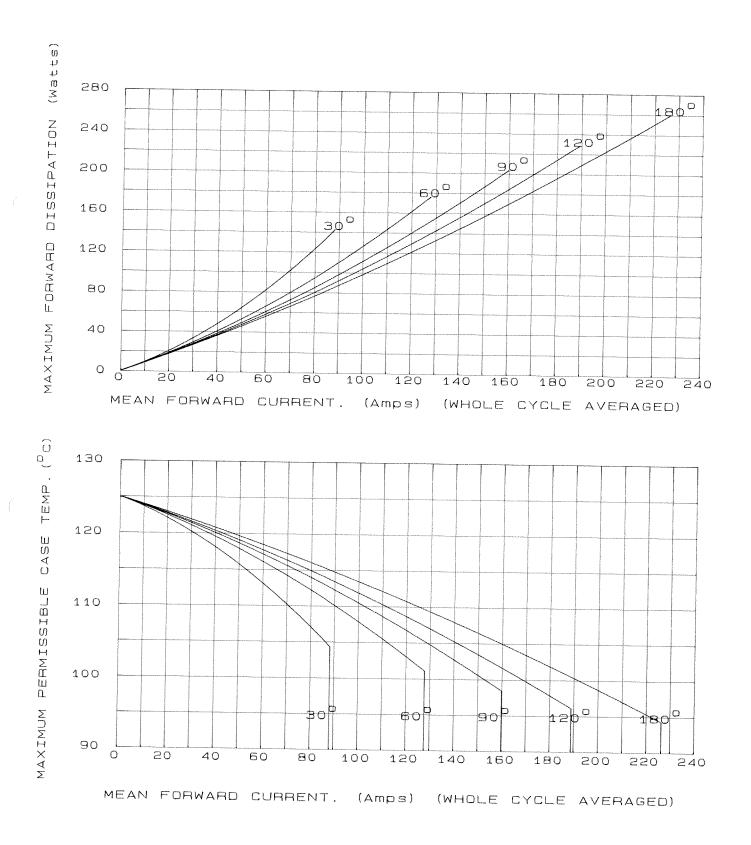
Voltage Grade 'H'	V <sub>DSM</sub> V <sub>DRM</sub> V <sub>RRM</sub> V	V <sub>RSM</sub> V	V <sub>D</sub> V <sub>R</sub> DC
02	200	300	140
03	300	400	210
04	400	500	260
06	600		
	600	700	420
08	800	900	560

## Extension of Voltage Grades

This report is applicable to other and higher voltage grades when supply has been agreed by Sales/Production.

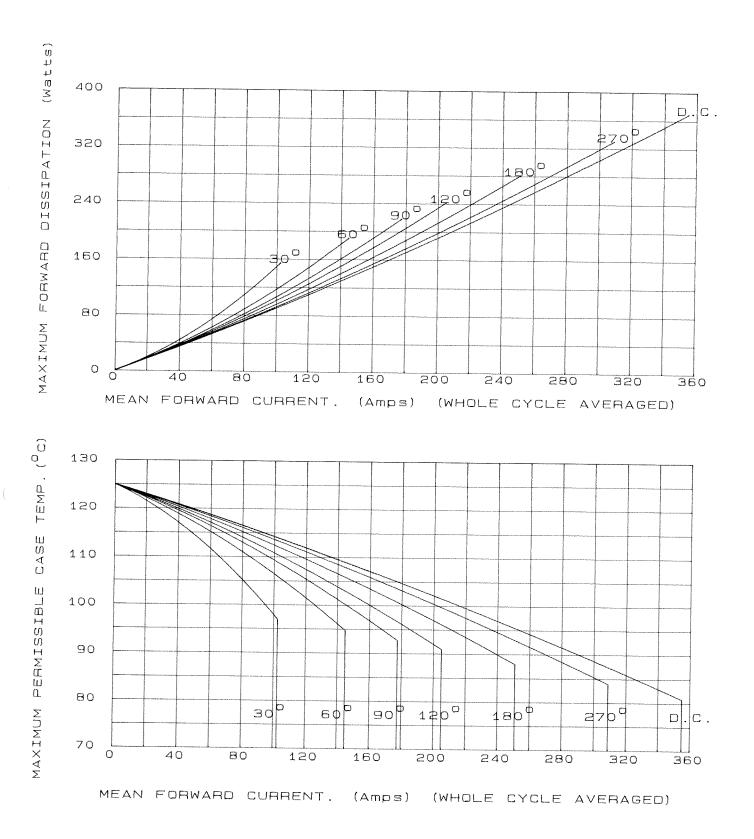
Page			 -		93TF	1
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SINE WAVE

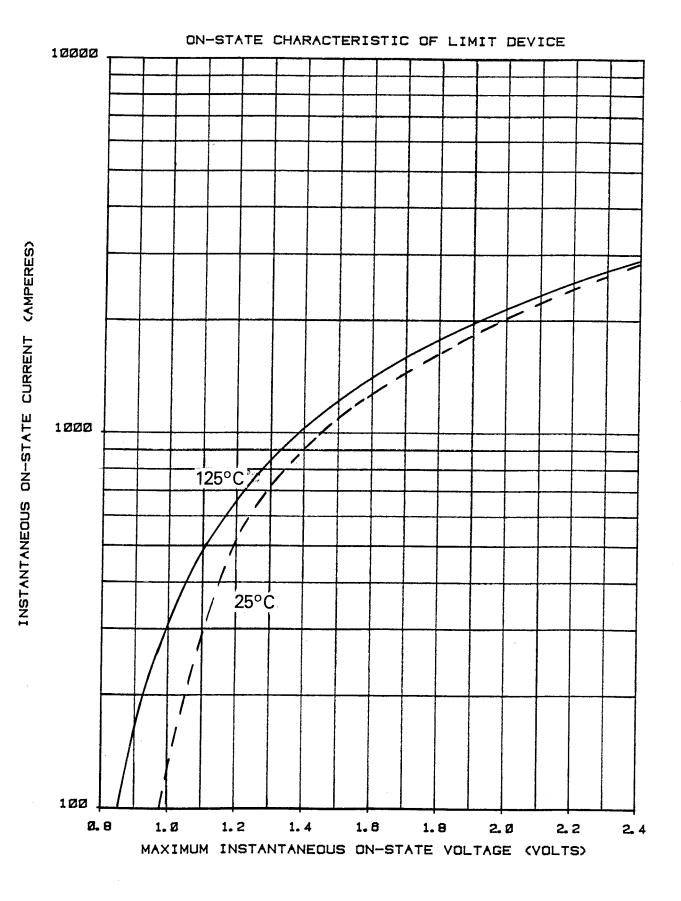


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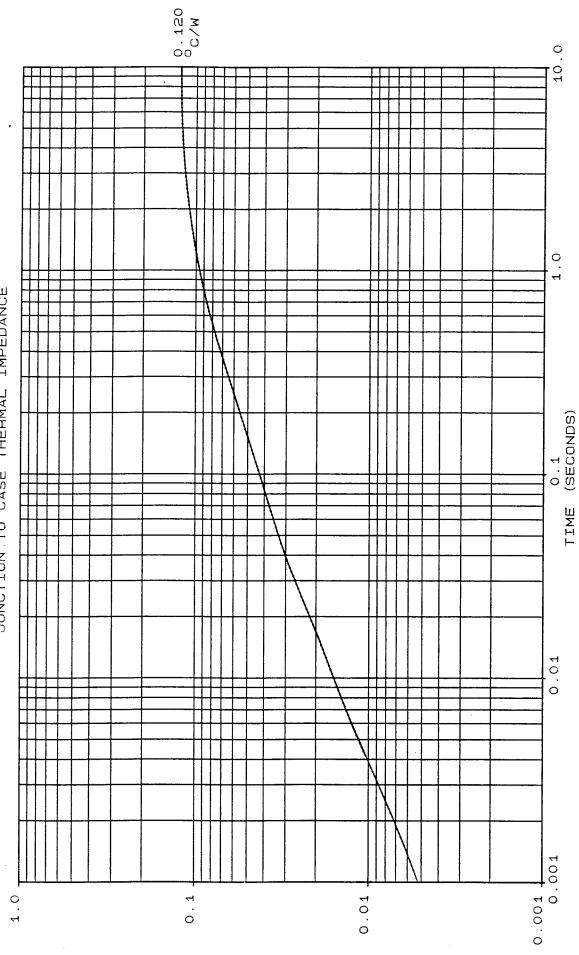
SQUARE WAVE



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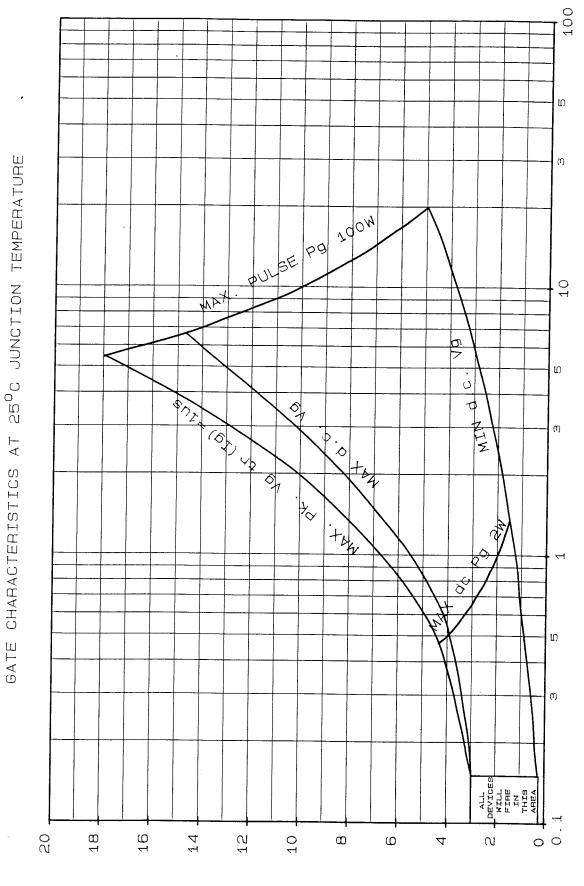


JUNCTION TO CASE THERMAL IMPEDANCE

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THERMAL IMPEDANCE (°C/WATT)

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Page Iss. 1	A.A.Iss. 1

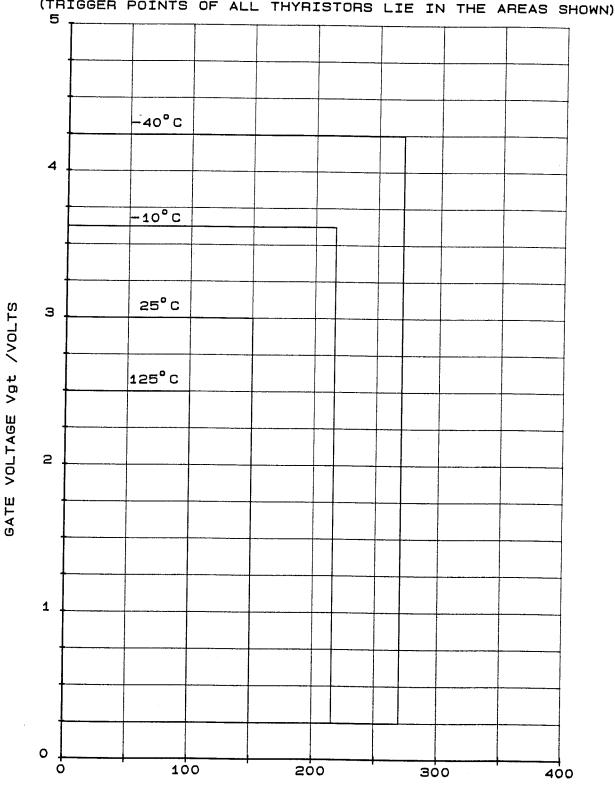


(STJOV) (UV) ESATJOV ETAS

GATE CURRENT (AMPS)

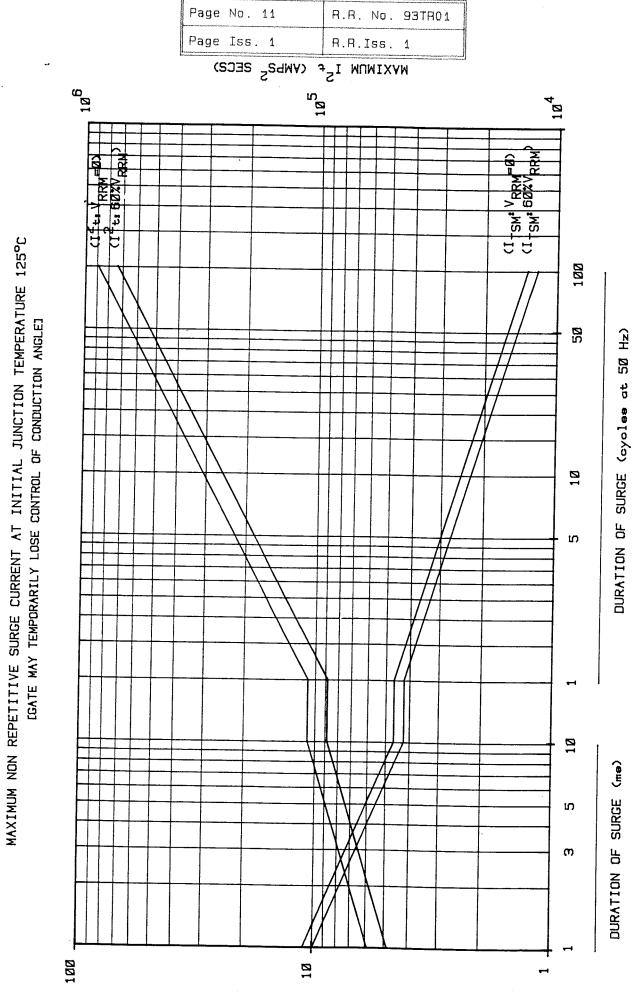
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GATE TRIGGERING CHARACTERISTICS (TRIGGER POINTS OF ALL THYRISTORS LIE IN THE AREAS SHOWN)

GATE CURRENT Igt /mA



TOTAL PEAK HALF SINE SURGE CURRENT (KA)

