

N0600N

MOS FIELD EFFECT TRANSISTOR

R07DS0220EJ0100 Rev.1.00 Jan 25, 2011

Description

The N0600N is N-channel MOS Field Effect Transistor designed for high current switching applications.

Features

- Low on-state resistance
 - --- $R_{DS(on)1}$ = 25 mΩ MAX. (V_{GS} =10 V, I_D = 15 A)
 - --- $R_{DS(on)2}$ = 36 mΩ MAX. (V_{GS} = 4.5 V, I_D = 15 A)
- Low input capacitance
 - C_{iss} = 1380 pF TYP. (V_{DS} = 10 V, V_{GS} = 0 V)

Ordering Information

Part No.	Lead Plating	Packing	Package
N0600N-S17-AY *1	Pure Sn (Tin)	Tube	Isolated TO-220
		50p/tube	typ. 2.2 g

Note: *1. Pb-free (This product does not contain Pb in the external electrode and other parts.)

Absolute Maximum Ratings (T_A = 25°C)

Item	Symbol	Ratings	Unit
Drain to Source Voltage (V _{GS} = 0 V)	V _{DSS}	60	V
Gate to Source Voltage (V _{DS} = 0 V)	V_{GSS}	±20	V
Drain Current (DC)	I _{D(DC)}	±30	Α
Drain Current (pulse) *1	I _{D(pulse)}	±60	Α
Total Power Dissipation (T _C = 25°C)	P _{T1}	20	W
Total Power Dissipation (T _A = 25°C)	P _{T2}	2.0	W
Channel Temperature	T _{ch}	150	°C
Storage Temperature	T _{stg}	-55 to +150	°C
Single Avalanche Current *2	I _{AS}	9.2	Α
Single Avalanche Energy *2	E _{AS}	12.5	mJ

Thermal Resistance

Channel to Case (Drain) Thermal Resistance $R_{th(ch-C)}$ 6.25 °C/W Channel to Ambient Thermal Resistance *2 $R_{th(ch-A)}$ 62.5 °C/W

Notes: *1. PW \leq 10 μ s, Duty Cycle \leq 1%

*2. Starting T_{ch} = 25°C, R_G = 25 Ω , V_{DD} = 30 V, V_{GS} = 20 \rightarrow 0 V

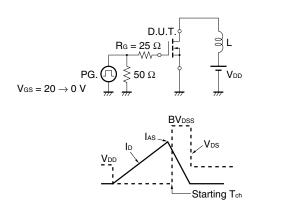
Electrical Characteristics ($T_A = 25$ °C)

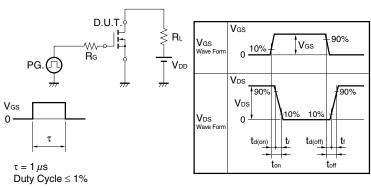
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Zero Gate Voltage Drain Current	I _{DSS}			1	μΑ	V _{DS} = 60 V, V _{GS} = 0 V
Gate Leakage Current	I _{GSS}			±100	nA	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$
Gate to Source Cut-off Voltage	$V_{GS(off)}$	1.5	2.0	2.5	V	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$
Forward Transfer Admittance *1	y _{fs}	4			S	V _{DS} = 10 V, I _D = 15 A
Drain to Source On-state	R _{DS(on)1}		17.5	25	mΩ	$V_{GS} = 10 \text{ V}, I_{D} = 15 \text{ A}$
Resistance *1	R _{DS(on)2}		22.3	36	mΩ	$V_{GS} = 4.5 \text{ V}, I_D = 15 \text{ A}$
Input Capacitance	C _{iss}		1380		pF	V _{DS} = 10 V,
Output Capacitance	Coss		186		pF	$V_{GS} = 0 V$,
Reverse Transfer Capacitance	C _{rss}		109		pF	f = 1 MHz
Turn-on Delay Time	t _{d(on)}		5.7		ns	V _{DD} = 30 V, I _D = 15 A,
Rise Time	tr		6.3		ns	V _{GS} = 10 V,
Turn-off Delay Time	t _{d(off)}		33.2		ns	$R_G = 0 \Omega$
Fall Time	t _f		3.9		ns	
Total Gate Charge	Q _G		29.8		nC	V _{DD} = 48 V,
Gate to Source Charge	Q _{GS}		4.2		nC	V _{GS} = 10 V,
Gate to Drain Charge	Q _{GD}		9.0		nC	I _D = 30 A
Body Diode Forward Voltage *1	V _{F(S-D)}		0.92	1.5	V	I _F = 30A, V _{GS} = 0 V
Reverse Recovery Time	t _{rr}		30		ns	I _F = 30 A, V _{GS} = 0 V,
Reverse Recovery Charge	Qrr		39.6		nC	di/dt = 100 A/μs

Note: *1. Pulsed

TEST CIRCUIT 1 AVALANCHE CAPABILITY

TEST CIRCUIT 2 SWITCHING TIME





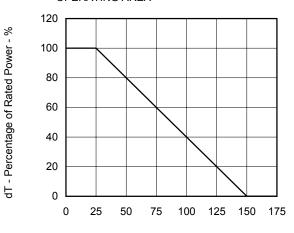
TEST CIRCUIT 3 GATE CHARGE

$$\begin{array}{c|c} D.U.T. \\ I_G = 2 \text{ mA} \\ \hline W. & V \end{array}$$

$$\begin{array}{c|c} PG. & \bigcirc \\ \hline \end{array} \begin{array}{c} S50 \ \Omega \\ \hline \end{array} \begin{array}{c} V_{DD} \\ \hline \end{array}$$

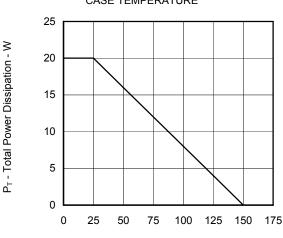
Typical Characteristics (T_A = 25°C)

DERATING FACTOR OF FORWARD BIAS SAFE OPERATING AREA



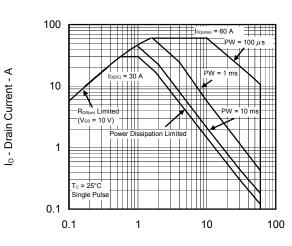
 T_{C} - Case Temperature - $^{\circ}\text{C}$

TOTAL POWER DISSIPATION vs. CASE TEMPERATURE



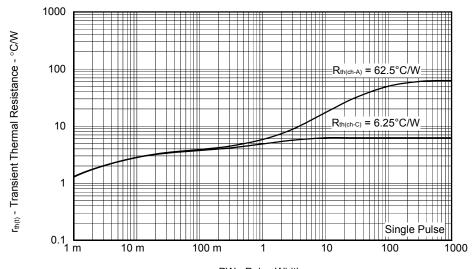
T_C - Case Temperature - °C

FORWARD BIAS SAFE OPERATING AREA



 $V_{\text{\scriptsize DS}}$ - Drain to Source Voltage - V

TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH



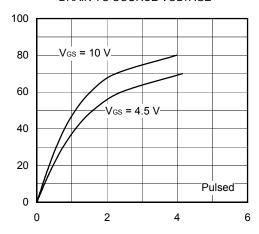
PW - Pulse Width - s

I_D - Drain Current - A

V_{GS(off)} - Gate to Source Cut-off Voltage - V

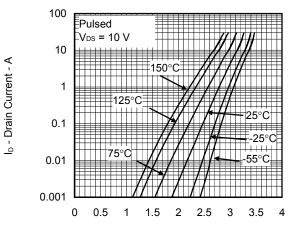
 $R_{\text{DS}(\text{on})}$ - Drain to Source On-state Resistance - $m\Omega$

DRAIN CURRENT vs. DRAIN TO SOURCE VOLTAGE



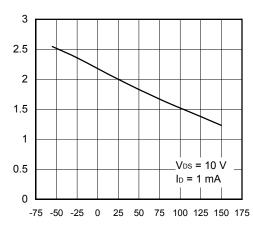
 V_{DS} - Drain to Source Voltage - V

FORWARD TRANSFER CHARACTERISTICS



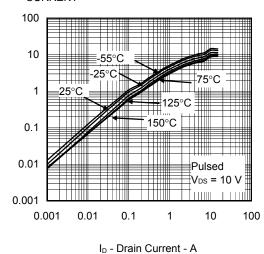
 V_{GS} - Gate to Source Voltage - V

GATE TO SOURCE CUT-OFF VOLTAGE vs. CHANNEL TEMPERATURE



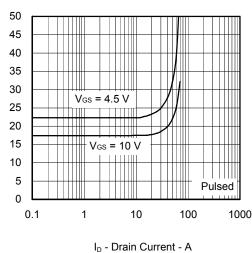
 T_{ch} - Channel Temperature - $^{\circ}\text{C}$

FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT

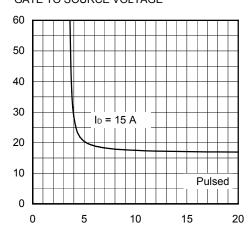


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DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT



DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE



 V_{GS} - Gate to Source Voltage - V

 $\mathsf{R}_{\mathsf{DS}(\mathsf{on})}$ - Drain to Source On-state Resistance - $m\Omega$

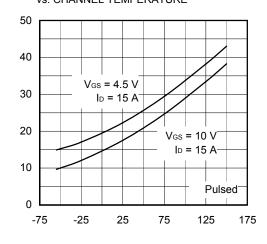
y_{fs} | - Forward Transfer Admittance - S

 $\mathsf{R}_{\mathsf{DS}(\mathsf{on})}$ - Drain to Source On-state Resistance - $m\Omega$

td(on), tr, td(off), tr - Switching Time - ns

IF - Diode Forward Current - A

DRAIN TO SOURCE ON-STATE RESISTANCE vs. CHANNEL TEMPERATURE



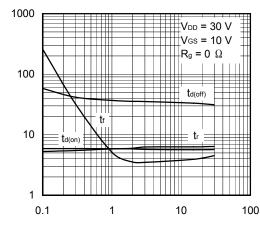
T_{ch} - Channel Temperature - °C

10000 Ciss 1000 Ciss 1000 Ciss 1000 Ciss 1000 Ciss 1000 Ciss 1000 Ciss 100 Ciss 100

CAPACITANCE vs. DRAIN TO SOURCE VOLTAGE

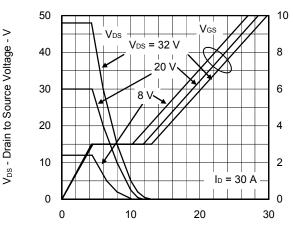
V_{DS} - Drain to Source Voltage - V

SWITCHING CHARACTERISTICS



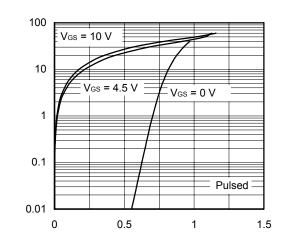
ID - Drain Current - A

DYNAMIC INPUT/OUTPUT CHARACTERISTICS



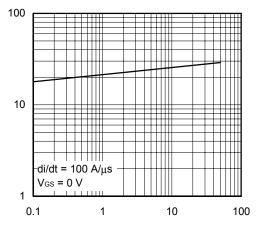
Q_G - Gate Charge - nC

SOURCE TO DRAIN DIODE FORWARD VOLTAGE



 $V_{F(S-D)}$ - Source to Drain Voltage - V

REVERSE RECOVERY TIME vs. DIODE FORWARD CURRENT

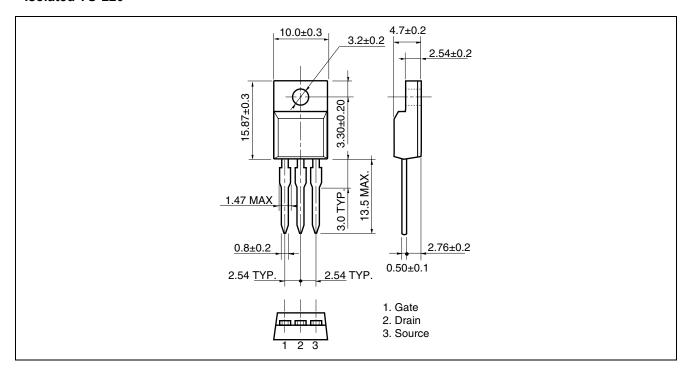


I_F - Diode Forward Current - A

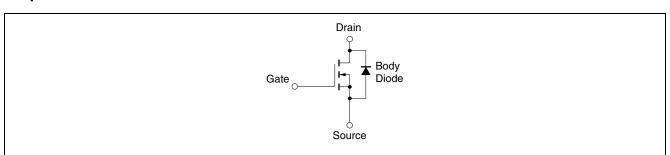
t_{rr} - Reverse Recovery Time - ns

Package Drawings (Unit: mm)

Isolated TO-220



Equivalent Circuit



Remark

Strong electric field, when exposed to this device, can cause destruction of the gate oxide and ultimately degrade the device operation. Steps must be taken to stop generation of static electricity as much as possible, and quickly dissipate it once, when it has occurred.

Revision History

N0600N Data Sheet

		Description		
Rev.	Date	Page	Summary	
1.00	Jan 25, 2011	-	First Edition Issued	

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Renesas Electronics America Inc. 2880 Scott Boulevard Santa Clara, CA 95050-2554, U.S.A. Tel: +1-408-588-6000, Fax: +1-408-588-6130

Renesas Electronics Canada Limited 1101 Nicholson Road, Newmarket, Ontario L3Y 9C3, Canada Tel: +1-905-898-5441, Fax: +1-905-898-3220

Renesas Electronics Europe Limited Dukes Meadow, Millboard Road, Boume End, Buckinghamshire, SL8 5FH, U.K Tel: +44-1628-585-100, Fax: +44-1628-585-900

Renesas Electronics Europe GmbH

Arcadiastrasse 10, 40472 Düsseldorf, Germany Tel: +49-211-65030, Fax: +49-211-6503-1327

Renesas Electronics (China) Co., Ltd.
7th Floor, Quantum Plaza, No.27 ZhiChunLu Haidian District, Beijing 100083, P.R.China
Tel: +86-10-2035-1155, Fax: +86-10-8235-7679

Renesas Electronics (Shanghai) Co., Ltd.
Unit 204, 205, AZIA Center, No. 1233 Lujiazui Ring Rd., Pudong District, Shanghai 200120, China
Tel: +86-21-5877-1818, Fax: +86-21-5887-7589

Renesas Electronics Hong Kong Limited
Unit 1601-1613, 16/F., Tower 2, Grand Century Place, 193 Prince Edward Road West, Mongkok, Kowloon, Hong Kong
Tel: +852-2868-9318, Fax: +852-2886-9022/9044

Renesas Electronics Taiwan Co., Ltd. 7F, No. 363 Fu Shing North Road Taipei, Taiwa Tel: +886-2-8175-9600, Fax: +886 2-8175-9670

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Renesas Electronics Malaysia Sdn.Bhd.
Unit 906, Block B, Menara Amcorp, Amcorp Trade Centre, No. 18, Jln Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia
Tel: +60-3-7955-9390, Fax: +60-3-7955-9510

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