


## TFT LCD Approval Specification

# MODEL NO.: N133I6-P01

Customer: \_\_\_\_\_

Approved by: \_\_\_\_\_

Note:

核准時間	部門	審核	角色	投票
2009-12-14 10:12:49	NB 產品管理處		Director	Accept

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**REVISION HISTORY**

Version	Date	Section	Description
Ver. 2.0	Sep, 10 '09	-	N13316-P01 Approval Specifications was first issued °

## 1. GENERAL DESCRIPTION

### 1.1 OVERVIEW

The N13316-P01 is a 13.3-inch TFT LCD cell with driver ICs and a 30-pin-and-1ch-LVDS circuit board. The product supports 1280 x 800 WXGA mode and can display up to 262,144 colors. The backlight unit is not built in.

### 1.2 FEATURES

- WXGA (1280 x 800 pixels) resolution
- DE (Data Enable) only mode
- 3.3V LVDS (Low Voltage Differential Signaling) interface
- RoHS Compliance

### 1.3 APPLICATION

- TFT LCD Notebook
- TFT LCD Monitor
- TFT LCD TV

### 1.4 GENERAL SPECIFICATIONS

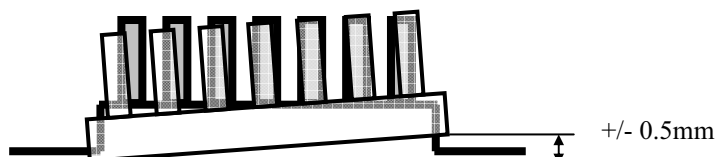
Item	Specification	Unit	Note
Active Area	286.08 (H) x 178.8 (V)	mm	(1)
Driver Element	a-si TFT active matrix	-	-
Pixel Number	1280 x R.G.B. x 800	pixel	-
Pixel Pitch	0.2235 (H) x 0.2235 (V)	mm	-
Pixel Arrangement	RGB vertical stripe	-	-
Display Colors	262,144	color	-
Transmissive Mode	Normally White	-	-
Surface Treatment	Glare, N2T (Reflection rate< 0.5%), 3H	-	-

### 1.5 MECHANICAL SPECIFICATIONS

Item	Min.	Typ.	Max.	Unit	Note
Weight	-	<b>216.5</b>	<b>226.5</b>	g	-
I/F connector mounting position	The mounting inclination of the connector makes the screen center within $\pm 0.5\text{mm}$ as the horizontal.				(2)

Note (1) Please refer to the attached drawings for more information of front and back outline dimensions.

(2) Connector mounting position



## 2. ABSOLUTE MAXIMUM RATINGS

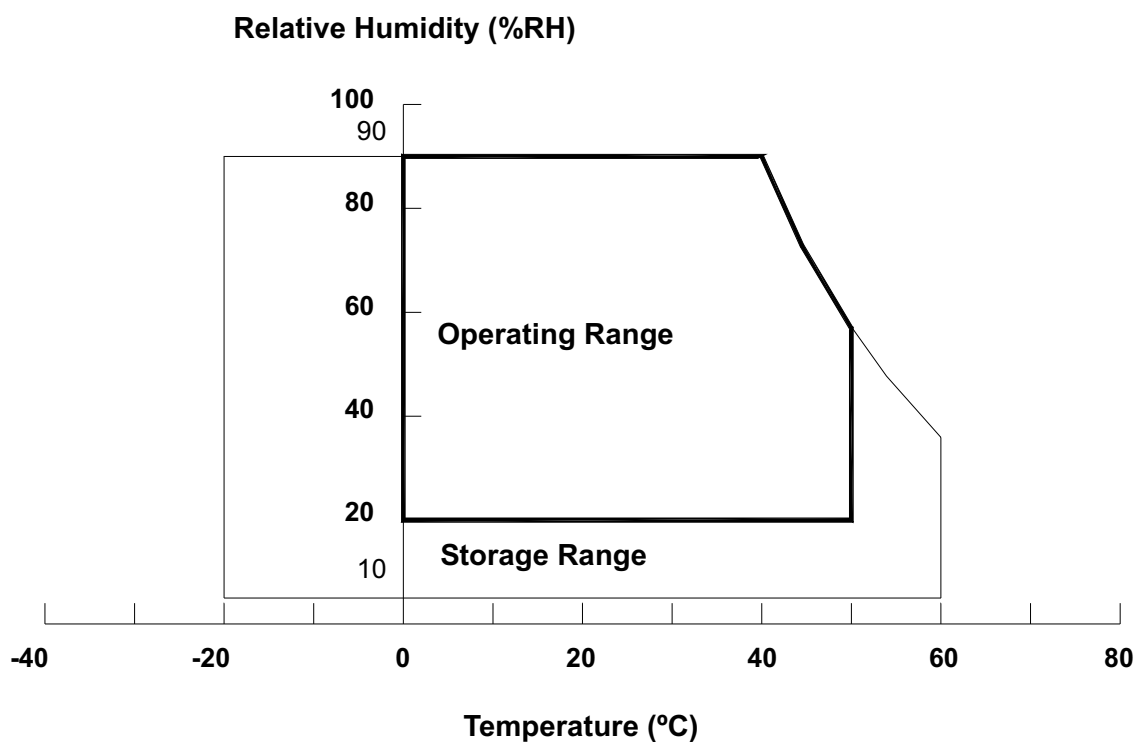
### 2.1 ABSOLUTE RATINGS OF ENVIRONMENT (BASED ON CMO MODULE N15413-L02)

Item	Symbol	Value		Unit	Note
		Min.	Max.		
Storage Temperature	T <sub>ST</sub>	-20	+60	°C	(1)
Operating Ambient Temperature	T <sub>OP</sub>	0	+50	°C	(1), (2)

Note (1) Temperature and relative humidity range is shown in the figure below.

- (a) 90 %RH Max. ( $T_a \leq 40$  °C).
- (b) Wet-bulb temperature should be 39 °C Max. ( $T_a > 40$  °C).
- (c) No condensation.

Note (2) The temperature of panel surface should be 0 °C Min. and 60 °C Max.



## 2.2 ABSOLUTE RATINGS OF ENVIRONMENT (OPEN CELL)

High temperature or humidity may reduce the performance of panel. Please store LCD panel within the specified storage conditions.

Storage Condition: With packing.

Storage temperature range:  $25\pm 5$  °C.

Storage humidity range:  $50\pm 10\%$ RH.

Shelf life: 30days

## 2.3 ELECTRICAL ABSOLUTE RATINGS (OPEN CELL)

Item	Symbol	Value		Unit	Note
		Min	Max		
Power Supply Voltage	$V_{CC}$	-0.3	+4.0	V	(1)
Logic Input Voltage	$V_{IN}$	-0.3	$V_{CC}+0.3$	V	

Note (1) Permanent damage to the device may occur if maximum values are exceeded. Function operation should be restricted to the conditions described under Normal Operating Conditions.

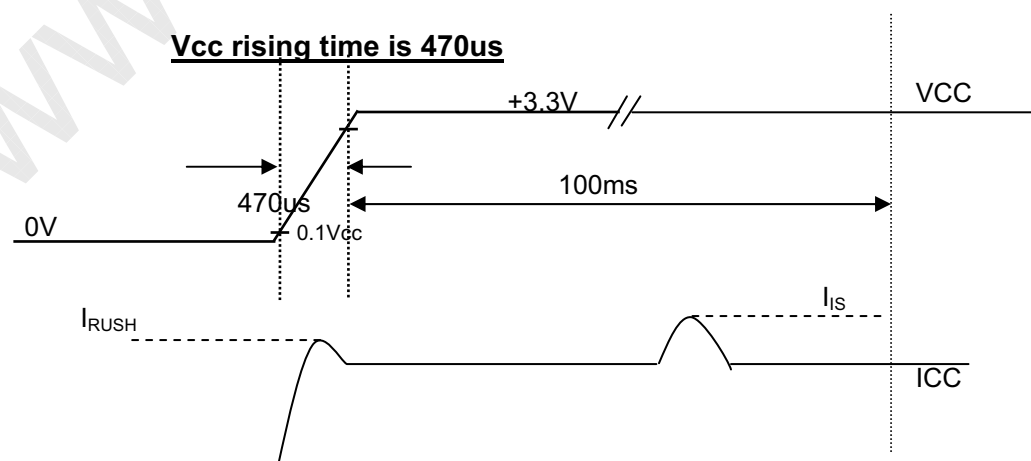
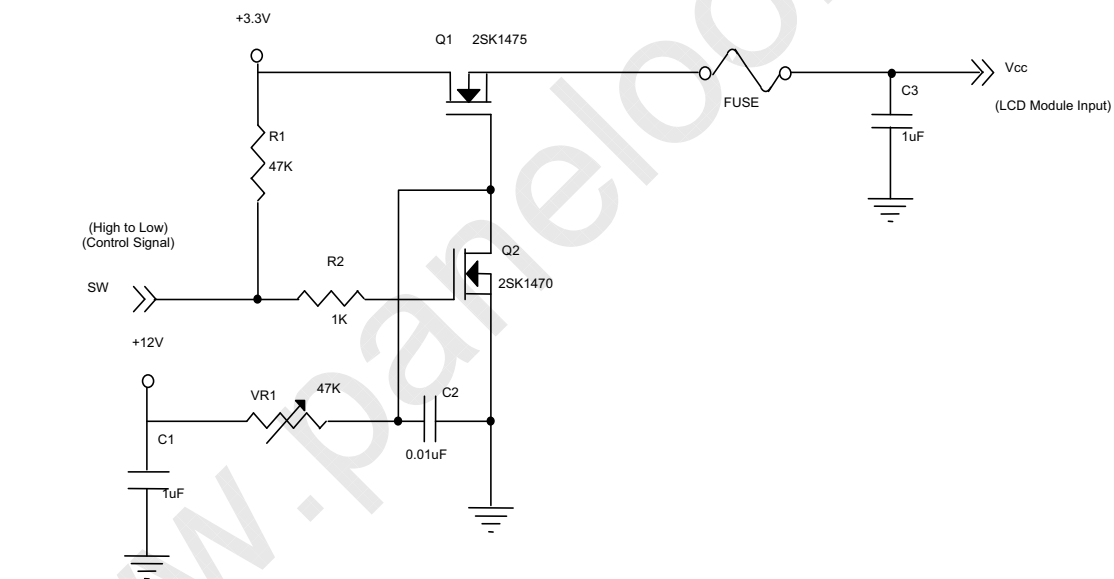
### 3. ELECTRICAL CHARACTERISTICS

#### 3.1 TFT LCD OPEN CELL

Parameter	Symbol	Value			Unit	Note
		Min.	Typ.	Max.		
Power Supply Voltage	$V_{CC}$	3.0	3.3	3.6	V	-
Ripple Voltage	$V_{RP}$	-	50		mV	-
Rush Current	$I_{RUSH}$	-	-	1.5	A	(2)
Initial Stage Current	$I_{IS}$	-	-	1.0	A	(2)
Power Supply Current	White	-	320	360	mA	(3)a
	Black	-	380	480	mA	(3)b
LVDS Differential Input High Threshold	$V_{TH(LVDS)}$	-	-	+100	mV	(5), $V_{CM}=1.2V$
LVDS Differential Input Low Threshold	$V_{TL(LVDS)}$	-100	-	-	mV	(5), $V_{CM}=1.2V$
LVDS Common Mode Voltage	$V_{CM}$	1.125	-	1.375	V	(5)
LVDS Differential Input Voltage	$ V_{ID} $	100	-	600	mV	(5)
Terminating Resistor	$R_T$	-	100	-	Ohm	-

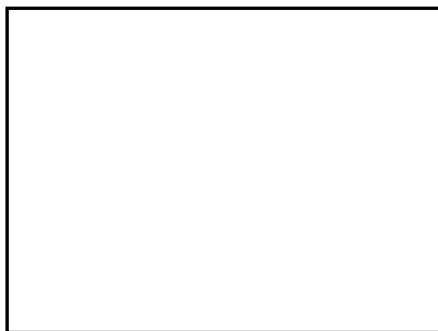
Note (1) The module should be always operated within above ranges.

Note (2) Measurement Conditions:



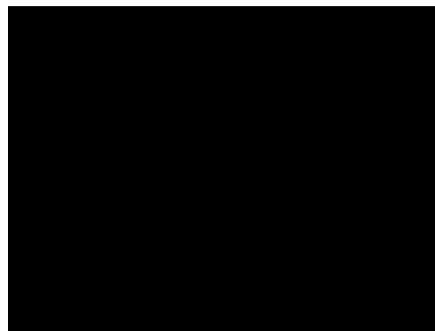
Note (3) The specified power supply current is under the conditions at  $V_{CC} = 3.3\text{ V}$ ,  $T_a = 25 \pm 2\text{ }^\circ\text{C}$ ,  $f_v = 60\text{ Hz}$ , whereas a power dissipation check pattern below is displayed.

a. White Pattern



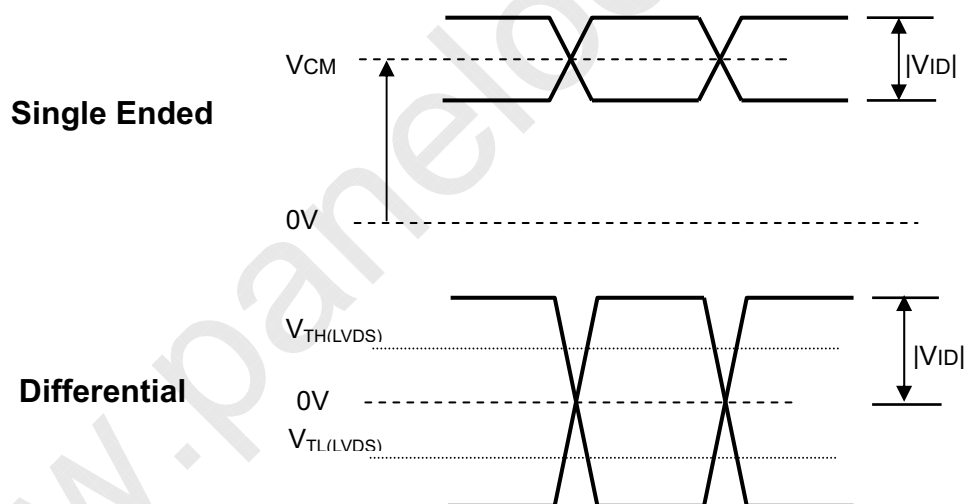
Active Area

b. Black Pattern



Active Area

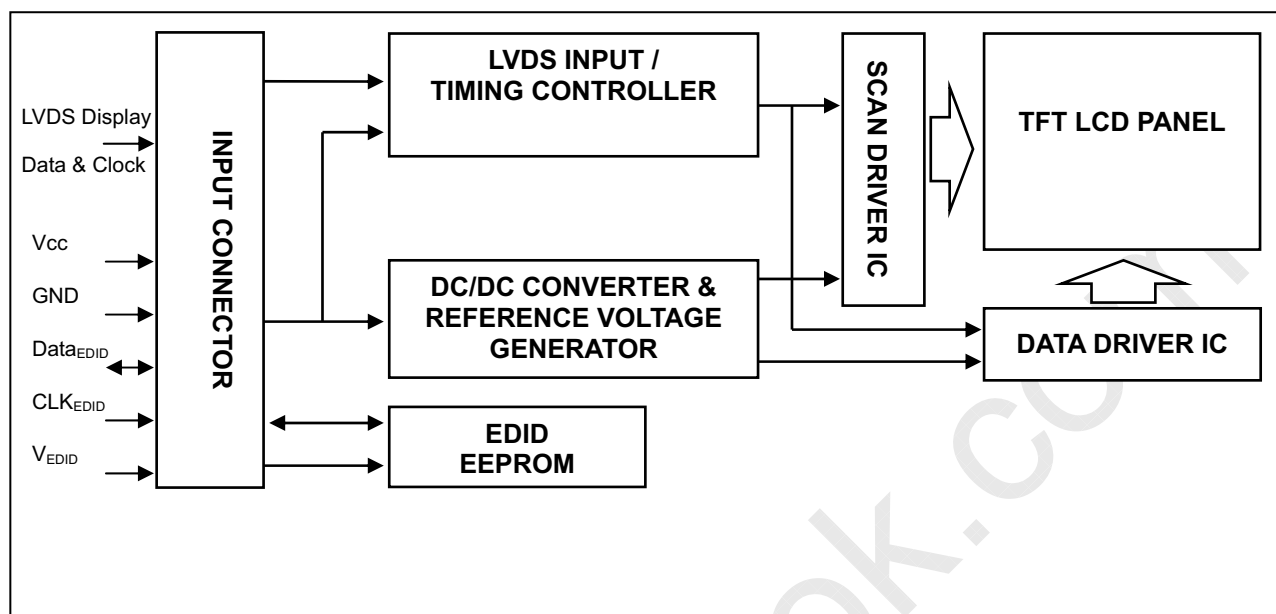
Note (4) The parameters of LVDS signals are defined as the following figures.





## 4. BLOCK DIAGRAM

### 4.1 TFT LCD OPEN CELL



## 5. INPUT TERMINAL PIN ASSIGNMENT

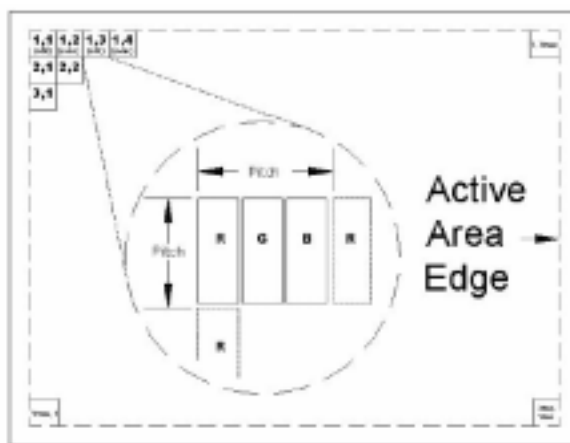
### 5.1 TFT LCD OPEN CELL

Pin	Symbol	Description	Polarity	Remark
1	Vss	Ground		
2	Vcc	Power Supply +3.3 V (typical)		
3	Vcc	Power Supply +3.3 V (typical)		
4	V <sub>EDID</sub>	DDC 3.3V Power		DDC 3.3V Power
5	NC	Non-Connection		
6	CLK <sub>EDID</sub>	DDC Clock		DDC Clock
7	DATA <sub>EDID</sub>	DDC Data		DDC Data R0~R5,G0
8	Rxin0-	LVDS Differential Data Input	Negative	
9	Rxin0+	LVDS Differential Data Input	Positive	
10	Vss	Ground		
11	Rxin1-	LVDS Differential Data Input	Negative	G1~G5, B0, B1
12	Rxin1+	LVDS Differential Data Input	Positive	
13	Vss	Ground		
14	Rxin2-	LVDS Differential Data Input	Negative	B2~B5, DE, Hsync, Vsync
15	Rxin2+	LVDS Differential Data Input	Positive	
16	Vss	Ground		
17	CLK-	LVDS Clock Data Input	Negative	LVDS Level Clock
18	CLK+	LVDS Clock Data Input	Positive	
19	Vss	Ground		
20	Vss	Ground		
21	LED_VCCS	LED Power		
22	LED_VCCS	LED Power		
23	NC	Non-Connection		
24	CH1	Light-bar Feedback Channel 1		
25	CH2	Light-bar Feedback Channel 2		
26	CH3	Light-bar Feedback Channel 3		
27	CH4	Light-bar Feedback Channel 4		
28	CH5	Light-bar Feedback Channel 5		
29	CH6	Light-bar Feedback Channel 6		
30	Vss	Ground		

Note (1) Connector Part No.: 20347-330E-12(I-PEX) or equivalent

Note (2) User's connector Part No: 20347-030T-12(I-PEX) or equivalent

Note (3) The first pixel is odd as shown in the following figure.

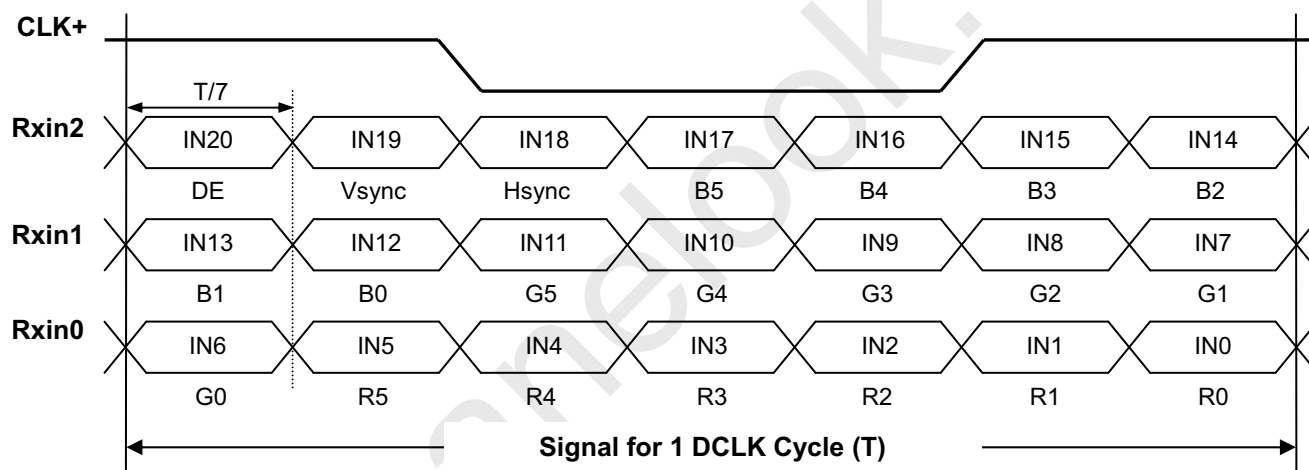


## 5.2 BACKLIGHT Connector Pin Define:

Pin	Symbol	Description
1	LED_VCCS	LED Power
2	LED_VCCS	LED Power
3	NC	No Connection
4	CH1	Light-bar Feedback Channel 1
5	CH2	Light-bar Feedback Channel 2
6	CH3	Light-bar Feedback Channel 3
7	CH4	Light-bar Feedback Channel 4
8	CH5	Light-bar Feedback Channel 5
9	CH6	Light-bar Feedback Channel 6

Note (1) User's connector Part No.: FCI, 59453-091110EDLF or equivalent

## 5.3 TIMING DIAGRAM OF LVDS INPUT SIGNAL



#### 5.4 COLOR DATA INPUT ASSIGNMENT

The brightness of each primary color (red, green and blue) is based on the 6-bit gray scale data input for the color. The higher the binary input, the brighter the color. The table below provides the assignment of color versus data input.

Color		Data Signal																	
		Red						Green						Blue					
		R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	B4	B3	B2	B1	B0
Basic Colors	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	0	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Gray Scale Of Red	Red(0)/Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Red(1)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	
	Red(2)	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	
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	Red(61)	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	
	Red(62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	
Red(63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0		
Gray Scale Of Green	Green(0)/Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Green(1)	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	
	Green(2)	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	
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	Green(61)	0	0	0	0	0	0	1	1	1	1	0	1	0	0	0	0	0	
	Green(62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	
Green(63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0		
Gray Scale Of Blue	Blue(0)/Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	
	Blue(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	
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	Blue(61)	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	0	1	
	Blue(62)	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0	
Blue(63)	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1		

Note (1) 0: Low Level Voltage, 1: High Level Voltage

## 6. INTERFACE TIMING

### 6.1 INPUT SIGNAL TIMING SPECIFICATIONS

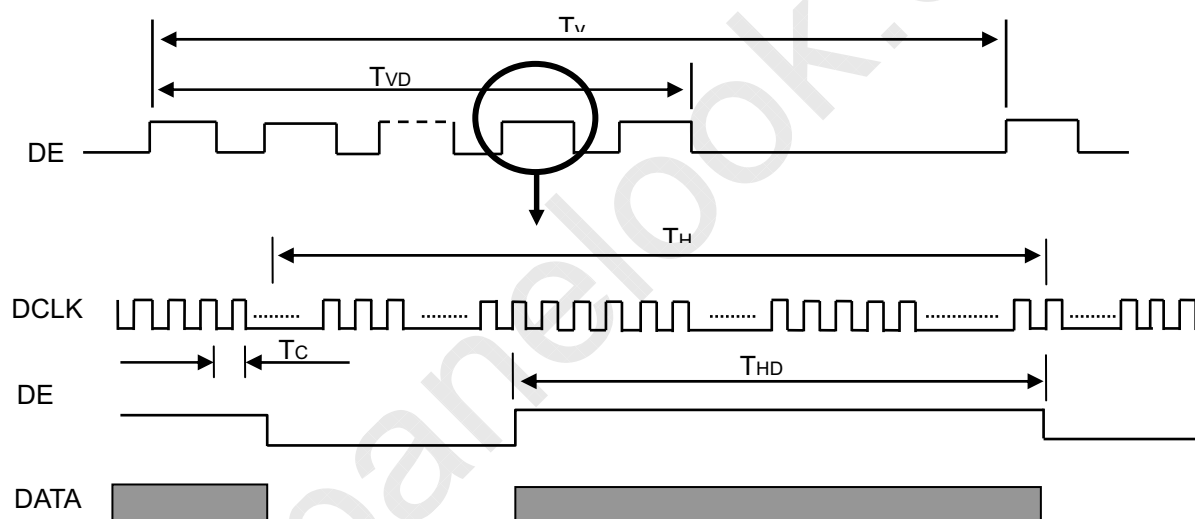
The input signal timing specifications are shown as the following table and timing diagram.

Signal	Item	Symbol	Min.	Typ.	Max.	Unit	Note
DCLK	Frequency	1/Tc	66	71	75.6	MHz	(2)
DE	Vertical Total Time	TV	802	823	840	TH	-
	Vertical Active Display Period	TVD	800	800	800	TH	-
	Vertical Active Blanking Period	TVB	TV-TVD	23	TV-TVD	TH	-
	Horizontal Total Time	TH	1380	1440	1500	Tc	(2)
	Horizontal Active Display Period	THD	1280	1280	1280	Tc	(2)
	Horizontal Active Blanking Period	THB	TH-THD	160	TH-THD	Tc	(2)

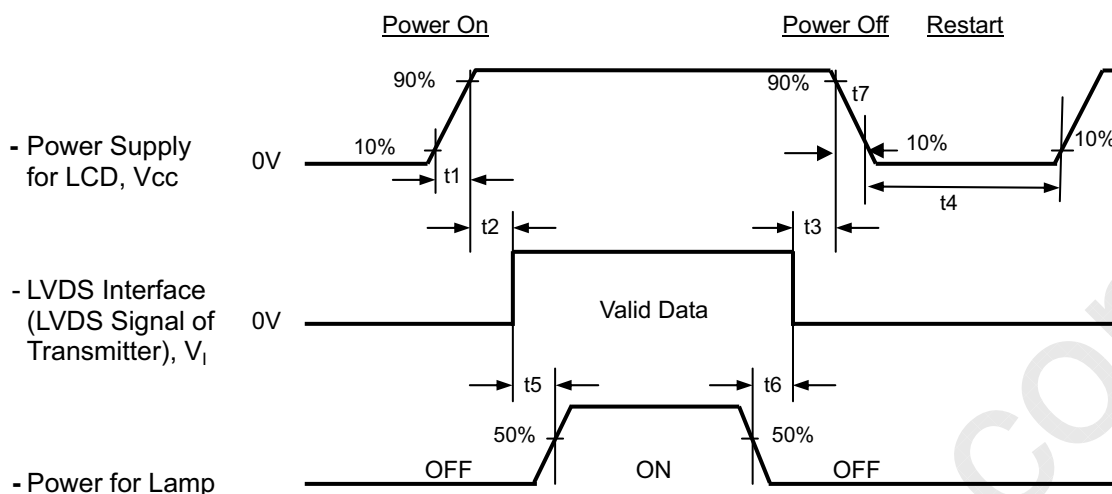
Note (1) Because this module is operated by DE only mode, Hsync and Vsync are ignored.

Note (2) 1 channels LVDS input.

#### INPUT SIGNAL TIMING DIAGRAM



## 6.2 POWER ON/OFF SEQUENCE



## Timing Specifications:

$$0.5 \leq t1 \leq 10 \text{ ms}$$

$$0 \leq t2 \leq 50 \text{ ms}$$

$$0 \leq t3 \leq 50 \text{ ms}$$

$$t4 \geq 500 \text{ ms}$$

$$t5 \geq 200 \text{ ms}$$

$$t6 \geq 200 \text{ ms}$$

Note (1) Please follow the power on/off sequence described above. Otherwise, the LCD module might be damaged.

Note (2) Please avoid floating state of interface signal at invalid period. When the interface signal is invalid, be sure to pull down the power supply of LCD Vcc to 0 V.

Note (3) The Backlight inverter power must be turned on after the power supply for the logic and the interface signal is valid. The Backlight inverter power must be turned off before the power supply for the logic and the interface signal is invalid.

Note (4) Sometimes some slight noise shows when LCD is turned off (even backlight is already off). To avoid this phenomenon, we suggest that the Vcc falling time is better to follow  $5\text{ms} \leq t7 \leq 300 \text{ ms}$ .

## 7. OPTICAL CHARACTERISTICS

### 7.1 TEST CONDITIONS

Item	Symbol	Value	Unit
Ambient Temperature	Ta	25±2	°C
Ambient Humidity	Ha	50±10	%RH
Supply Voltage	V <sub>CC</sub>	3.3	V
Input Signal	According to typical value in "3. ELECTRICAL CHARACTERISTICS"		
Inverter Current	I <sub>L</sub>	6	mA
Inverter Driving Frequency	F <sub>L</sub>	61	KHz

### 7.2 OPTICAL SPECIFICATIONS

The relative measurement methods of optical characteristics are shown as below. The following items should be measured under the test conditions described in 7.1 and stable environment shown in Note (6).

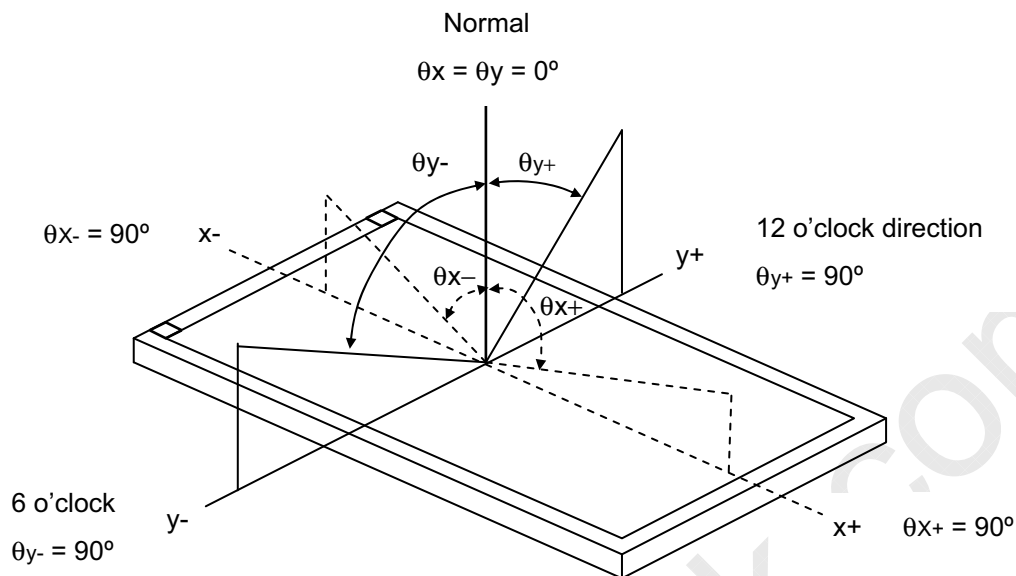
Item		Symbol	Condition	Min.	Typ.	Max.	Unit	Note					
Color Chromaticity	Red	Rcx	$\theta_x=0^\circ, \theta_y=0^\circ$ CS-1000T Standard light source "C"	Typ - 0.03	0.546	Typ + 0.03	-	(0),(6)					
		Rcy			0.325		-						
	Green	Gcx			0.291		-						
		Gcy			0.542		-						
	Blue	Bcx			0.155		-						
		Bcy			0.127		-						
	White	Wcx			0.276		-						
		Wcy			0.299		-						
	Center Transmittance				T%		$\theta_x=0^\circ, \theta_y=0^\circ$		7.2	8.0	-	-	(1), (8)
	Contrast Ratio				CR		CS-1000T, CMO BLU		300	500	-	-	(1), (4)
Response Time		T <sub>R</sub>	$\theta_x=0^\circ, \theta_y=0^\circ$	-	3	8	ms	(5)					
		T <sub>F</sub>		-	5	12	ms						
Transmittance uniformity		$\delta T\%$	$\theta_x=0^\circ, \theta_y=0^\circ$ BM-5A	--	--	1.25	-	(1), (7)					
Viewing Angle	Horizontal	$\theta_{x+}$	CR≥10 BM-5A	40	45	-	Deg.	(1), (3) (6)					
		$\theta_{x-}$		40	45	-							
	Vertical	$\theta_{y+}$		15	20	-							
		$\theta_{y-}$		40	45	-							

Note (0) Light source is the standard light source "C" which is defined by CIE and driving voltages are based on suitable gamma voltages. The calculating method is as following :

1. Measure Module's and BLU's spectrums. White is without signal input and R, G, B are with signal input. BLU is supplied by CMO.
2. Calculate cell's spectrum.
3. Calculate cell's chromaticity by using the spectrum of standard light source "C"

Note (1) Light source is the BLU which is supplied by CMO and driving voltages are based on suitable gamma voltages. White is without signal input and R, G, B are with signal input. SPEC is judged by CMO's golden sample.

Note (2) Definition of Viewing Angle ( $\theta_x$ ,  $\theta_y$ ):



Note (3) Definition of Contrast Ratio (CR):

$$CR_{AVE} = [CR(1) + CR(2) + CR(3) + CR(4) + CR(5)] / 5$$

$CR_{max}$  = Max value of CR at whole Viewing Angle

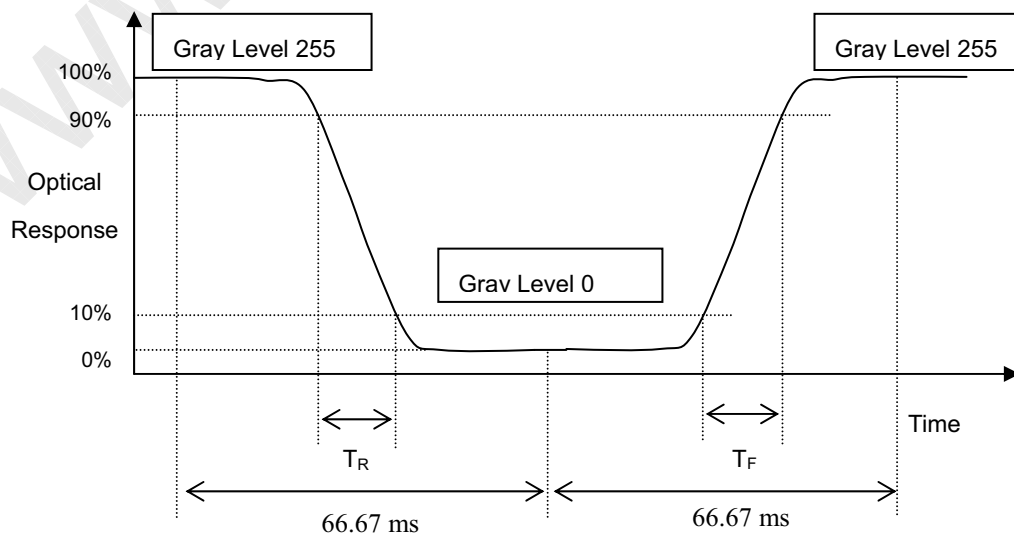
CR (X) is corresponding to the Contrast Ratio of the point X at Figure in Note (6).

$$CR = \frac{\text{Luminance with all pixel white (Gmax)}}{\text{Luminance with all pixel black (Gmin)}}$$

Gmax: Luminance of gray max at the center point of panel.

Gmin: Luminance of gray min at the center point of panel.

Note (4) Definition of Response Time ( $T_R$ ,  $T_F$ ):





**Note (5) Definition of Luminance of White ( $L_C$ ):**

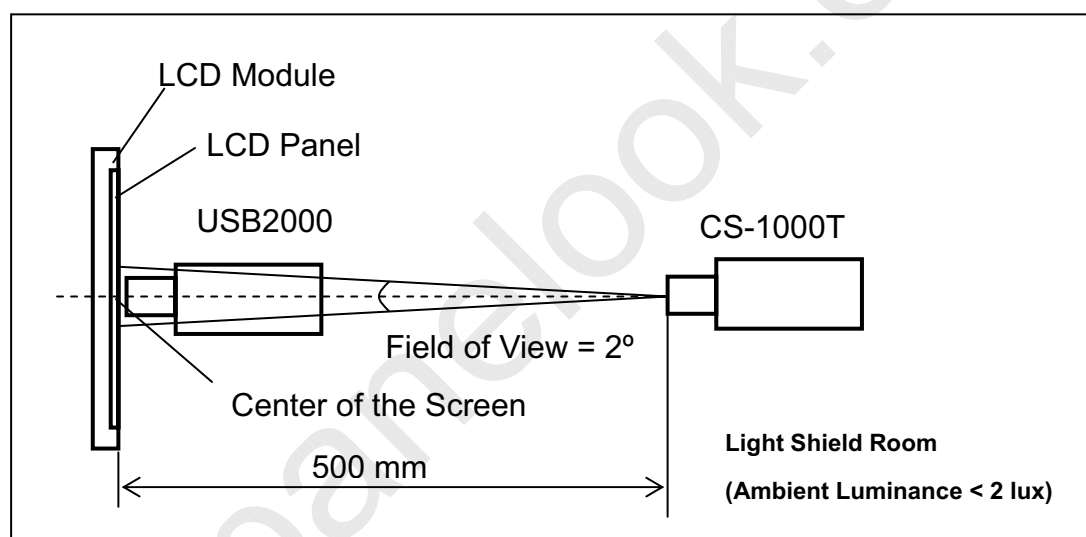
Measure the luminance of gray level 255 at center point

$$L_C = L(5)$$

$L(x)$  is corresponding to the luminance of the point X at Figure in Note (7).

**Note (6) Measurement Setup:**

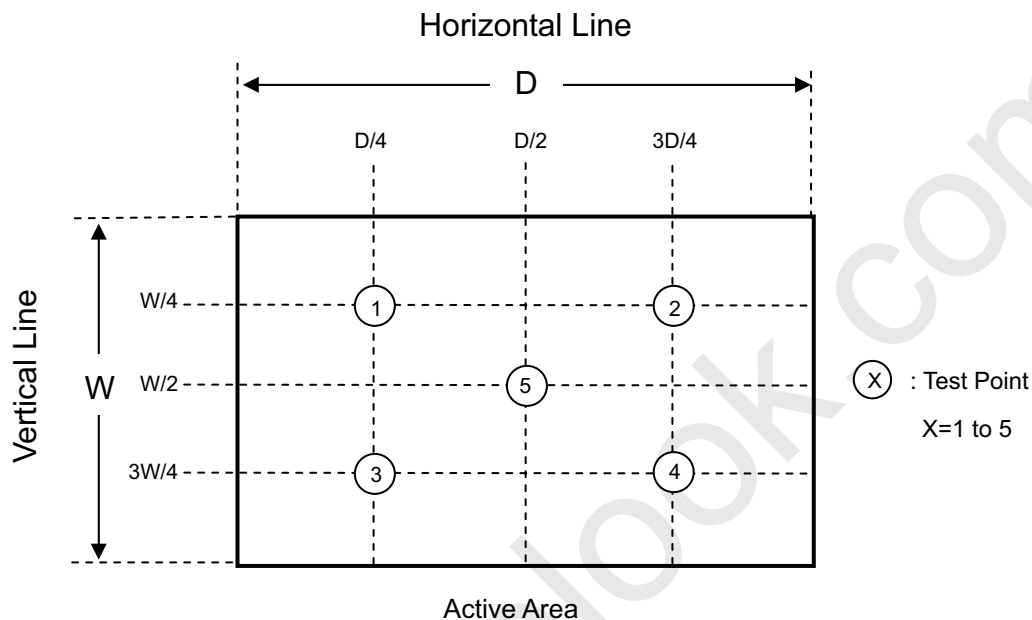
The LCD module should be stabilized at given temperature for 20 minutes to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting Backlight for 20 minutes in a windless room.



Note (7) Definition of Transmittance Variation ( $\delta T\%$ ):

Measure the transmittance at 5 points

$$\delta T\% = \frac{\text{Maximum } [T\%(1), T\%(2), \dots T\%(5)]}{\text{Minimum } [T\%(1), T\%(2), \dots T\%(5)]}$$



Note (8) Definition of Transmittance (T%):

Module is without signal input.

BLU is supplied by CMO.

$$\text{Transmittance} = \frac{\text{Luminance of LCD module}}{\text{Luminance of backlight}} * 100\%$$

### 7.3 Flicker Adjustment

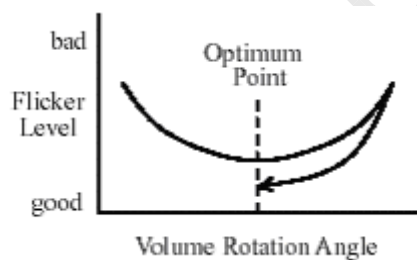
(1) Adjustment Pattern: 2H1V checker pattern as follows.

R	G	B	R	G	B	R	G	B	R	G	B	R	G	B	R	G	B	R	G	B	R	G	B
R	G	B	R	G	B	R	G	B	R	G	B	R	G	B	R	G	B	R	G	B	R	G	B
R	G	B	R	G	B	R	G	B	R	G	B	R	G	B	R	G	B	R	G	B	R	G	B
R	G	B	R	G	B	R	G	B	R	G	B	R	G	B	R	G	B	R	G	B	R	G	B
R	G	B	R	G	B	R	G	B	R	G	B	R	G	B	R	G	B	R	G	B	R	G	B
R	G	B	R	G	B	R	G	B	R	G	B	R	G	B	R	G	B	R	G	B	R	G	B
R	G	B	R	G	B	R	G	B	R	G	B	R	G	B	R	G	B	R	G	B	R	G	B
R	G	B	R	G	B	R	G	B	R	G	B	R	G	B	R	G	B	R	G	B	R	G	B
R	G	B	R	G	B	R	G	B	R	G	B	R	G	B	R	G	B	R	G	B	R	G	B



(2) Adjustment Method:

Flicker should be adjusted by turning the volume for flicker adjustment by the ceramic driver. It is adjusted to the point with least flickering of the whole screen. After making it surely overrun at once, it should be adjusted to the optimum point.



## 8. PACKAGING

### 8.1 PACKING SPECIFICATIONS

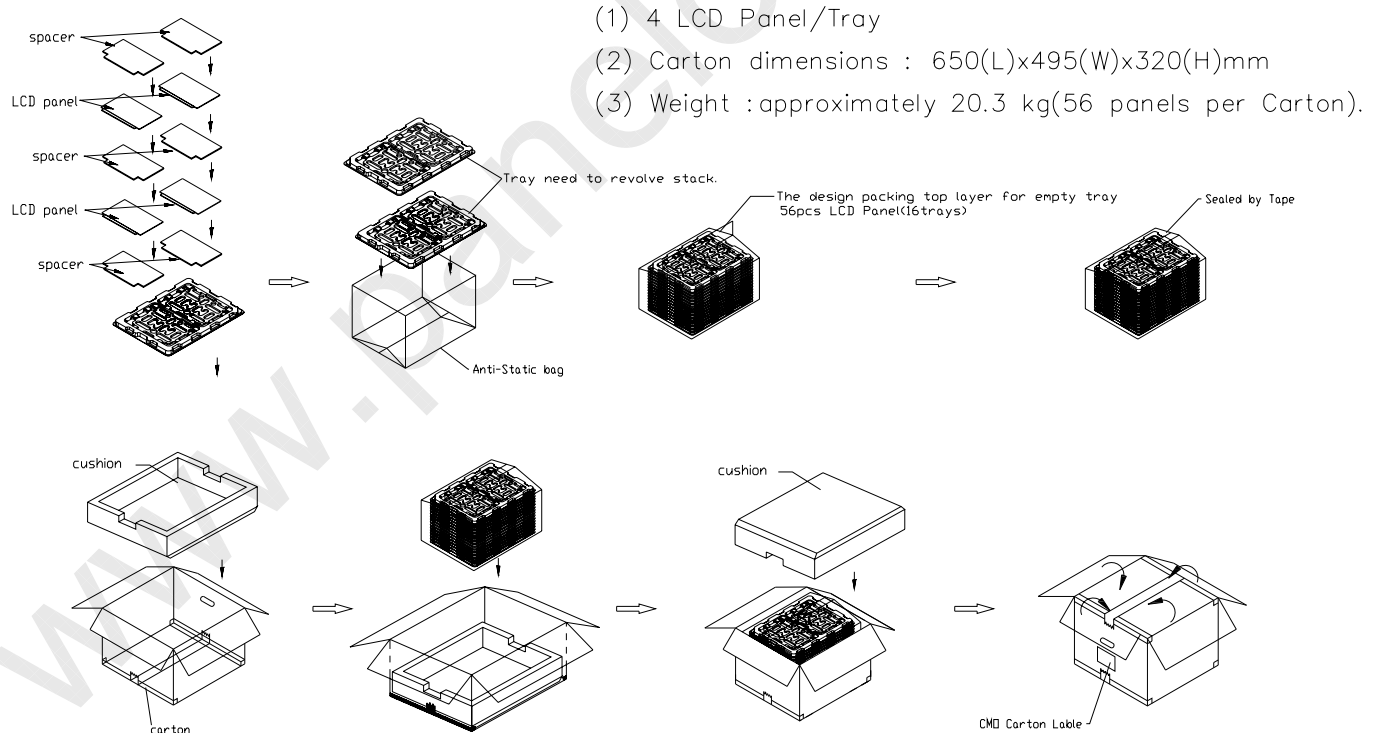
- (1) 56 open cells / 16 Tray / 1 Box
- (2) Box dimensions: 650(L)x 495(W)x 320(H)
- (3) Weight: approximately 20.3Kg (56 open cells per box)

### 8.2 PACKING METHOD

- (1) Carton Packing should have no failure in the following reliability test items

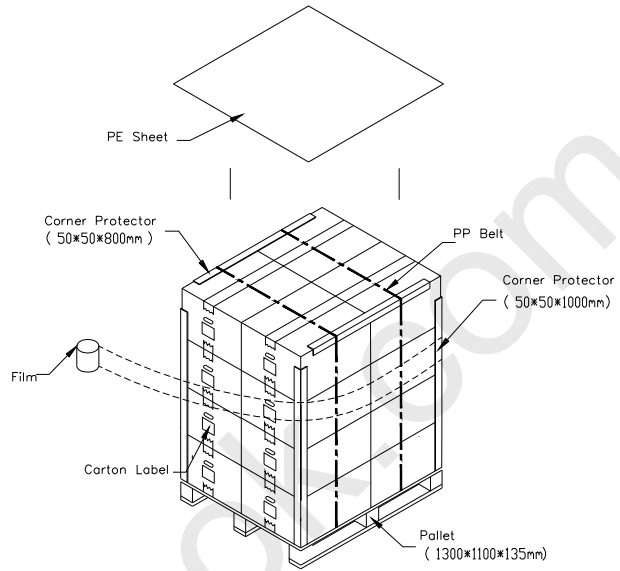
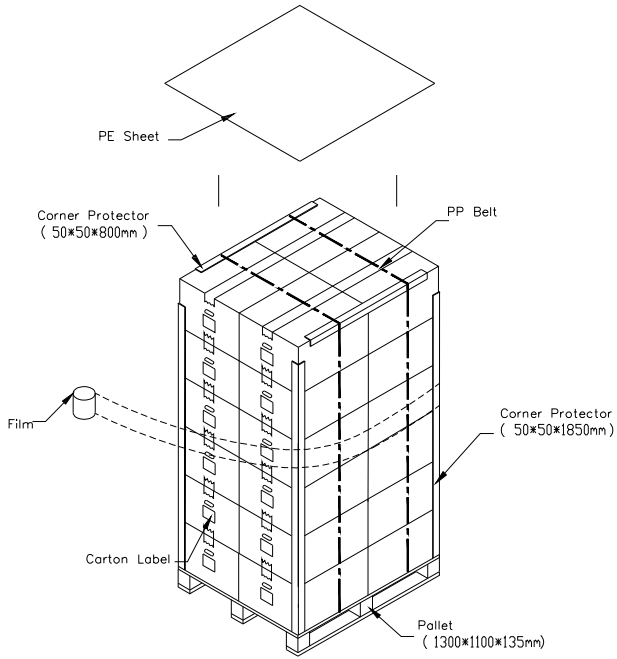
Test Item	Test Conditions	Note
Packing Vibration	ISTA STANDARD Random, Frequency Range: 1 – 200 Hz Top & Bottom: 30 minutes (+Z), 10 min (-Z), Right & Left: 10 minutes (X) Back & Forth 10 minutes (Y)	Non Operation

- (2) Packing method.



Sea and Land Transportation

Air Transportation



## 9. DEFINITION OF LABELS

### 9.1 CARTON LABEL

The barcode nameplate is pasted on each box as illustration, and its definitions are as following explanation

- (a) Model Name: N133I6 -P01
- (b) Carton ID: CMO internal control
- (c) Quantities: 23



The illustration shows a rectangular carton label with a light blue header. The header contains the CHI MEI logo (a square with 'm' inside) and the text 'CHI MEI OPTOELECTRONICS' on the left, and 'RoHS' on the right. Below the header, there are four horizontal lines for text entry: 'PO.NO', 'Part ID', 'Model Name', and 'Carton ID'. The 'Model Name' field is filled with 'N133I6 -P01' in red. The 'Carton ID' field contains a barcode and the text 'XXXXXXXXXXXXXXXX' below it. The 'Quantities' field is filled with '23' in red. At the bottom of the label, it says 'Made in Taiwan'.

## 10. PRECAUTIONS

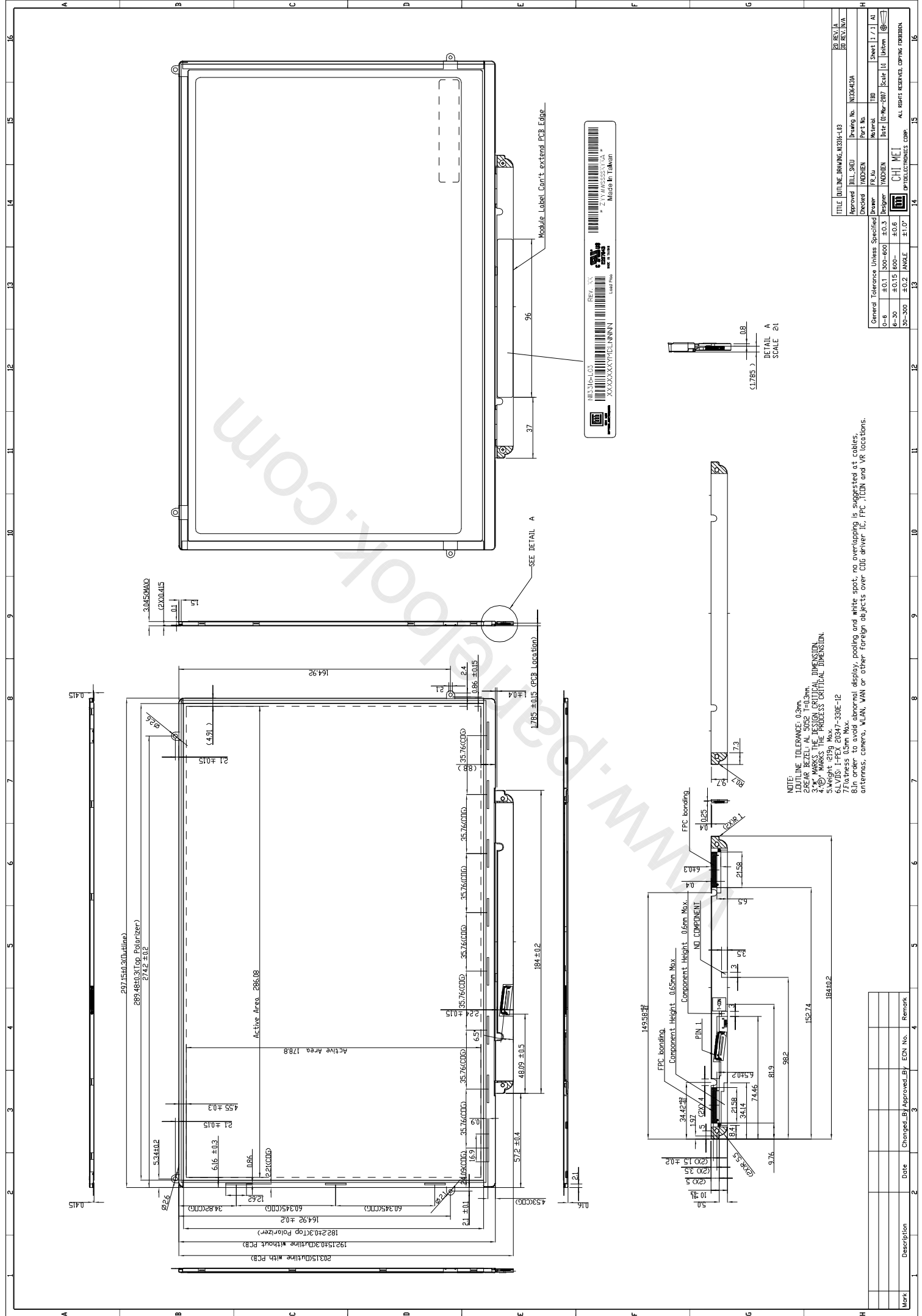
### 10.1 ASSEMBLY AND HANDLING PRECAUTIONS

- (1) Do not apply rough force such as bending or twisting to the product during assembly.
- (2) To assemble backlight or install module into user's system can be only in clean working areas. The dust and oil may cause electrical short or worsen the polarizer.
- (3) It's not permitted to have pressure or impulse on the module because the LCD panel will be damaged.
- (4) Always follow the correct power sequence when the product is connecting and operating. This can prevent damage to the CMOS LSI chips during latch-up.
- (5) Do not pull the I/F connector in or out while the module is operating.
- (6) Use a soft dry cloth without chemicals for cleaning, because the surface of polarizer is very soft and easily scratched.
- (7) It is dangerous that moisture come into or contacted the product, because moisture may damage the product when it is operating.
- (8) High temperature or humidity may reduce the performance of module. Please store this product within the specified storage conditions.
- (9) When ambient temperature is lower than 10°C may reduce the display quality. For example, the response time will become slowly.

### 10.2 SAFETY PRECAUTIONS

- (1) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contact with hands, skin or clothes, it has to be washed away thoroughly with soap.
- (2) After the product's end of life, it is not harmful in case of normal operation and storage.

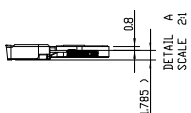
## 11. MECHANICAL DRAWING



NOTE:  
 1. OUTLINE TOLERANCE: 0.3mm.  
 2. REAR BEZEL AL SIZE: T=0.3mm.  
 3. \* MARKS THE DESIGN CRITICAL DIMENSION.  
 4. \* MARKS THE PROCESS CRITICAL DIMENSION.  
 5. UNLESS OTHERWISE SPECIFIED, DIMENSIONS ARE IN MILLIMETERS.  
 6. UNITS: L-FPE 20847-50E-12  
 7. Flatness 0.5mm Max.  
 8. In order to avoid abnormal display, pooling and white spot, no overlapping is suggested at cables, antennas, camera, V-LAN, VAN or other foreign objects over COG driver IC, FPC, TCDN and VR locations.

REV: 1.03  
 N133164-03  
 XXXXXXXXXXXXXXXXXXXX  
 MADE IN TAIWAN  
 Load Free

TITLE		IDLINE: N133164-03	
Approved	JILL SHU	Drawing No.	N133164-03A
Decided	YADHREN	Part No.	EPD
General	Tolerance Unless Specified	Drawer	YADHREN
0-8	±0.1	500-800	±0.3
8-30	±0.15	800-	±0.6
30-300	±0.2	ANGLE	±1.0°



Mark	Description	Date	Changed By	Approved By	ECN No.	Remark