





TFT LCD Approval Specification

MODEL NO.: N150X3 - L03

Customer: HP

Approved by: _____

Note:

Liquid Crystal Display Division	
QRA Dept.	PDD I Dept.
Approval	Approval
	

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REVISION HISTORY

Version	Date	Page (New)	Section	Description
Ver 0.0	Oct.30,'02	All	All	Tentative Specification was first issued.
Ver 0.1	Apr.08,'03	6	3.1	Add typ. value of POWER SUPPLY CURRENT "White" of power supply current: 300; "Black": 400
		8	3.2	Modify Note(1) HV signal cable's color from "White" to "Pink"
		9	4.2	Modify BACKLIGHT UNIT HV(White) to be HV(Pink)
			5.1	Modify INPUT TERMINAL PIN ASSIGNMENT
		10	5.2	Modify HV Symbol cable color to be "Pink" Modify section 5.2 Note(1) connector part no. from JST-FI-XB30S-HF10 to BHSR-02VS-1 (JST)
		11	6.1	Modify INPUT SIGNAL TIMING SPECIFICATIONS Revise min. DCLK frequency to be "50" Revise max DE Horizontal Total Time to be "1500"
Ver 1.0	May.20,'03	20,21		Outline dimension updated to change connector length to 105mm.
		4	1.5	Modify "Vertical(V)" of Module Size min 241.5 typ. 242 max.242.5 Modify typ. of Weight to be 510 instead of 520; Add max to be 525
		5	2.1	Modify "Shock (Non-operating)" of ABSOLUTE RATINGS OF ENVIRONMENT to be 240 instead of 200.
		6	2.2.2	Modify "Lamp Current" of "BACKLIGHT UNIT" to be 7.0 instead of 7.5
		8	3.2	Add "Lamp input voltage" of "BACKLIGHT" min. = 608 max. = 743 Modify "Lamp turn on voltage" to be max. 1360 instead of 1100. Modify "Power consumption" to be typ. 4.1 instead of 4.4
		13	7.2	Modify OPTICAL SPECIFICATION Add min. 250 & modify typ. to be 350 instead of 250 Add max. response time $T_R = 10$ $T_F = 25$ Add min. Average Luminance of White to be "170" Add typ. White Variation to be "1.2" Add $R_x = 0.590$ $R_y = 0.346$ $G_x = 0.316$ $G_y = 0.534$ $B_x = 0.149$ $B_y = 0.131$ Add min $W_x = 0.283$ max $W_x = 0.343$; min $W_y = 0.299$ max $W_y = 0.359$ Modify min $\theta_{\gamma+}$ to be 15 instead of 10; typ. to be 20 instead of 15 Modify min $\theta_{\gamma-}$ to be 35 instead of 30; typ. to be 40 instead of 35
Ver 2.0	Jun.18 '03	4	1.4	Modify "Haze" of "Surface Treatment" from 20 to 25
		13	7.1	Modify "Inverter Current" of "Test Condition" from 6.5 to 6.0
Ver 3.0	Jul. 3 '03	4	1.5	Modify "weight" of "mechanical specifications" typ. from 510 to 505; Max. from 525 to 520
		16		Add note (4) of section 6.2 - POWER ON/OFF SEQUENCE

1. GENERAL DESCRIPTION

1.1 OVERVIEW

N150X3 - L03 is a 15.0" TFT Liquid Crystal Display module with single CCFL Backlight unit and 30 pins LVDS interface. This module supports 1024 x 768 XGA mode and can display 262,144 colors. The optimum viewing angle is at 6 o'clock direction. The inverter module for Backlight is not built in.

1.2 FEATURES

- Thin and Light weight
- XGA (1024 x 768 pixels) resolution
- DE only mode
- 3.3V LVDS (Low Voltage Differential Signaling) interface with 1 pixel/clock

1.3 APPLICATION

- TFT LCD Notebook

1.4 GENERAL SPECIFICATIONS

Item	Specification	Unit	Note
Active Area	304.1 (H) x 228.1 (V) (15.0" diagonal)	mm	(1)
Bezel Opening Area	307.8 (H) x 231.6 (V)	mm	
Driver Element	a-si TFT active matrix	-	-
Pixel Number	1024 x R.G.B. x 768	pixel	-
Pixel Pitch	0.297 (H) x 0.297 (V)	mm	-
Pixel Arrangement	RGB vertical stripe	-	-
Display Colors	262,144	color	-
Transmissive Mode	Normally white	-	-
Surface Treatment	Hard coating (3H), Anti-glare (Haze 25)	-	-

1.5 MECHANICAL SPECIFICATIONS

Item		Min.	Typ.	Max.	Unit	Note
Module Size	Horizontal(H)	316.8	317.3	317.8	mm	(1)
	Vertical(V)	241.5	242	242.5	mm	
	Depth(D)	-	5.7	6.0	mm	
Weight		-	505	520	g	-

Note (1) Please refer to the attached drawings for more information of front and back outline dimensions.

2. ABSOLUTE MAXIMUM RATINGS

2.1 ABSOLUTE RATINGS OF ENVIRONMENT

Item	Symbol	Value		Unit	Note
		Min.	Max.		
Storage Temperature	T _{ST}	-20	+60	°C	(1)
Operating Ambient Temperature	T _{OP}	0	+50	°C	(1), (2)
Shock (Non-Operating)	S _{NOP}	-	240	G	(3), (5)
Vibration (Non-Operating)	V _{NOP}	-	2.0	G	(4), (5)

Note (1) Temperature and relative humidity range is shown in the figure below.

(a) 90 %RH Max. ($T_a \leq 40$ °C).

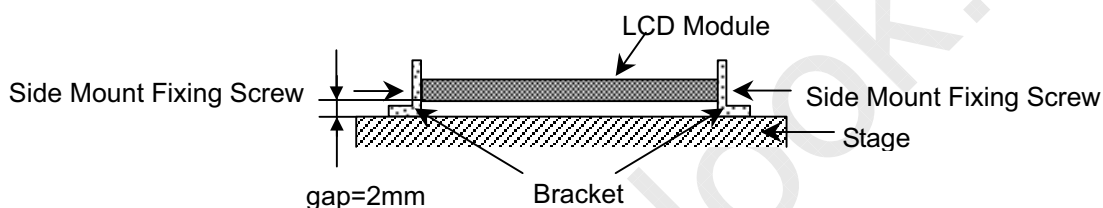
(b) Wet-bulb temperature should be 39 °C Max. ($T_a > 40$ °C).

(c) No condensation.

Note (2) The ambient temperature means the temperature of panel surface.

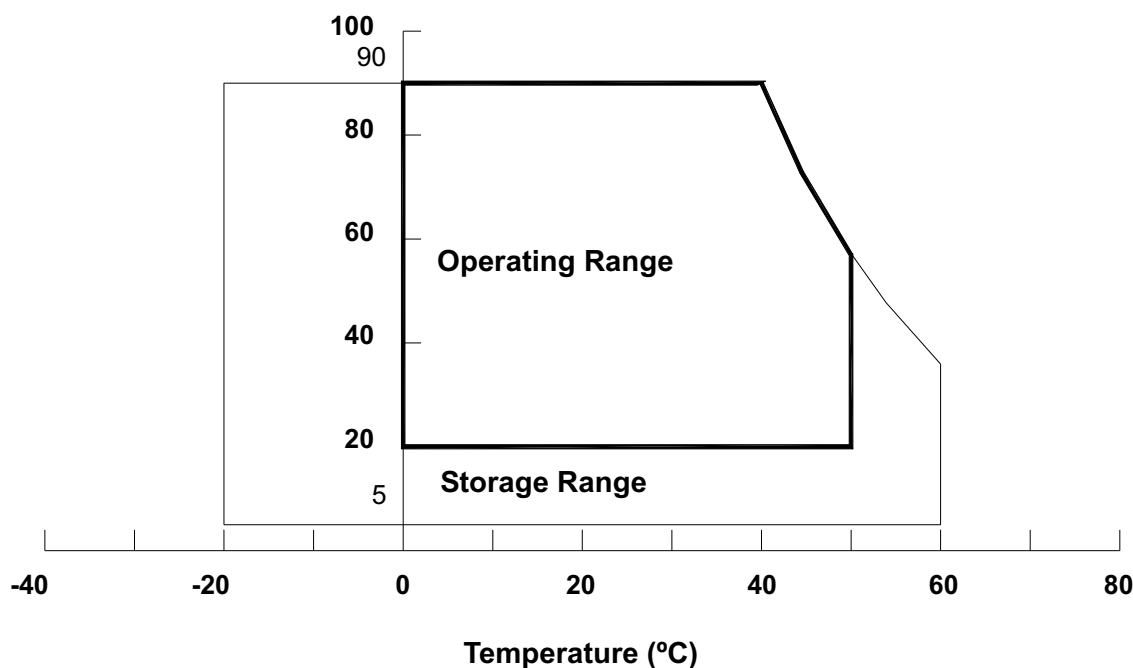
Note (3) 2ms, half sine wave, 1 times for $\pm X$, $\pm Y$, $\pm Z$.

Note (4) 10 ~ 500 Hz, 0.5 Hr/Cycle, (4) cycles each X, Y, Z. The fixing condition is shown as below:



Note (5) At testing Vibration and Shock, the fixture in holding the module has to be hard and rigid enough so that the module would not be twisted or bent by the fixture.

Relative Humidity (%RH)



2.2.1 TFT LCD MODULE

Item	Symbol	Value		Unit	Note
		Min.	Max.		
Power Supply Voltage	V_{CC}	-0.3	+4.0	V	(1)
Logic Input Voltage	V_{IN}	-0.3	$V_{CC}+0.3$	V	

2.2.2 BACKLIGHT UNIT

Item	Symbol	Value		Unit	Note
		Min.	Max.		
Lamp Voltage	V_L	-	2.5K	V_{RMS}	(1), (2), $I_L = 6.0 \text{ mA}$
Lamp Current	I_L	-	7.0	mA_{RMS}	
Lamp Frequency	F_L	-	80	KHz	(1), (2)

Note (1) Permanent damage to the device may occur if maximum values are exceeded. Function operation should be restricted to the conditions described under Normal Operating Conditions.

Note (2) Specified values are for lamp (Refer to 3.2 for further information).

3. ELECTRICAL CHARACTERISTICS

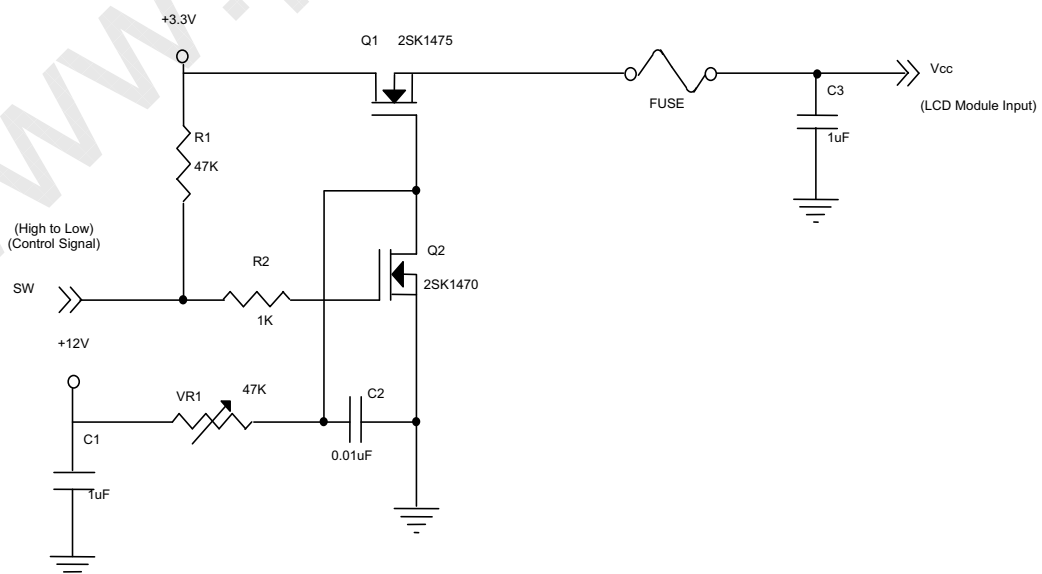
3.1 TFT LCD MODULE

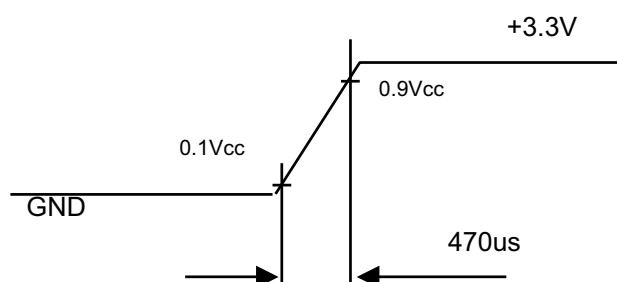
 $T_a = 25 \pm 2 \text{ }^\circ\text{C}$

Parameter	Symbol	Value			Unit	Note	
		Min.	Typ.	Max.			
Power Supply Voltage	V_{CC}	3.0	3.3	3.6	V	-	
Ripple Voltage	V_{RP}	-	50		mV	-	
Rush Current	I_{RUSH}	-		1.5	A	(2)	
Power Supply Current	White	I_{CC}	-	300	mA	(3)a	
	Black		-	400			
Logical Input Voltage	"H" Level	V_{IL}	-	-	+100	mV	-
	"L" Level	V_{IH}	-100	-	-	mV	-
Terminating Resistor	R_T	-	100	-	Ohm	-	

Note (1) The module should be always operated within above ranges.

Note (2) Measurement Conditions:



Vcc rising time is 470us

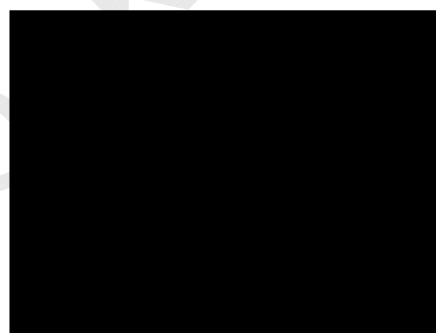
Note (3) The specified power supply current is under the conditions at $V_{cc} = 3.3 \text{ V}$, $T_a = 25 \pm 2 \text{ }^\circ\text{C}$, $f_v = 60 \text{ Hz}$, whereas a power dissipation check pattern below is displayed.

a. White Pattern



Active Area

b. Black Pattern



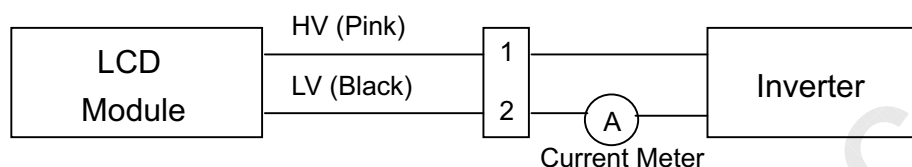
Active Area

3.2 BACKLIGHT UNIT

Ta = 25 ± 2 °C

Parameter	Symbol	Value			Unit	Note
		Min.	Typ.	Max.		
Lamp Input Voltage	V _L	608	675	743	V _{RMS}	I _L = 6.0 mA
Lamp Current	I _L	2.0	6.5	7.0	mA _{RMS}	(1)
Lamp Turn On Voltage	V _s	-		1360 (25 °C)	V _{RMS}	(2)
		-		1500 (0 °C)	V _{RMS}	(2)
Operating Frequency	F _L	40	50	67	KHz	(3)
Lamp Life Time	L _{BL}	10,000		-	Hrs	(5)
Power Consumption	P _L	-	4.1	-	W	(4), I _L = 6.0 mA

Note (1) Lamp current is measured by utilizing a high frequency current meter as shown below:



Note (2) The voltage shown above should be applied to the lamp for more than 1 second after startup. Otherwise the lamp may not be turned on.

Note (3) The lamp frequency may produce interference with horizontal synchronous frequency from the display, and this may cause line flow on the display. In order to avoid interference, the lamp frequency should be detached from the horizontal synchronous frequency and its harmonics as far as possible.

Note (4) $P_L = I_L \times V_L$

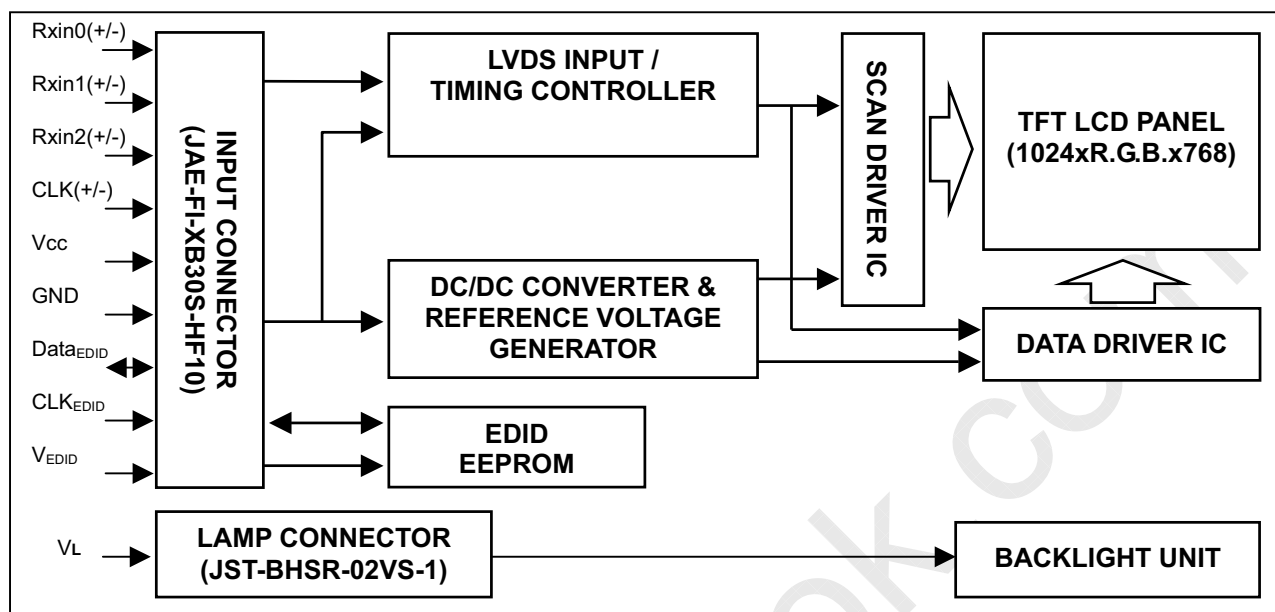
Note (5) The lifetime of lamp can be defined as the time in which it continues to operate under the condition Ta = 25 ± 2 °C and I_L = 6.0 mA_{RMS} until one of the following events occurs:

- When the brightness becomes or lower than 50% of its original value.
- When the effective ignition length becomes or lower than 80% of its original value. (Effective ignition length is defined as an area that has less than 70% brightness compared to the brightness in the center point.)

Note (6) The waveform of the voltage output of inverter must be area-symmetric and the design of the inverter must have specifications for the modularized lamp. The performance of the Backlight, such as lifetime or brightness, is greatly influenced by the characteristics of the DC-AC inverter for the lamp. All the parameters of an inverter should be carefully designed to avoid producing too much current leakage from high voltage output of the inverter. When designing or ordering the inverter please make sure that a poor lighting caused by the mismatch of the Backlight and the inverter (miss-lighting, flicker, etc.) never occurs. If the above situation is confirmed, the module should be operated in the same manners when it is installed in your instrument.

4. BLOCK DIAGRAM

4.1 TFT LCD MODULE



4.2 BACKLIGHT UNIT



5. INPUT TERMINAL PIN ASSIGNMENT

5.1 TFT LCD MODULE

Pin	Symbol	Description	Polarity	Remark
1	V _{SS}	Ground		
2	V _{CC}	Power Supply +3.3 V (typical)		
3	V _{CC}	Power Supply +3.3 V (typical)		
4	V _{EDID}	DDC 3.3V Power		DDC 3.3V Power
5	NC	Non-Connection		
6	CLK _{EDID}	DDC Clock		DDC Clock
7	DATA _{EDID}	DDC Data		DDC Data
8	Rxin0-	LVDS Differential Data Input	Negative	R0~R5,G0
9	Rxin0+	LVDS Differential Data Input	Positive	-
10	V _{SS}	Ground		
11	Rxin1-	LVDS Differential Data Input	Negative	G1~G5,B0,B1
12	Rxin1+	LVDS Differential Data Input	Positive	-
13	V _{SS}	Ground		
14	Rxin2-	LVDS Differential Data Input	Negative	B2~B5,DE,Hsync,Vsync

15	Rxin2+	LVDS Differential Data Input	Positive	
16	Vss	Ground		
17	CLK-	LVDS Clock Data Input	Negative	LVDS Level Clock
18	CLK+	LVDS Clock Data Input	Positive	
19	Vss	Ground		
20	NC	Non-Connection		
21	NC	Non-Connection		
22	Vss	Ground		
23	NC	Non-Connection		
24	NC	Non-Connection		
25	Vss	Ground		
26	NC	Non-Connection		
27	NC	Non-Connection		
28	Vss	Ground		
29	NC	Non-Connection		
30	NC	Non-Connection		

Note (1) Connector Part No.: JAE-FI-XB30S-HF10 or equivalent

Note (2) User's connector Part No: FI-X30M or equivalent

Note (3) The first pixel is even.

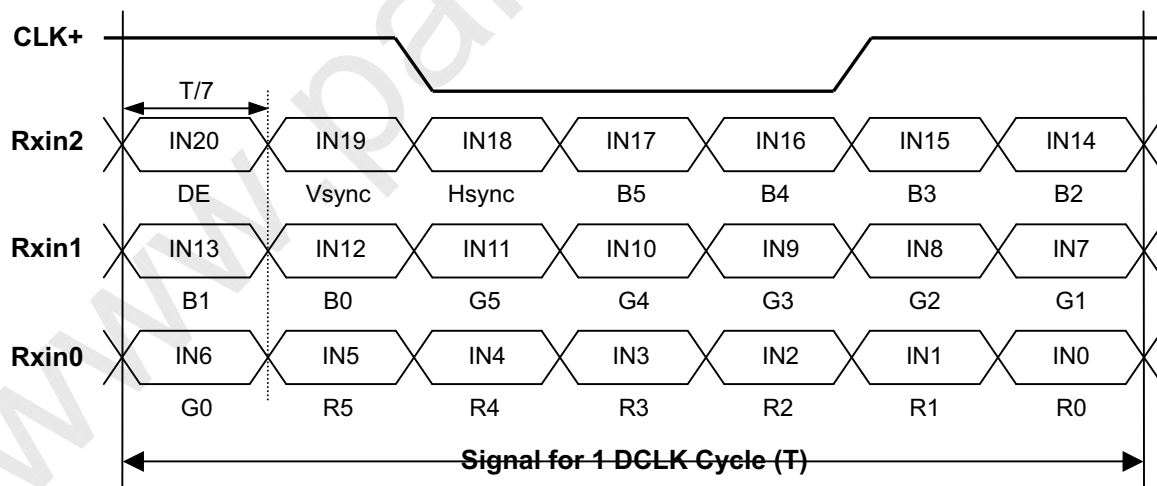
5.2 BACKLIGHT UNIT

Pin	Symbol	Description	Color
1	HV	High Voltage	Pink
2	LV	Ground	Black

Note (1) Connector Part No.: BHSR-02VS-1 (JST) or equivalent

Note (2) User's connector Part No.: SM02B-BHSS-1-TB (JST) or equivalent

5.3 TIMING DIAGRAM OF LVDS INPUT SIGNAL



5.4 COLOR DATA INPUT ASSIGNMENT

The brightness of each primary color (red, green and blue) is based on the 6-bit gray scale data input for the color. The higher the binary input, the brighter the color. The table below provides the assignment of color versus data input.

Color		Data Signal																	
		Red						Green						Blue					
		R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	B4	B3	B2	B1	B0
Basic Colors	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	0	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Gray Scale Of Red	Red(0)/Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(1)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red(2)	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
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	Red(61)	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red(62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
Red(63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	
Gray Scale Of Green	Green(0)/Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(1)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
	Green(2)	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
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	Green(61)	0	0	0	0	0	0	1	1	1	1	0	1	0	0	0	0	0	0
	Green(62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
Green(63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0	
Gray Scale Of Blue	Blue(0)/Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Blue(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
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	Blue(61)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	0	1
	Blue(62)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
Blue(63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	

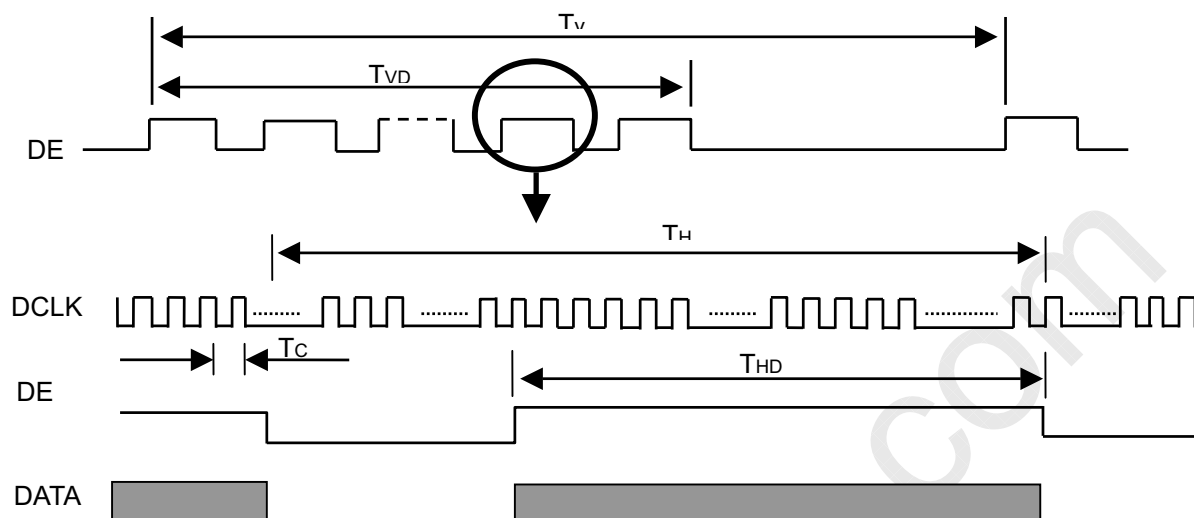
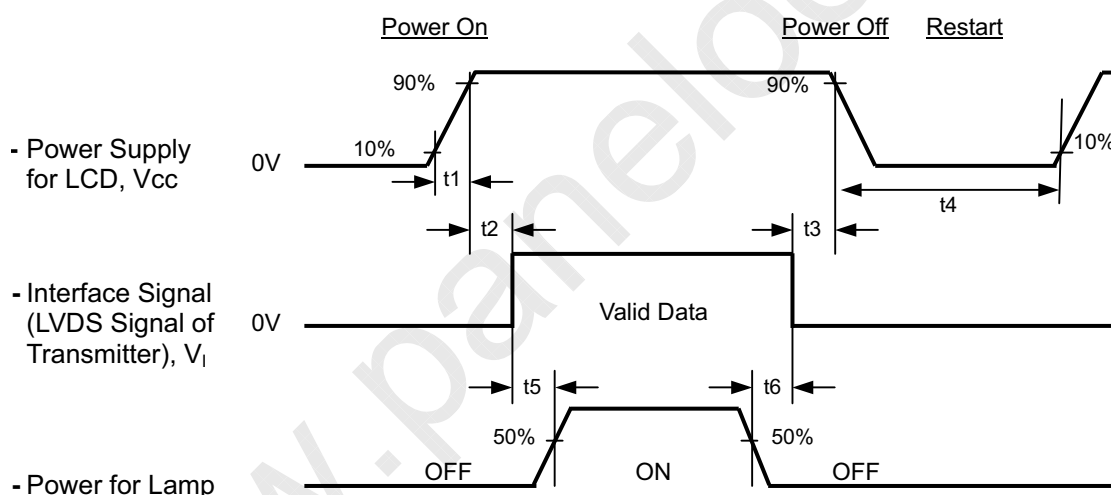
Note (1) 0: Low Level Voltage, 1: High Level Voltage

6. INTERFACE TIMING

6.1 INPUT SIGNAL TIMING SPECIFICATIONS

The specifications of input signal timing are as the following table and timing diagram.

Signal	Item	Symbol	Min.	Typ.	Max.	Unit	Note
DCLK	Frequency	1/Tc	50	65	68	MHz	-
DE	Vertical Total Time	TV	771	806	850	TH	-
	Vertical Addressing Time	TVD	768	768	768	TH	-
	Horizontal Total Time	TH	1200	1344	1500	Tc	-
	Horizontal Addressing Time	THD	1024	1024	1024	Tc	-

INPUT SIGNAL TIMING DIAGRAM**6.2 POWER ON/OFF SEQUENCE****Timing Specifications:**

$$0 < t_1 \leq 10 \text{ msec}$$

$$0 < t_2 \leq 50 \text{ msec}$$

$$0 < t_3 \leq 50 \text{ msec}$$

$$t_4 \geq 500 \text{ msec}$$

$$t_5 \geq 200 \text{ msec}$$

$$t_6 \geq 200 \text{ msec}$$

Note (1) Please avoid floating state of interface signal at invalid period.

Note (2) When the interface signal is invalid, be sure to pull down the power supply of LCD V_{cc} to 0 V.

Note (3) The Backlight inverter power must be turned on after the power supply for the logic and the interface signal is valid. The Backlight inverter power must be turned off before the power supply for the logic and the interface signal is invalid.

Note (4) It is recommended that t1 is the longer the better. If t1 is too short (e.g less then 470us), there might be an in-rush current (more than 1.5A) as Vcc rising from 0 to 3.3V.

7. OPTICAL CHARACTERISTICS

7.1 TEST CONDITIONS

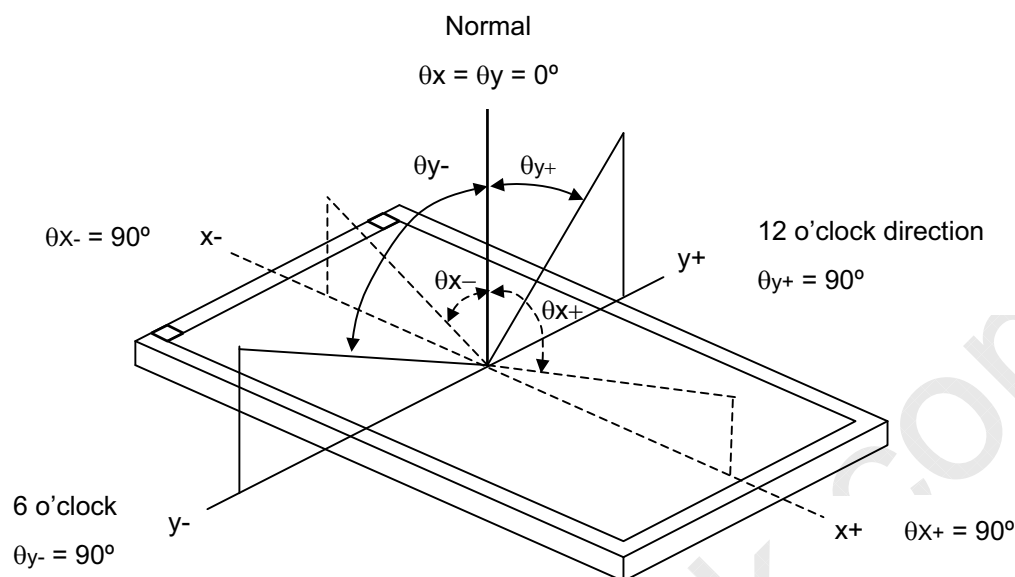
Item	Symbol	Value	Unit
Ambient Temperature	Ta	25±2	°C
Ambient Humidity	Ha	50±10	%RH
Supply Voltage	V _{CC}	3.3	V
Input Signal	According to typical value in "3. ELECTRICAL CHARACTERISTICS"		
Inverter Current	I _L	6.0	mA
Inverter Driving Frequency	F _L	50	KHz
Inverter	H05-4915		

The relative measurement methods of optical characteristics are shown in 6.2. The following items should be measured under the test conditions described in 6.1 and stable environment shown in Note (6).

7.2 OPTICAL SPECIFICATIONS

Item		Symbol	Condition	Min.	Typ.	Max.	Unit	Note
Contrast Ratio		CR	$\theta_x=0^\circ, \theta_y=0^\circ$ Viewing Normal Angle	250	350	-	-	(2), (6)
Response Time		T _R		-	6	10	ms	(3)
		T _F		-	17	25	ms	
Average Luminance of White		L _{AVE}		170	200	-	cd/m ²	(4), (6)
White Variation		δW		-	1.2	1.4	-	(6), (7)
Cross Talk		CT		-	-	3.0	%	(5), (6)
Color Chromaticity	Red	R _x		0.560	0.590	0.620	-	(1), (6)
		R _y		0.316	0.346	0.376	-	
	Green	G _x		0.286	0.316	0.346	-	
		G _y		0.504	0.534	0.564	-	
	Blue	B _x	0.119	0.149	0.179	-		
		B _y	0.101	0.131	0.161	-		
	White	W _x	0.283	0.313	0.343	-		
		W _y	0.299	0.329	0.359	-		
Viewing Angle	Horizontal	θ _{x+}	40	45		Deg.		
		θ _{x-}	40	45				
	Vertical	θ _{y+}	15	20				
		θ _{y-}	35	40				

Note (1) Definition of Viewing Angle (θ_x , θ_y):



Note (2) Definition of Contrast Ratio (CR):

The contrast ratio can be calculated by the following expression.

$$\text{Contrast Ratio (CR)} = L_{63} / L_0$$

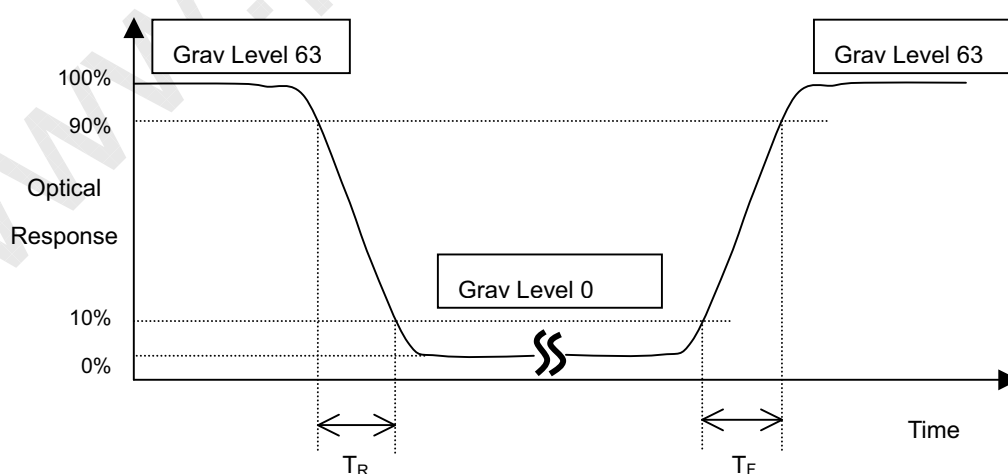
L₆₃: Luminance of gray level 63

L₀: Luminance of gray level 0

$$\text{CR} = \text{CR} (5)$$

CR (X) is corresponding to the Contrast Ratio of the point X at Figure in Note (7).

Note (3) Definition of Response Time (T_R , T_F):



Note (4) Definition of Average Luminance of White (L_{AVE}):

Measure the luminance of gray level 63 at 5 points

$$L_{AVE} = [L(1) + L(2) + L(3) + L(4) + L(5)] / 5$$

$L(x)$ is corresponding to the luminance of the point X at Figure in Note (7).

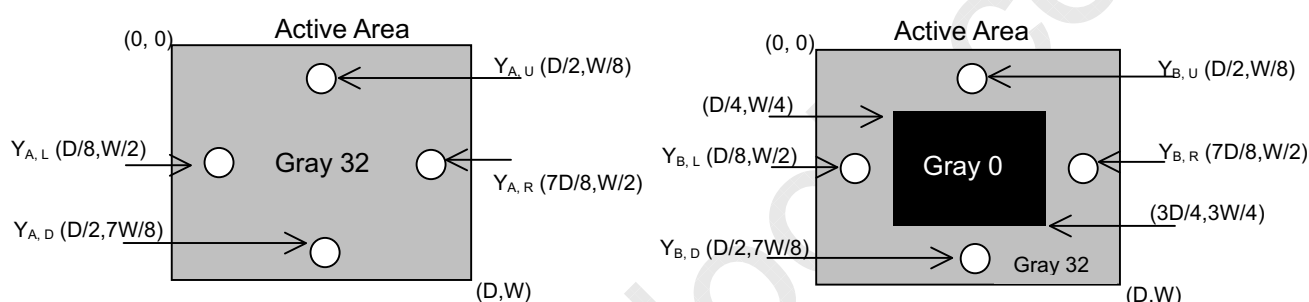
Note (5) Definition of Cross Talk (CT):

$$CT = |Y_B - Y_A| / Y_A \times 100 (\%)$$

Where:

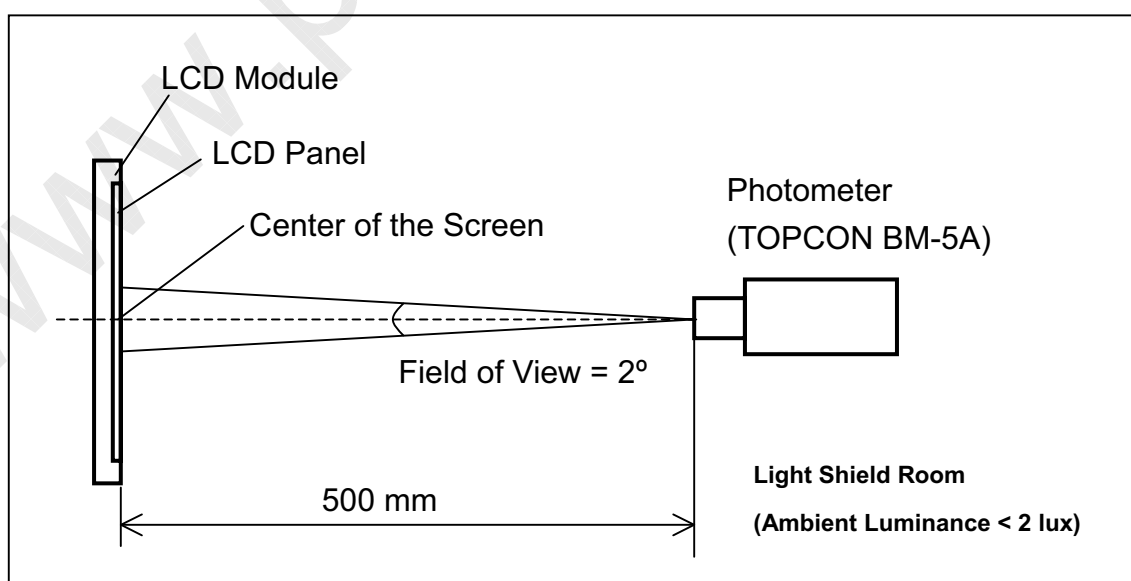
Y_A = Luminance of measured location without gray level 0 pattern (cd/m^2)

Y_B = Luminance of measured location with gray level 0 pattern (cd/m^2)



Note (6) Measurement Setup:

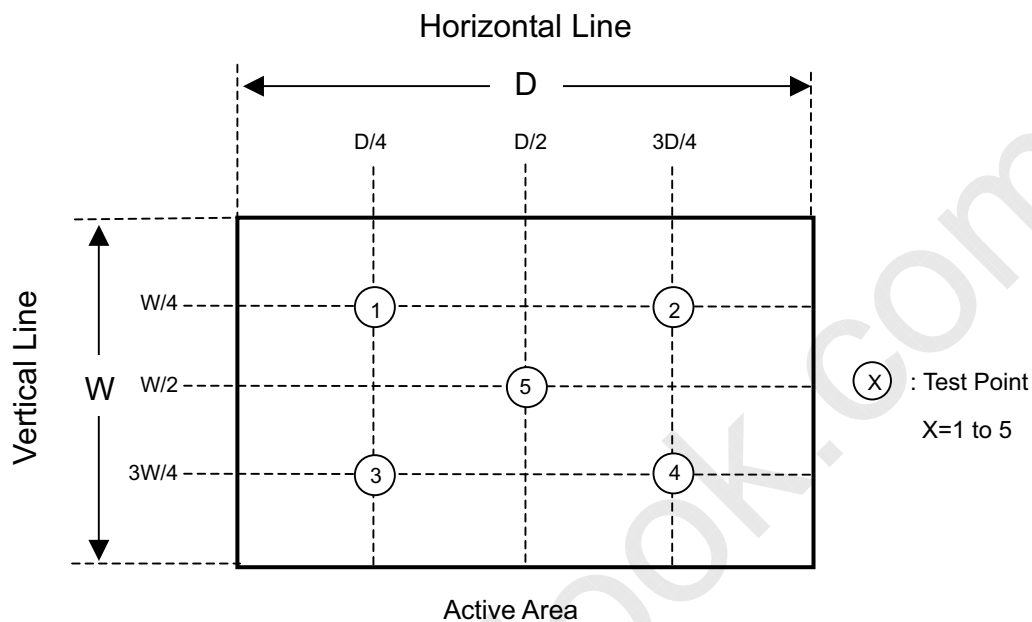
The LCD module should be stabilized at given temperature for 20 minutes to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting Backlight for 20 minutes in a windless room.



Note (7) Definition of White Variation (δW):

Measure the luminance of gray level 63 at 5 points

$$\delta W = \text{Maximum [L (1), L (2), L (3), L (4), L (5)]} / \text{Minimum [L (1), L (2), L (3), L (4), L (5)]}$$



8. PRECAUTIONS

8.1 ASSEMBLY AND HANDLING PRECAUTIONS

- (1) Do not apply rough force such as bending or twisting to the module during assembly.
- (2) To assemble or install module into user's system can be only in clean working areas. The dust and oil may cause electrical short or worsen the polarizer.
- (3) It's not permitted to have pressure or impulse on the module because the LCD panel and Backlight will be damaged.
- (4) Always follow the correct power sequence when LCD module is connecting and operating. This can prevent damage to the CMOS LSI chips during latch-up.
- (5) Do not pull the I/F connector in or out while the module is operating.
- (6) Do not disassemble the module.
- (7) Use a soft dry cloth without chemicals for cleaning, because the surface of polarizer is very soft and easily scratched.
- (8) It is dangerous that moisture come into or contacted the LCD module, because moisture may damage LCD module when it is operating.
- (9) High temperature or humidity may reduce the performance of module. Please store LCD module within the specified storage conditions.
- (10) When ambient temperature is lower than 10°C may reduce the display quality. For example, the response time will become slowly, and the starting voltage of CCFL will be higher than room temperature.

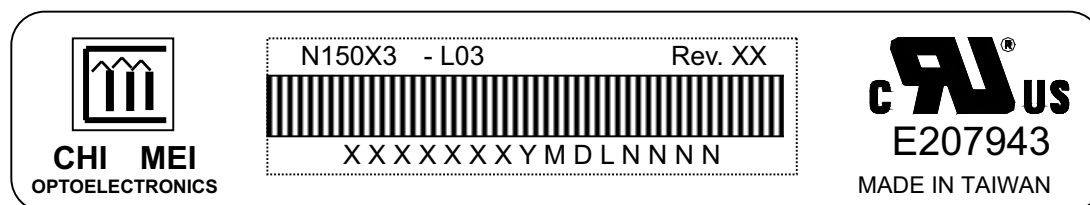
8.2 SAFETY PRECAUTIONS

- (1) The startup voltage of Backlight is approximately 1000 Volts. It may cause electrical shock while assembling with inverter. Do not disassemble the module or insert anything into the Backlight unit.
- (2) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contact with hands, skin or clothes, it has to be washed away thoroughly with soap.
- (3) After the module's end of life, it is not harmful in case of normal operation and storage.

9. DEFINITION OF LABELS

9.1 CMO MODULE LABEL

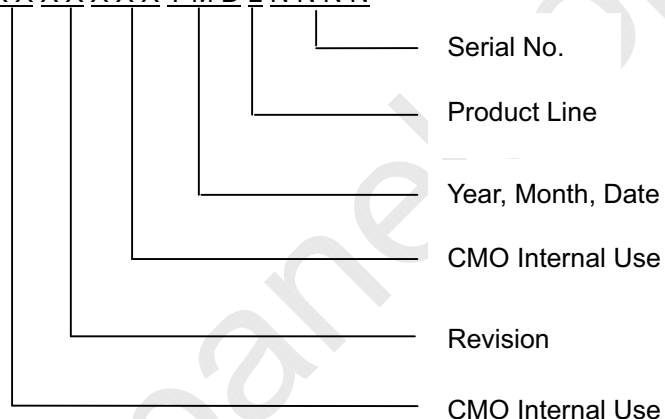
The barcode nameplate is pasted on each module as illustration, and its definitions are as following explanation.



- (a) Model Name: N150X3 - L03
- (b) Revision: Rev. XX: ES : A1, A2, A3, ..., etc.
CS & MP : C1, C2, C3, ..., etc.

Note: It will happen that revision code changed without product changed in developing duration because of CMO internal stage change, for example: AX → B1, BX → C1.

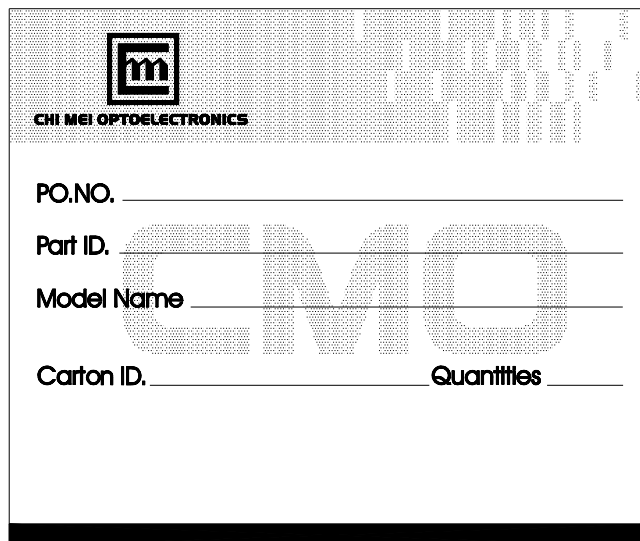
- (c) Serial ID: XXXXXXXXYMDLNNNN




Serial ID includes the information as below:

- (a) Manufactured Date: Year: 0 ~ 9, for 2000 ~ 2009.
Month: 1 ~ 9, A ~ C, for Jan. ~ Dec.
Day: 1 ~ 9, A ~ Y, for 1st to 31st, exclude I, O and U.
- (b) Revision Code: cover all the changes.
- (c) Serial No.: Manufacturing sequence of product.
- (d) Product Line: 1 -> Line1, 2 -> Line 2, ...etc.

9.2 CMO CARTON LABEL



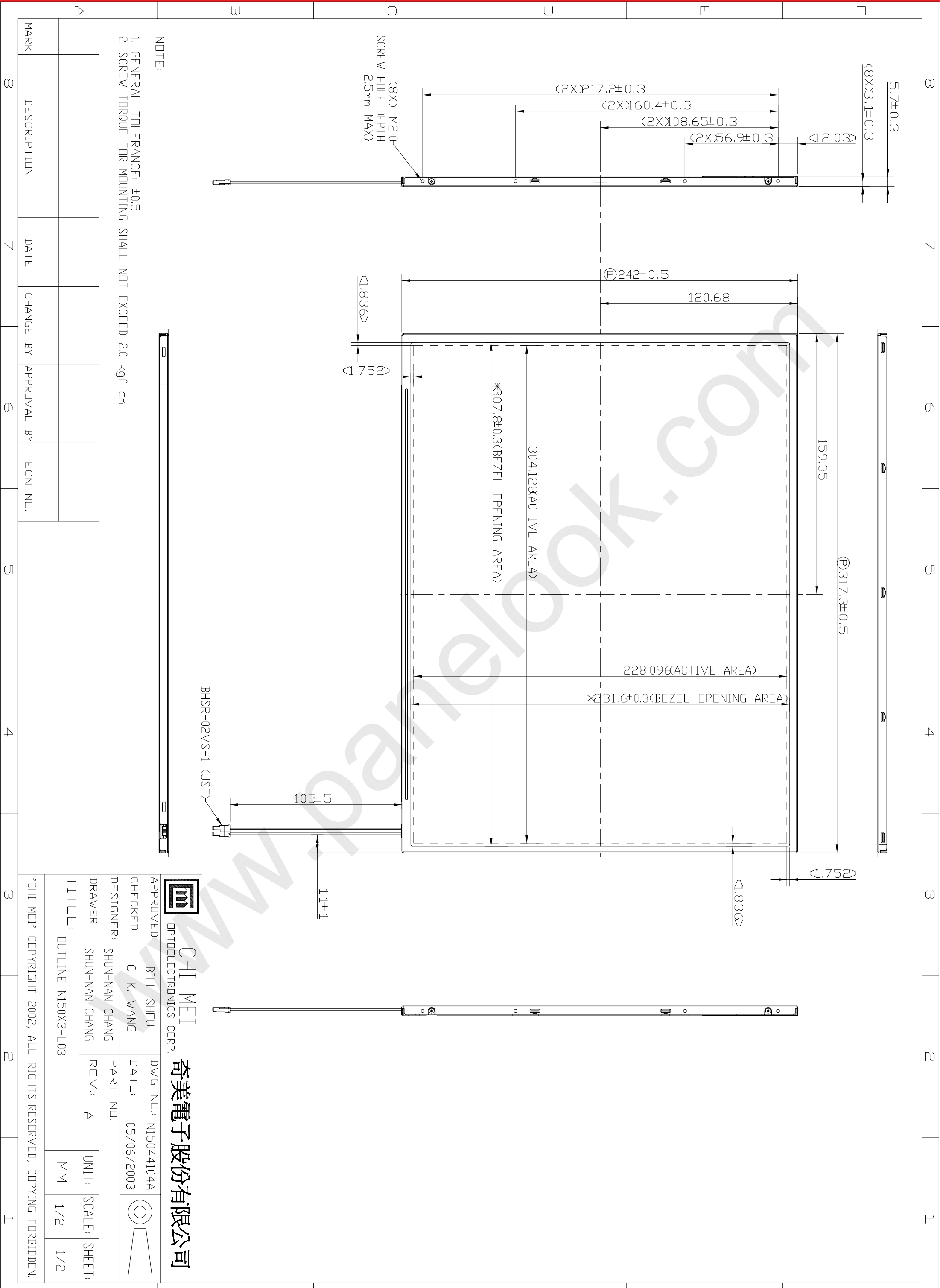

CHI MEI OPTOELECTRONICS

PO.NO. _____

Part ID. _____

Model Name _____

Carton ID. _____ Quantities _____

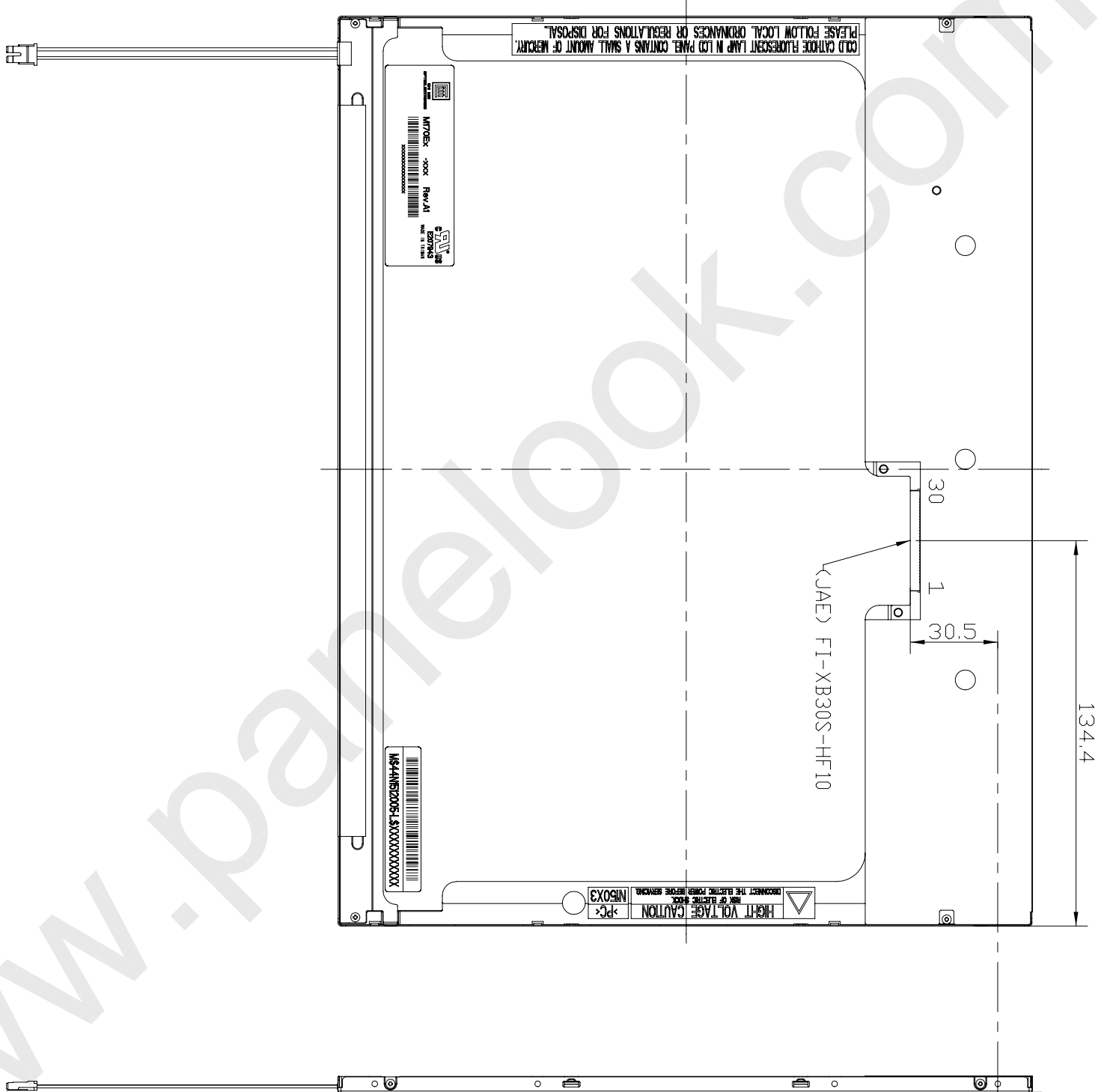


NOTE:
 1. GENERAL TOLERANCE: ±0.5
 2. SCREW TORQUE FOR MOUNTING SHALL NOT EXCEED 2.0 kgf-cm

MARK	DESCRIPTION	DATE	CHANGE BY	APPROVAL BY	ECN NO.
8					
7					
6					
5					
4					
3					
2					
1					

CHI MEI
OPTOELECTRONICS CORP. **奇美電子股份有限公司**

APPROVED:	BILL SHEU	DWG NO:	N15044104A	
CHECKED:	C. K. WANG	DATE:	05/06/2003	
DESIGNER:	SHUN-NAN CHANG	PART NO.:		
DRAWER:	SHUN-NAN CHANG	REV.:	A	UNIT: MM
TITLE:	DUTLINE N150X3-L03		SCALE:	1/2
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MARK	DESCRIPTION	DATE	CHANGE BY	APPROVAL BY	ECN NO.
8					
7					
6					
5					
4					
3					
2					
1					

CHI MEI
OPTOELECTRONICS CORP.

奇美電子股份有限公司

APPROVED: BILL SHEU DWG NO.: N15044104A

CHECKED: C K WANG DATE: 05/06/2003

DESIGNER: SHUN-NAN CHANG PART NO.:

DRAWER: SHUN-NAN CHANG REV.: A UNIT: SCALE: SHEET:

TITLE: OUTLINE N150X3-L03 MM 1/2 2/2

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