MARKING

XYZ



# **TinyLogic UHS Dual Buffer** with Schmitt Trigger Inputs

# NC7WZ17

### **Description**

The NC7WZ17 is a dual buffer with Schmitt trigger inputs from onsemi's Ultra–High Speed (UHS) series of TinyLogic products. The device is fabricated with advanced CMOS technology to achieve ultra–high speed with high output drive, while maintaining low static power dissipation over a very broad  $V_{CC}$  operating range. The device is specified to operate over the 1.65 V to 5.5 V  $V_{CC}$  range. The inputs and outputs are high–impedance when  $V_{CC}$  is 0 V. Inputs tolerate voltages up to 5.5 V, independent of  $V_{CC}$  operating voltage. Schmitt trigger inputs achieve 1 V typical hysteresis between the positive– and negative–going input threshold voltage at 5 V.

#### **Features**

- Ultra-High Speed:  $t_{PD}$  = 3.6 ns (Typical) into 50 pF at 5 V  $V_{CC}$
- High Output Drive: ±24 mA at 3 V V<sub>CC</sub>
- Broad V<sub>CC</sub> Operating Range: 1.65 V to 5.5 V
- Matches Performance of LCX when Operated at 3.3 V V<sub>CC</sub>
- Power Down High-Impedance Inputs / Outputs
- Over-Voltage Tolerance Inputs Facilitate 5 V to 3 V Translation
- Proprietary Noise / EMI Reduction Circuitry
- Ultra−Small MicroPak™ Packages
- Space-Saving SC-88 Package
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

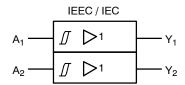
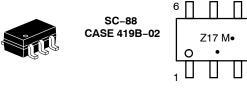


Figure 1. Logic Symbol

# DIAGRAMS SIP6 1.45x1.0 A5KK



CASE 127EB



A5, Z17 = Specific Device Code

KK = 2-Digit Lot Run Traceability Code
 XY = 2-Digit Date Code Format
 Z = Assembly Plant Code

M = Date Code

= Pb-Free Package

(Microdot may be in either location)

## ORDERING INFORMATION

See detailed ordering, marking and shipping information in the package dimensions section on page 6 of this data sheet.

NOTE: Some of the devices on this data sheet have been **DISCONTINUED**. Please refer to the table on page 6.

## **Pin Configurations**

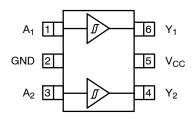


Figure 2. SC-88 (Top View)

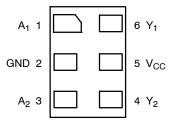
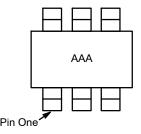


Figure 3. MicroPak (Top Through View)



## NOTES:

- AAA represents Product Code Top Mark (see ordering code).
   Orientation of Top Mark determines Pin One location. Read the top product code mark left to right, Pin One is the lower left pin.

Figure 4. SC-88 Pin 1 Orientation

## **PIN DEFINITIONS**

Pin # MicroPak	Name	Description
1	A <sub>1</sub>	Input
2	GND	Ground
3	A <sub>2</sub>	Input
4	Y <sub>2</sub>	Output
5	V <sub>CC</sub>	Supply Voltage
6	Y <sub>1</sub>	Output
	1 2 3 4 5	1 A <sub>1</sub> 2 GND 3 A <sub>2</sub> 4 Y <sub>2</sub> 5 V <sub>CC</sub>

## **FUNCTION TABLE** (Y = A)

Inputs	Output	
Α	Υ	
L	L	
Н	Н	

H = HIGH Logic Level L = LOW Logic Level

## NC7WZ17

## **ABSOLUTE MAXIMUM RATINGS**

Symbol	Paramet	er	Min	Max	Unit
V <sub>CC</sub>	Supply Voltage		-0.5	6.5	V
V <sub>IN</sub>	DC Input Voltage		-0.5	6.5	V
V <sub>OUT</sub>	DC Output Voltage		-0.5	6.5	V
I <sub>IK</sub>	DC Input Diode Current	V <sub>IN</sub> < 0 V	-	-50	mA
I <sub>OK</sub>	DC Output Diode Current	V <sub>OUT</sub> < 0 V	-	-50	mA
I <sub>OUT</sub>	DC Output Current	-	±50	mA	
I <sub>CC</sub> or I <sub>GND</sub>	DC V <sub>CC</sub> or Ground Current	-	±100	mA	
T <sub>STG</sub>	Storage Temperature Range		-65	+150	°C
TJ	Junction Temperature Under Bias		-	+150	°C
T <sub>L</sub>	Junction Lead Temperature (Solder	ing, 10 Seconds)	-	+260	°C
$P_{D}$	Power Dissipation in Still Air	SC-88	-	332	mW
		MicroPak-6	-	812	
		MicroPak2™-6	-	812	
ESD	Human Body Model, JEDEC: JESD22-A114		-	4000	V
	Charge Device Model, JEDEC: JES	SD22-C101	-	2000	

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

## **RECOMMENDED OPERATING CONDITIONS**

Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>CC</sub>	Supply Voltage Operating		1.65	5.50	V
	Supply Voltage Data Retention		1.50	5.5	
V <sub>IN</sub>	Input Voltage		0	5.5	V
V <sub>OUT</sub>	Output Voltage		0	V <sub>CC</sub>	V
T <sub>A</sub>	Operating Temperature		-40	+85	°C
$\theta_{\sf JA}$	Thermal Resistance	SC-88	-	377	°C/W
		MicroPak-6	-	154	
		MicroPak2-6	-	154	°C/W

Functional operation above the stresses listed in the Recommended Operating Ranges is not implied. Extended exposure to stresses beyond the Recommended Operating Ranges limits may affect device reliability.

3. Unused inputs must be held HIGH or LOW. They may not float.

## DC ELECTICAL CHARACTERISTICS

					T <sub>A</sub> = 25°C		$T_A = -40$	to 85°C	
Symbol	Parameter	V <sub>CC</sub> (V)	Conditions	Min	Тур	Max	Min	Max	Unit
V <sub>P</sub> Positive Threshold Voltage	Positive Threshold Voltage	1.65		-	1.00	1.40	-	1.40	V
	1.80		-	1.07	1.50	-	1.50		
		2.30		-	1.38	1.80	-	1.80	
		3.00		-	1.74	2.20	-	2.20	
		4.50		-	2.43	3.10	-	3.10	
		5.50		-	2.88	3.60	_	3.60	

## NC7WZ17

# DC ELECTICAL CHARACTERISTICS (continued)

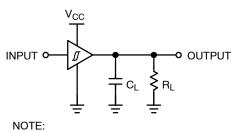
					T <sub>A</sub> = 25°C		$T_A = -40$	0 to 85°C	
Symbol	Parameter	V <sub>CC</sub> (V)	Conditions	Min	Тур	Max	Min	Max	Unit
V <sub>N</sub>	Negative Threshold Voltage	1.65		0.20	0.50	-	0.20	-	V
		1.80		0.25	0.56	-	0.25	-	
		2.30		0.40	0.75	-	0.40	-	
		3.00		0.60	0.98	-	0.60	-	
		4.50		1.00	1.42	_	1.00	-	
		5.50		1.20	1.68	_	1.20	-	
$V_{H}$	Hysteresis Voltage	1.65		0.10	0.48	0.90	0.10	0.90	V
		1.80		0.15	0.51	1.00	0.15	1.00	
		2.30		0.25	0.62	1.10	0.25	1.10	
		3.00		0.40	0.76	1.20	0.40	1.20	
		4.50		0.60	1.01	1.50	0.60	1.50	
		5.50		0.70	1.20	1.70	0.70	1.70	
V <sub>OH</sub>	HIGH Level Output Voltage	1.65	$V_{IN} = V_P \text{ or } V_N,$	1.55	1.65	-	1.55	-	V
		1.80	I <sub>OH</sub> = −100 μA	1.70	1.80	-	1.70	-	
		2.30		2.20	2.30	-	2.20	-	
		3.00	1	2.90	3.00	-	2.90	-	
		4.50	1	4.40	4.50	-	4.40	-	
		1.65	I <sub>OH</sub> = -4 mA	1.29	1.52	-	1.29	-	
		2.30	I <sub>OH</sub> = -8 mA	1.90	2.14	-	1.90	-	
		3.00	I <sub>OH</sub> = -16 mA	2.40	2.75	-	2.40	-	
		3.00	I <sub>OH</sub> = -24 mA	2.30	2.62	-	2.30	-	
		4.50	I <sub>OH</sub> = -32 mA	3.80	4.13	-	3.80	-	
V <sub>OL</sub>	LOW Level Output Voltage	1.65	$V_{IN} = V_P \text{ or } V_N$	-	0.00	0.10	_	0.10	V
		1.80	I <sub>OL</sub> = 100 μA	-	0.00	0.10	_	0.10	
		2.30		-	0.00	0.10	-	0.10	
		3.00		-	0.00	0.10	-	0.10	
		4.50		-	0.00	0.10	-	0.10	
		1.65	I <sub>OL</sub> = 4 mA	-	0.08	0.24	-	0.24	
		2.30	I <sub>OL</sub> = 8 mA	-	0.10	0.30	-	0.30	
		3.00	I <sub>OL</sub> = 16 mA	-	0.16	0.40	_	0.40	
		3.00	I <sub>OL</sub> = 24 mA	_	0.24	0.55	-	0.55	
		4.50	I <sub>OL</sub> = 32 mA	_	0.25	0.550	-	0.55	
I <sub>IN</sub>	Input Leakage Current	1.65 to 5.5	V <sub>IN</sub> = 5.5 V, GND	_	-	±0.1	-	±1.0	μΑ
I <sub>OFF</sub>	Power Off Leakage Current	0	V <sub>IN</sub> or V <sub>OUT</sub> = 5.5 V	_	-	1	-	10	μΑ
I <sub>CC</sub>	Quiescent Supply Current	1.65 to 5.50	V <sub>IN</sub> = 5.5 V, GND	-	-	1	-	10	μΑ

## **AC ELECTRICAL CHARACTERISTICS**

					T <sub>A</sub> = 25°C		$T_A = -40$	to 85°C	
Symbol	Parameter	V <sub>CC</sub> (V)	Conditions	Min	Тур	Max	Min	Max	Unit
t <sub>PLH</sub> , t <sub>PHL</sub>	Propagation Delay	1.65	C <sub>L</sub> = 15 pF,	-	8.3	14.3	-	15.8	ns
	(Figure 5, 6)	1.80	$R_L = 1 M\Omega$	-	6.9	11.9	-	13.1	
		2.50 ±0.20		-	4.8	8.2	-	9.0	
		3.30 ±0.30		-	3.7	5.6	-	6.2	
		5.00 ±0.50		-	3.0	4.7	-	5.2	
		3.30 ±0.30	C <sub>L</sub> = 50 pF,	-	4.3	6.6	-	7.3	
		5.00 ±0.50	$R_L = 500 \Omega$	-	3.6	5.6	-	6.2	
C <sub>IN</sub>	Input Capacitance	0		-	2.5	-	-	_	pF
C <sub>PD</sub>	Power Dissipation Capacitance	3.30		-	10.0	-	-	_	pF
	(Note 4) (Figure 7)	5.00		-	12.0	-	-	_	

<sup>4.</sup> C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is derived from dynamic operating current consumption (I<sub>CCD</sub>) at no output loading and operating at 50% duty cycle. C<sub>PD</sub> is related to I<sub>CCD</sub> dynamic operating current by the expression:

I<sub>CCD</sub> = (C<sub>PD</sub>) (V<sub>CC</sub>) (f<sub>IN</sub>) + (I<sub>CCstatic</sub>).



 C<sub>L</sub> includes load and stray capacitance; Input PRR = 1.0 MHz, t<sub>W</sub> = 500 ns

OUTF les load and stray capacitance;

**INPUT** 

OUTPUT

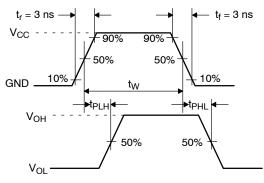
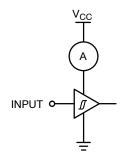


Figure 6. AC Waveforms





NOTE:

6. Input = AC Waveform;  $t_r = t_f = 1.8 \text{ ns}$ ; PRR = 10 MHz; Duty Cycle = 50%.

Figure 7. I<sub>CCD</sub> Test Circuit

## NC7WZ17

## **DEVICE ORDERING INFORMATION**

Device	Top Mark	Packages	Shipping <sup>†</sup>
NC7WZ17P6X	Z17	6-Lead SC70, EIAJ SC-88, 1.25 mm Wide	3000 / Tape & Reel
NC7WZ17L6X	A5	6-Lead MicroPak, 1.00 mm Wide	5000 / Tape & Reel
NC7WZ17FHX	A5	6-Lead, MicroPak2, 1x1 mm Body, .35 mm Pitch	5000 / Tape & Reel

## **DISCONTINUED** (Note 7)

NC7WZ17P6X-L22347	Z17	6-Lead SC70, EIAJ SC-88, 1.25 mm Wide	3000 / Tape & Reel	
NC7WZ17L6X-L22175	A5	6-Lead MicroPak, 1.00 mm Wide	5000 / Tape & Reel	
NC7WZ17FHX-L22175	A5	6-Lead, MicroPak2, 1x1 mm Body, .35 mm Pitch	5000 / Tape & Reel	

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

7. **DISCONTINUED:** These devices are not recommended for new design. Please contact your **onsemi** representative for information. The

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most current information on these devices may be available on www.onsemi.com.



**DATE 31 AUG 2016** 



NOTES:

- 1. CONFORMS TO JEDEC STANDARD MO-252 VARIATION UAAD
- 2. DIMENSIONS ARE IN MILLIMETERS
- 3. DRAWING CONFORMS TO ASME Y14.5M-2009
  4. PIN ONE IDENTIFIER IS 2X LENGTH OF ANY

  - OTHER LINE IN THE MARK CODE LAYOUT.

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DESCRIPTION:	SIP6 1.45X1.0		PAGE 1 OF 1		

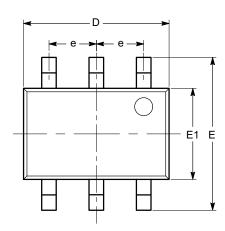
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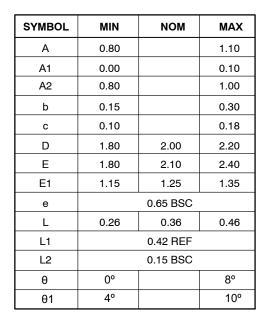


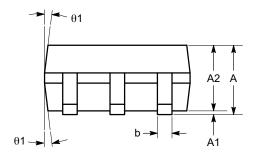
## SC-88 (SC-70 6 Lead), 1.25x2 CASE 419AD ISSUE A

**DATE 07 JUL 2010** 

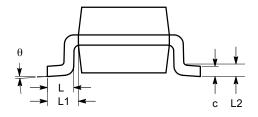


**TOP VIEW** 





SIDE VIEW



**END VIEW** 

#### Notes:

- (1) All dimensions are in millimeters. Angles in degrees.
- (2) Complies with JEDEC MO-203.

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E1

6X 0.30 -

e

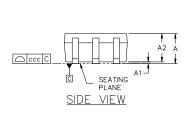
В

## SC-88 2.00x1.25x0.90, 0.65P CASE 419B-02 **ISSUE Z**

**DATE 18 APR 2024** 

#### NOTES:

- DIMENSIONING AND TOLERANCING CONFORM TO ASME Y14.5-2018.
- ALL DIMENSION ARE IN MILLIMETERS.
- DIMENSIONS D AND E1 DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS. MOLD FLASH, PROTRUSIONS, OR GATE BURRS SHALL NOT EXCEED 0.20 PER END.
- DIMENSIONS D AND E1 AT THE OUTERMOST EXTREMES OF THE PLASTIC BODY AND DATUM H.
  DATUMS A AND B ARE DETERMINED AT DATUM H.
- DIMENSIONS 6 AND C APPLY TO THE FLAT SECTION OF THE LEAD BETWEEN 0.08 AND 0.15 FROM THE TIP. 6.
- DIMENSION b DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.08 TOTAL IN EXCESS OF DIMENSION 6 AT MAXIMUM MATERIAL CONDITION. THE DAMBAR CANNOT BE LOCATED ON THE LOWER RADIUS OF THE FOOT.



TOP VIEW

∆aaa H A−B

<u></u> БЬБ С

⊕ ddd M C A−B D

6X 0.66

2.50





	MILLIMETERS				
DIM	MIN.	NOM.	MAX.		
Α			1.10		
A1	0.00		0.10		
A2	0.70	0.90	1.00		
b	0.15	0.20	0.25		
С	0.08	0.15	0.22		
D	2.00 BSC				
E		2.10 BSC	:		
E1		1.25 BSC	;		
е		0.65 BSC	;		
L	0.26	0.36	0.46		
L2	0.15 BSC				
aaa	0.15				
bbb	0.30				
ccc	0.10				
ddd		0.10			

# **GENERIC MARKING DIAGRAM\***



XXX = Specific Device Code = Date Code\*

= Pb-Free Package

(Note: Microdot may be in either location)

- \*Date Code orientation and/or position may vary depending upon manufacturing location.
- \*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

## RECOMMENDED MOUNTING FOOTPRINT\*

FOR ADDITIONAL INFORMATION ON OUR Pb-FREE STRATEGY AND SOLDERING DETAILS, PLEASE DOWNLOAD THE ONSEMI SOLDERING AND MOUNTING TECHNIQUES REFERENCE MANUAL, SOLDERRM/D.

## **STYLES ON PAGE 2**

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DESCRIPTION:	SC-88 2.00x1.25x0.90, 0.65P		PAGE 1 OF 2

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## SC-88 2.00x1.25x0.90, 0.65P CASE 419B-02 ISSUE Z

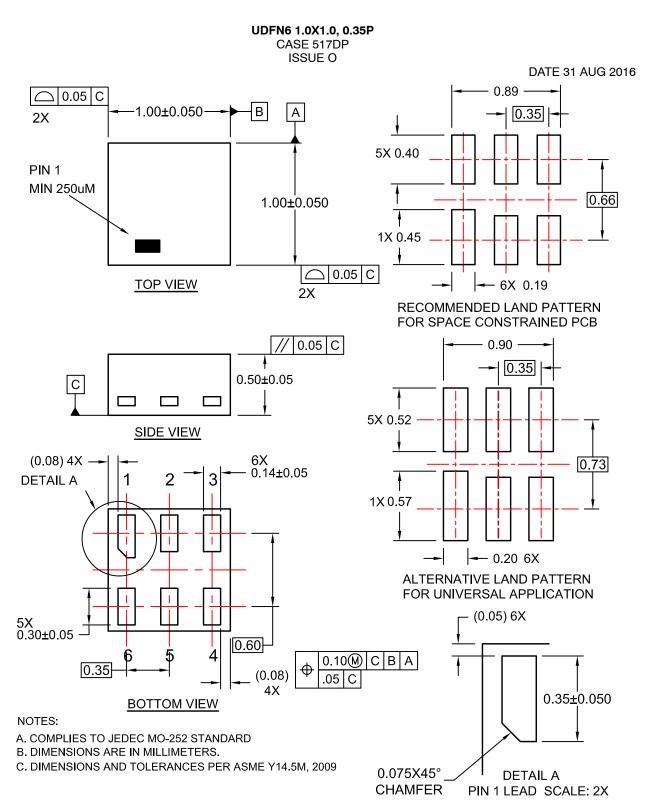
**DATE 18 APR 2024** 

STYLE 1: PIN 1. EMITTER 2 2. BASE 2 3. COLLECTOR 1 4. EMITTER 1 5. BASE 1 6. COLLECTOR 2	STYLE 2: CANCELLED	STYLE 3: CANCELLED	STYLE 4: PIN 1. CATHODE 2. CATHODE 3. COLLECTOR 4. EMITTER 5. BASE 6. ANODE	STYLE 5: PIN 1. ANODE 2. ANODE 3. COLLECTOR 4. EMITTER 5. BASE 6. CATHODE	STYLE 6: PIN 1. ANODE 2 2. N/C 3. CATHODE 1 4. ANODE 1 5. N/C 6. CATHODE 2
STYLE 7: PIN 1. SOURCE 2 2. DRAIN 2 3. GATE 1 4. SOURCE 1 5. DRAIN 1 6. GATE 2	STYLE 8: CANCELLED	STYLE 9: PIN 1. EMITTER 2 2. EMITTER 1 3. COLLECTOR 1 4. BASE 1 5. BASE 2 6. COLLECTOR 2	STYLE 10: PIN 1. SOURCE 2 2. SOURCE 1 3. GATE 1 4. DRAIN 1 5. DRAIN 2 6. GATE 2	STYLE 11: PIN 1. CATHODE 2 2. CATHODE 2 3. ANODE 1 4. CATHODE 1 5. CATHODE 1 6. ANODE 2	STYLE 12: PIN 1. ANODE 2 2. ANODE 2 3. CATHODE 1 4. ANODE 1 5. ANODE 1 6. CATHODE 2
STYLE 13: PIN 1. ANODE 2. N/C 3. COLLECTOR 4. EMITTER 5. BASE 6. CATHODE	STYLE 14: PIN 1. VREF 2. GND 3. GND 4. IOUT 5. VEN 6. VCC	STYLE 15: PIN 1. ANODE 1 2. ANODE 2 3. ANODE 3 4. CATHODE 3 5. CATHODE 2 6. CATHODE 1	STYLE 16: PIN 1. BASE 1 2. EMITTER 2 3. COLLECTOR 2 4. BASE 2 5. EMITTER 1 6. COLLECTOR 1	STYLE 17: PIN 1. BASE 1 2. EMITTER 1 3. COLLECTOR 2 4. BASE 2 5. EMITTER 2 6. COLLECTOR 1	STYLE 18: PIN 1. VIN1 2. VCC 3. VOUT2 4. VIN2 5. GND 6. VOUT1
STYLE 19: PIN 1. I OUT 2. GND 3. GND 4. V CC 5. V EN 6. V REF	STYLE 20: PIN 1. COLLECTOR 2. COLLECTOR 3. BASE 4. EMITTER 5. COLLECTOR 6. COLLECTOR	STYLE 21: PIN 1. ANODE 1 2. N/C 3. ANODE 2 4. CATHODE 2 5. N/C 6. CATHODE 1	STYLE 22: PIN 1. D1 (i) 2. GND 3. D2 (i) 4. D2 (c) 5. VBUS 6. D1 (c)	STYLE 23: PIN 1. Vn 2. CH1 3. Vp 4. N/C 5. CH2 6. N/C	STYLE 24: PIN 1. CATHODE 2. ANODE 3. CATHODE 4. CATHODE 5. CATHODE 6. CATHODE
STYLE 25: PIN 1. BASE 1 2. CATHODE 3. COLLECTOR 2 4. BASE 2 5. EMITTER 6. COLLECTOR 1	STYLE 26: PIN 1. SOURCE 1 2. GATE 1 3. DRAIN 2 4. SOURCE 2 5. GATE 2 6. DRAIN 1	STYLE 27: PIN 1. BASE 2 2. BASE 1 3. COLLECTOR 1 4. EMITTER 1 5. EMITTER 2 6. COLLECTOR 2	STYLE 28: PIN 1. DRAIN 2. DRAIN 3. GATE 4. SOURCE 5. DRAIN 6. DRAIN	STYLE 29: PIN 1. ANODE 2. ANODE 3. COLLECTOR 4. EMITTER 5. BASE/ANODE 6. CATHODE	STYLE 30: PIN 1. SOURCE 1 2. DRAIN 2 3. DRAIN 2 4. SOURCE 2 5. GATE 1 6. DRAIN 1

Note: Please refer to datasheet for style callout. If style type is not called out in the datasheet refer to the device datasheet pinout or pin assignment.

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