

### NCE P-Channel Enhancement Mode Power MOSFET

### **Description**

The NCE2321 uses advanced trench technology to provide excellent  $R_{\rm DS(ON)}$ , low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a load switch or in PWM applications.

### **General Features**

•  $V_{DS} = -20V, I_D = -3.9A$ 

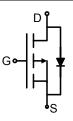
 $R_{DS(ON)}$  <70m $\Omega$  @  $V_{GS}$ =-2.5V

 $R_{DS(ON)} < 50 \text{m}\Omega$  @  $V_{GS}$ =-4.5V

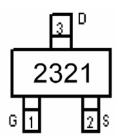
- High power and current handing capability
- Lead free product is acquired
- Surface mount package

## **Application**

- PA switch
- Load switch
- Power management



Schematic diagram



Marking and pin assignment



SOT-23 top view

### **Package Marking and Ordering Information**

<b>Device Marking</b>	Device	Device Package	Reel Size	Tape width	Quantity
2321	NCE2321	SOT-23	Ø180mm	8 mm	3000 units

### Absolute Maximum Ratings (T<sub>A</sub>=25 ℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	VDS	-20	V
Gate-Source Voltage	V <sub>G</sub> s	±12	V
Drain Current -Continuous	I <sub>D</sub>	-3.9	Α
Drain Current -Pulsed (Note 1)	I <sub>DM</sub>	-12	Α
Maximum Power Dissipation	P <sub>D</sub>	1.4	W
Operating Junction and Storage Temperature Range	$T_{J}, T_{STG}$	-55 To 150	$^{\circ}$

#### **Thermal Characteristic**

Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{\theta JA}$	89	°C/W
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# Electrical Characteristics (T<sub>A</sub>=25 <sup>°</sup>C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit	
Off Characteristics							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =-250μA	-20	-	-	V	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-20V,V <sub>GS</sub> =0V	-	-	-1	μA	
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±12V,V <sub>DS</sub> =0V	-	-	±100	nA	
On Characteristics (Note 3)							
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> ,I <sub>D</sub> =-250μA	-0.45	-0.7	-1.0	V	
Davis Course On Otata Basistana	_	V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-3.3A	-	37	50	mΩ	
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =-2.5V, I <sub>D</sub> =-3.0A	-	48	70		
Forward Transconductance	<b>g</b> FS	V <sub>DS</sub> =-5V,I <sub>D</sub> =-3.0A	5	-	-	S	
Dynamic Characteristics (Note4)		_				•	
Input Capacitance	C <sub>lss</sub>	\/ - 40\/\/ -0\/	-	560	-	PF	
Output Capacitance	C <sub>oss</sub>	$V_{DS}$ =-10V, $V_{GS}$ =0V, F=1.0MHz	-	80	-	PF	
Reverse Transfer Capacitance	C <sub>rss</sub>	F-1.UIVIDZ	-	70	-	PF	
Switching Characteristics (Note 4)							
Turn-on Delay Time	t <sub>d(on)</sub>		-	12	-	nS	
Turn-on Rise Time	t <sub>r</sub>	$V_{DD}$ =-10V, $I_{D}$ =-3.3A ,	-	35	-	nS	
Turn-Off Delay Time	t <sub>d(off)</sub>	$R_L$ =2.2 $\Omega$ , $V_{GS}$ =-4.5 $V$ , $R_g$ =6 $\Omega$	-	55	-	nS	
Turn-Off Fall Time	t <sub>f</sub>		-	40	-	nS	
Total Gate Charge	Qg		-	8.5	-	nC	
Gate-Source Charge	e Charge $Q_{gs}$ $V_{DS}$ =-10V, $I_D$ =-3.3A, $V_{GS}$ =-4.5V		-	1.2	-	nC	
Gate-Drain Charge	$Q_{gd}$		-	2.1	-	nC	
Drain-Source Diode Characteristics							
Diode Forward Voltage (Note 3)	V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>S</sub> =-3.9A	-	-	-1.2	V	
Diode Forward Current (Note 2)	Is		-	-	-3.9	Α	

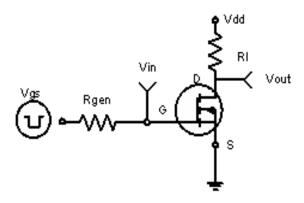
### Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- 3. Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
- 4. Guaranteed by design, not subject to production

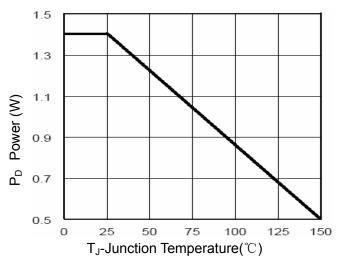
50%



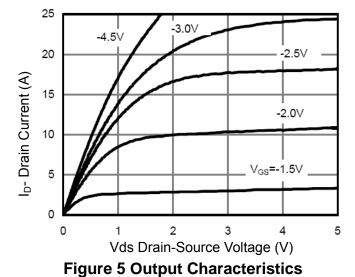
## **Typical Electrical and Thermal Characteristics**



**Figure 1:Switching Test Circuit** 



**Figure 3 Power Dissipation** 

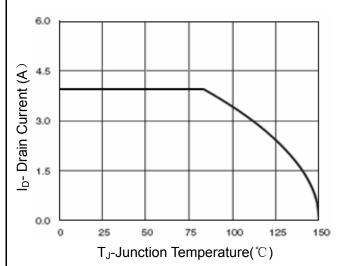


 $t_{d(on)} \rightarrow t_{on} \rightarrow t_{off} \rightarrow t_{d(off)} \rightarrow t_{f} \rightarrow$ 

Figure 2:Switching Waveforms

**PULSE WIDTH** 

V<sub>IN</sub>



**Figure 4 Drain Current** 

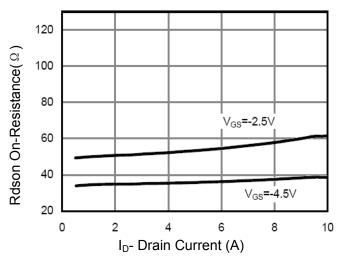
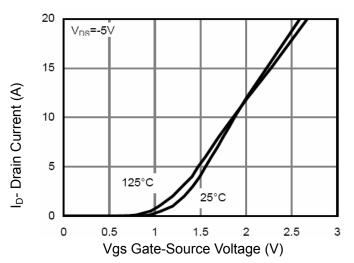


Figure 6 Drain-Source On-Resistance





**Figure 7 Transfer Characteristics** 

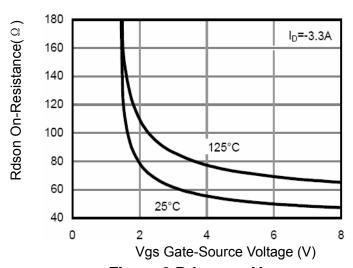


Figure 9 Rdson vs Vgs

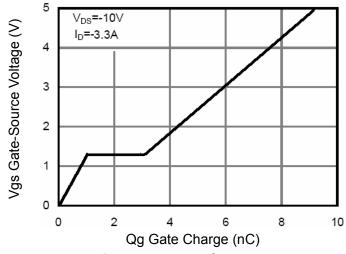


Figure 11 Gate Charge

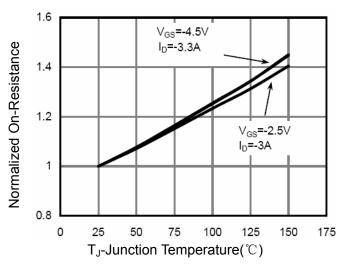


Figure 8 Drain-Source On-Resistance

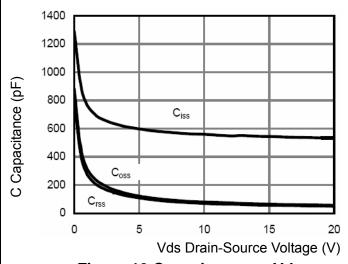


Figure 10 Capacitance vs Vds

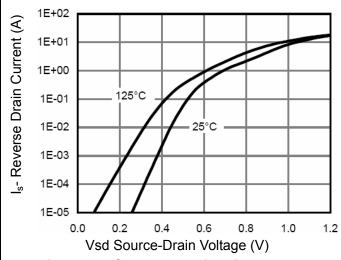
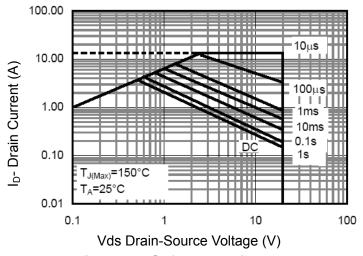
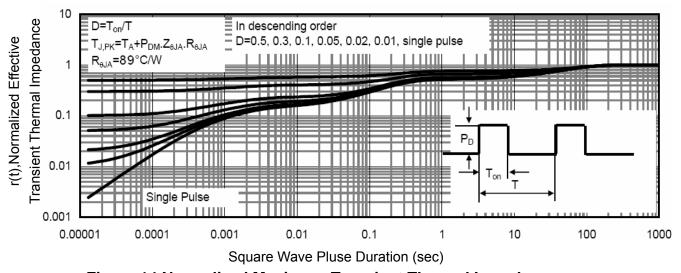


Figure 12 Source- Drain Diode Forward





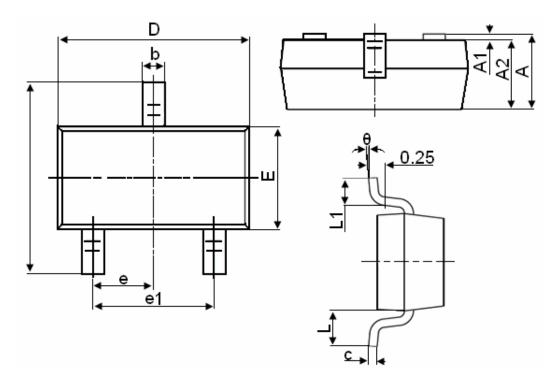
**Figure 13 Safe Operation Area** 



**Figure 14 Normalized Maximum Transient Thermal Impedance** 



# **SOT-23 Package Information**



Symbol	Dimensions in Millimeters				
Symbol	MIN.	MAX.			
Α	0.900	1.150			
A1	0.000	0.100			
A2	0.900	1.050			
b	0.300	0.500			
С	0.080	0.150			
D	2.800	3.000			
E	1.200	1.400			
E1	2.250	2.550			
е	0.950TYP				
e1	1.800	2.000			
L	0.550REF				
L1	0.300	0.500			
θ	0°	8°			

### Notes

- 1. All dimensions are in millimeters.
- 2. Tolerance ±0.10mm (4 mil) unless otherwise specified
- 3. Package body sizes exclude mold flash and gate burrs. Mold flash at the non-lead sides should be less than 5 mils.
- 4. Dimension L is measured in gauge plane.
- $5. \ Controlling \ dimension \ is \ millimeter, \ converted \ inch \ dimensions \ are \ not \ necessarily \ exact.$



### http://www.ncepower.com

NCE2321

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