

## N and P-Channel Enhancement Mode Power MOSFET

### Description

The NCE30NP07S uses advanced trench technology to provide excellent  $R_{DS(ON)}$  and low gate charge. The complementary MOSFETs may be used to form a level shifted high side switch, and for a host of other applications.

### General Features

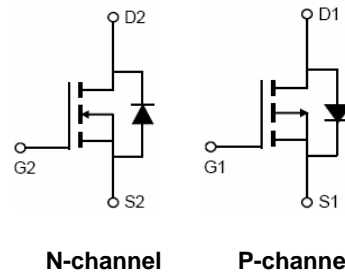
#### ● N-Channel

- $V_{DS} = 30V, I_D = 6.5A$
- $R_{DS(ON)} < 24m\Omega @ V_{GS}=10V$
- $R_{DS(ON)} < 37m\Omega @ V_{GS}=4.5V$

#### ● P-Channel

- $V_{DS} = -30V, I_D = -7A$
- $R_{DS(ON)} < 32m\Omega @ V_{GS}=-10V$
- $R_{DS(ON)} < 70m\Omega @ V_{GS}=-4.5V$

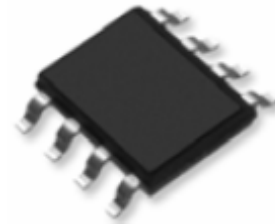
- High power and current handling capability
- Lead free product is acquired
- Surface mount package



Schematic diagram



Marking and pin assignment



SOP-8 top view

### Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCE30NP07S	NCE30NP07S	SOP-8	Ø330mm	12mm	4000 units

### Absolute Maximum Ratings ( $T_A=25^\circ C$ unless otherwise noted)

Parameter	Symbol	N-Channel	P-Channel	Unit	
Drain-Source Voltage	$V_{DS}$	30	-30	V	
Gate-Source Voltage	$V_{GS}$	$\pm 20$	$\pm 20$	V	
Continuous Drain Current	$I_D$	$T_A=25^\circ C$	6.5	-7	A
		$T_A=70^\circ C$	5.4	-5.8	
Pulsed Drain Current <sup>(Note 1)</sup>	$I_{DM}$	30	-30	A	
Maximum Power Dissipation	$P_D$	2.0	2.0	W	
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 To 150	-55 To 150	$^\circ C$	

### Thermal Characteristic

Thermal Resistance, Junction-to-Ambient <sup>(Note2)</sup>	$R_{\theta JA}$	N-Ch	62.5	$^\circ C/W$
Thermal Resistance, Junction-to-Ambient <sup>(Note2)</sup>	$R_{\theta JA}$	P-Ch	62.5	$^\circ C/W$

## N-CH Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
<b>Off Characteristics</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	30	33	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =30V, V <sub>GS</sub> =0V	-	-	1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	-	-	±100	nA
<b>On Characteristics</b> (Note 3)						
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	1	1.6	3	V
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =6A	-	19	24	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =6A	-	26	37	mΩ
Forward Transconductance	g <sub>FS</sub>	V <sub>DS</sub> =5V, I <sub>D</sub> =6A	15	-	-	S
<b>Dynamic Characteristics</b> (Note 4)						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =15V, V <sub>GS</sub> =0V, F=1.0MHz	-	485.8	-	PF
Output Capacitance	C <sub>oss</sub>		-	65.2	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>		-	54	-	PF
<b>Switching Characteristics</b> (Note 4)						
Turn-on Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> =15V, R <sub>L</sub> =2.5Ω V <sub>GS</sub> =10V, R <sub>GEN</sub> =3Ω	-	4.0	-	nS
Turn-on Rise Time	t <sub>r</sub>		-	2.0	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>		-	14.0	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	3.0	-	nS
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =15V, I <sub>D</sub> =6A, V <sub>GS</sub> =10V	-	12.6	-	nC
Gate-Source Charge	Q <sub>gs</sub>		-	1.9	-	nC
Gate-Drain Charge	Q <sub>gd</sub>		-	2.6	-	nC
<b>Drain-Source Diode Characteristics</b>						
Diode Forward Voltage (Note 3)	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =6A	-	0.8	1.2	V

## P-CH Electrical Characteristics ( $T_A=25^{\circ}\text{C}$ unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
<b>Off Characteristics</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS}=0V, I_D=-250\mu A$	-30	-33	-	V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=-30V, V_{GS}=0V$	-	-	-1	$\mu A$
Gate-Body Leakage Current	$I_{GSS}$	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	$\pm 100$	nA
<b>On Characteristics (Note 3)</b>						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=-250\mu A$	-1.3	-1.65	-2.5	V
Drain-Source On-State Resistance	$R_{DS(ON)}$	$V_{GS}=-10V, I_D=-6.5A$	-	28	32	m $\Omega$
		$V_{GS}=-4.5V, I_D=-6.5A$	-	49	70	m $\Omega$
Forward Transconductance	$g_{FS}$	$V_{DS}=-5V, I_D=-6.5A$	10	-	-	S
<b>Dynamic Characteristics (Note 4)</b>						
Input Capacitance	$C_{ISS}$	$V_{DS}=-15V, V_{GS}=0V,$ $F=1.0MHz$	-	691.9	-	PF
Output Capacitance	$C_{OSS}$		-	113.7	-	PF
Reverse Transfer Capacitance	$C_{RSS}$		-	109.4	-	PF
<b>Switching Characteristics (Note 4)</b>						
Turn-on Delay Time	$t_{d(on)}$	$V_{DD}=-15V, R_L=2.3\Omega$ $V_{GS}=-10V, R_{GEN}=6\Omega$	-	7.5	-	nS
Turn-on Rise Time	$t_r$		-	5.5	-	nS
Turn-Off Delay Time	$t_{d(off)}$		-	19	-	nS
Turn-Off Fall Time	$t_f$		-	7	-	nS
Total Gate Charge	$Q_g$	$V_{DS}=-15V, I_D=-6.5A$ $V_{GS}=-10V$	-	16.3	-	nC
Gate-Source Charge	$Q_{gs}$		-	2.2	-	nC
Gate-Drain Charge	$Q_{gd}$		-	4.1	-	nC
<b>Drain-Source Diode Characteristics</b>						
Diode Forward Voltage (Note 3)	$V_{SD}$	$V_{GS}=0V, I_S=-6.5A$	-	-	-1.2	V

### Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board,  $t \leq 10$  sec.
3. Pulse Test: Pulse Width  $\leq 300\mu s$ , Duty Cycle  $\leq 2\%$ .
4. Guaranteed by design, not subject to production

## N- Channel Typical Electrical and Thermal Characteristics (Curves)

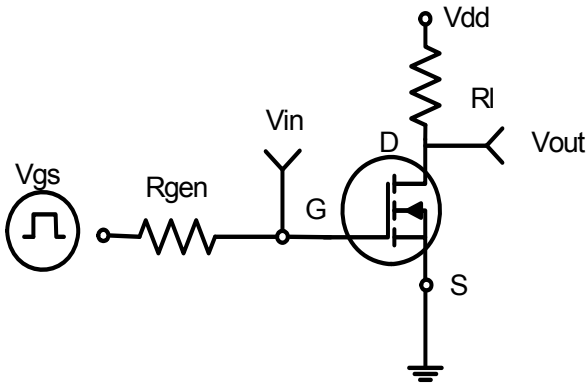


Figure 1: Switching Test Circuit

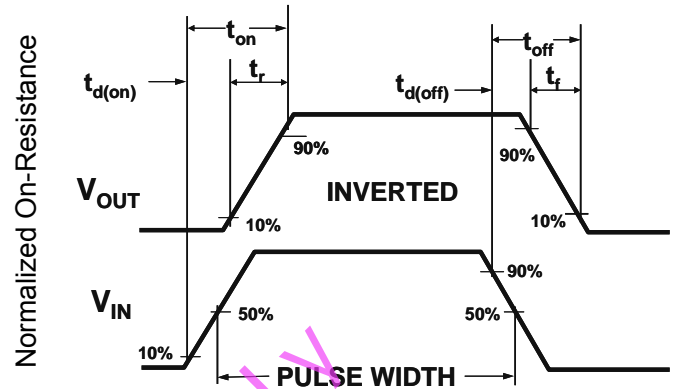


Figure 2: Switching Waveforms

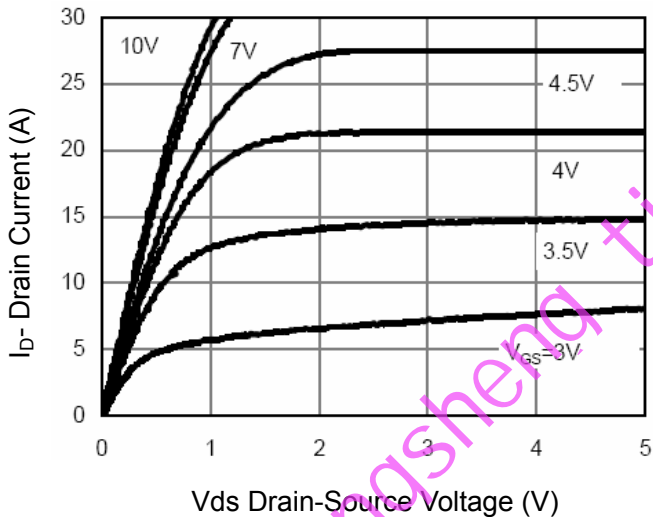


Figure 3 Output Characteristics

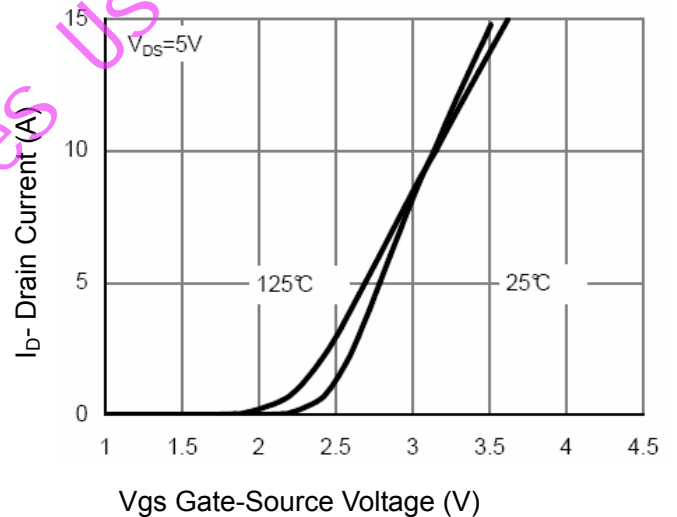


Figure 4 Transfer Characteristics

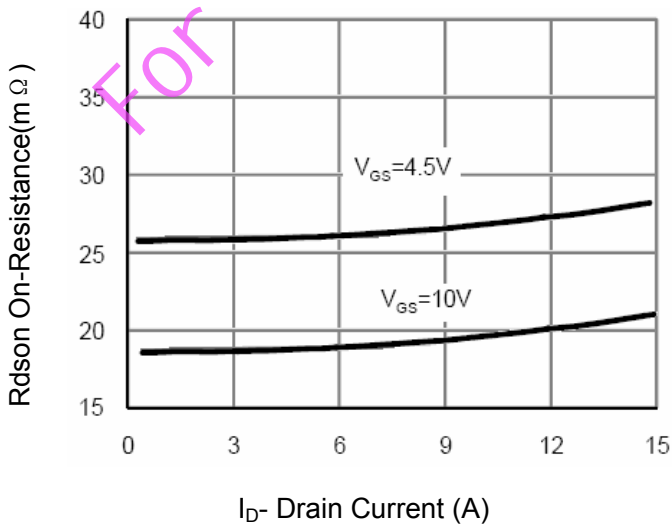


Figure 5 Drain-Source On-Resistance

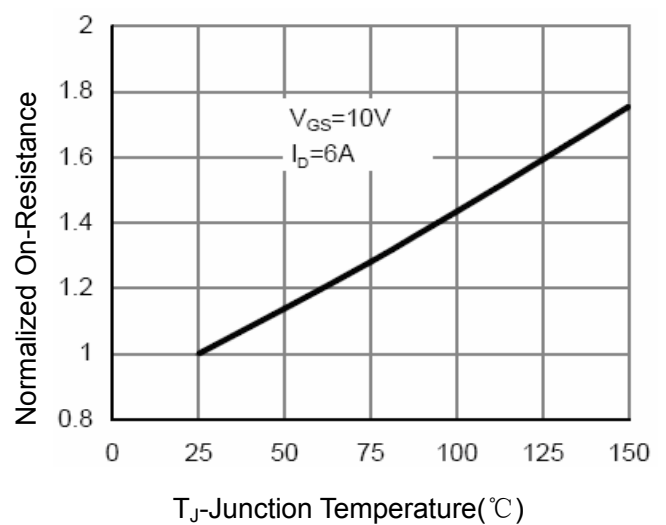
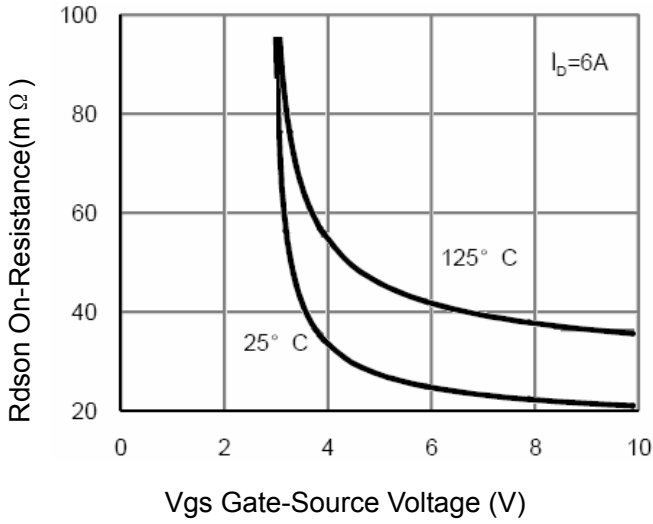
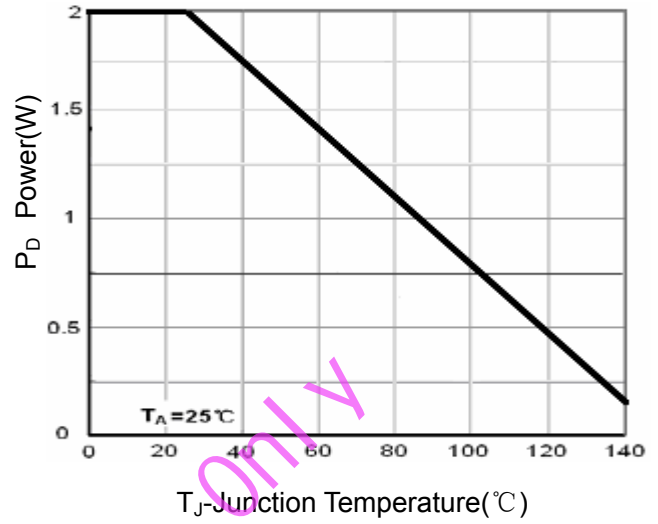


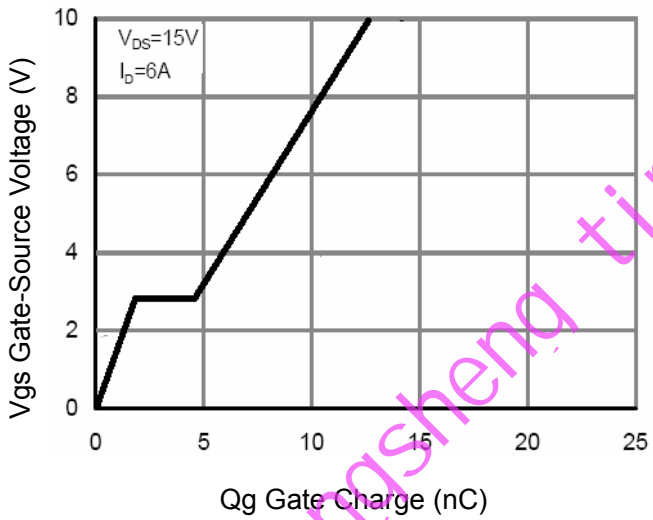
Figure 6 Drain-Source On-Resistance



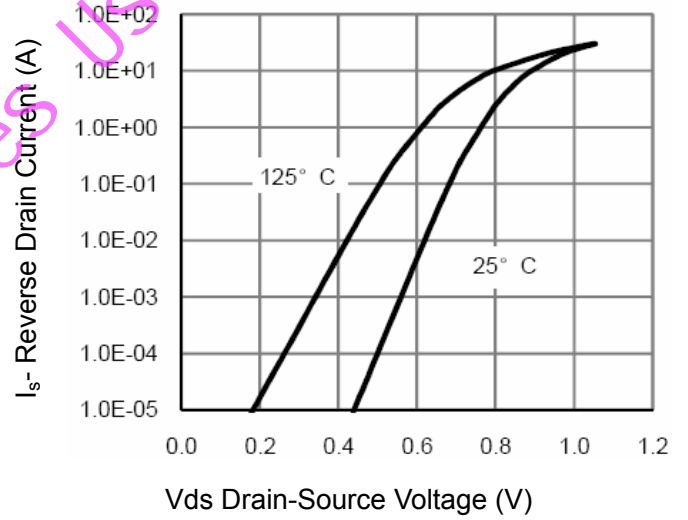
Vgs Gate-Source Voltage (V)  
**Figure 7 Rdson vs Vgs**



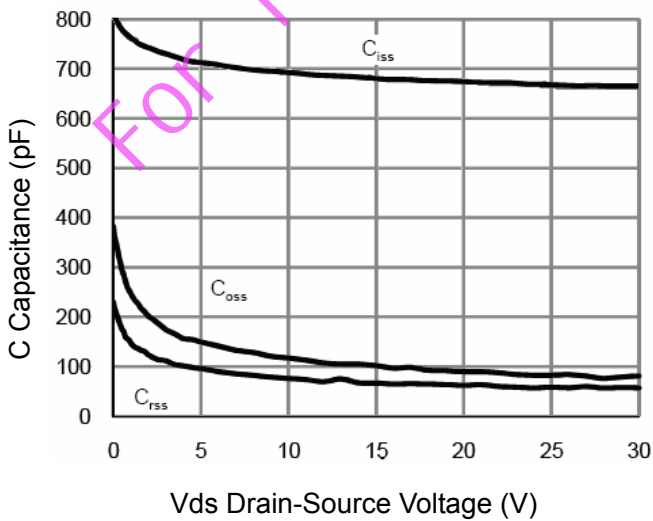
Tj Junction Temperature(°C)  
**Figure 8 Power Dissipation**



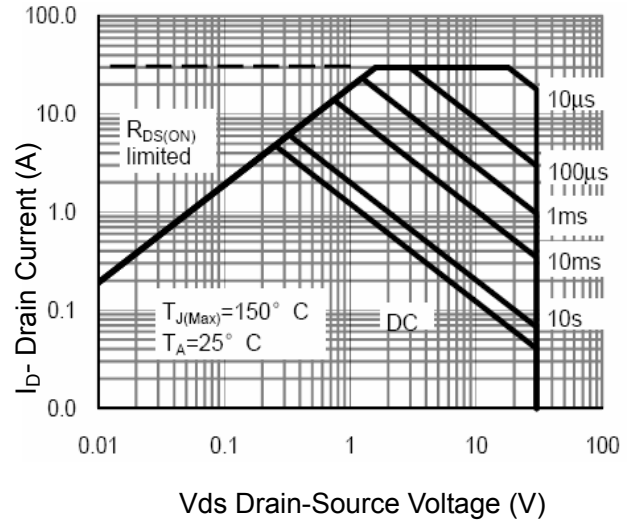
Qg Gate Charge (nC)  
**Figure 9 Gate Charge**



Vds Drain-Source Voltage (V)  
**Figure 10 Source- Drain Diode Forward**



Vds Drain-Source Voltage (V)  
**Figure 11 Capacitance vs Vds**



Vds Drain-Source Voltage (V)  
**Figure 12 Safe Operation Area**

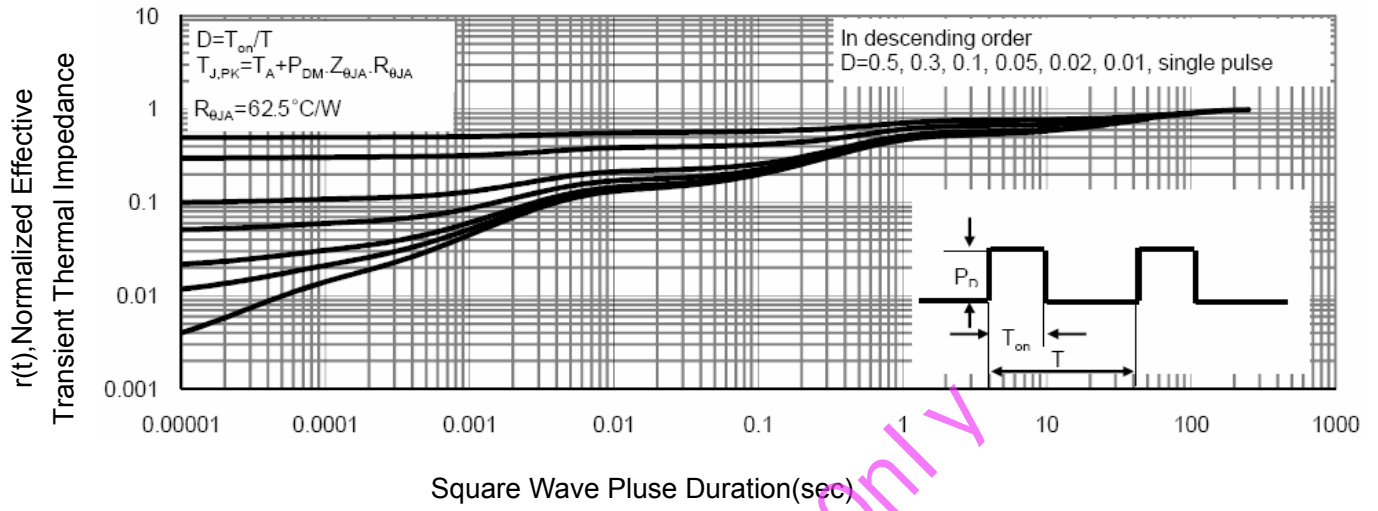
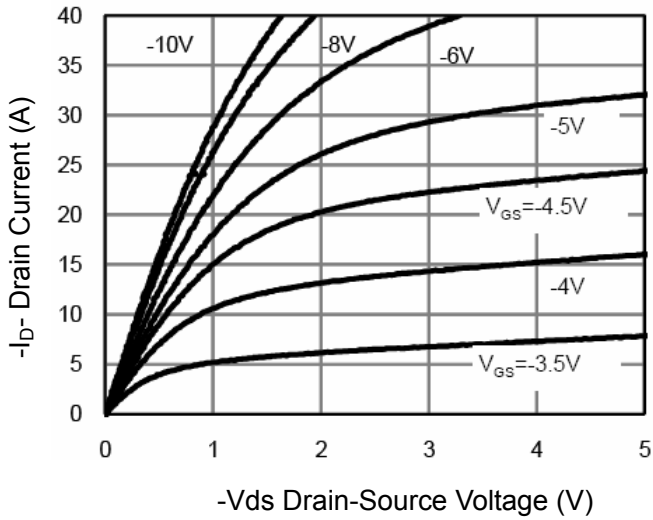


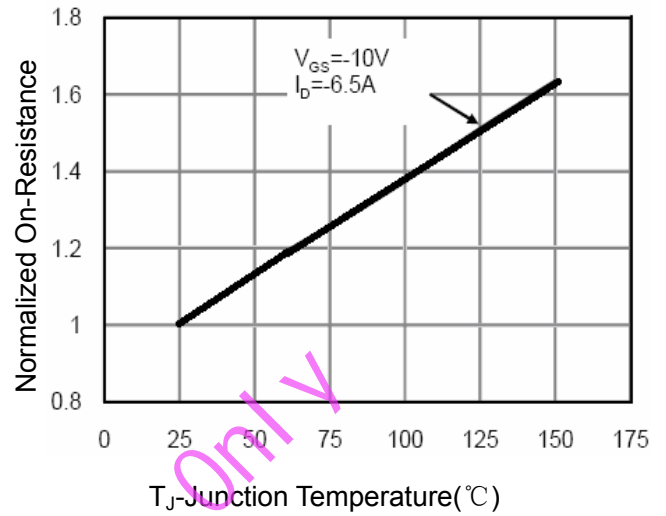
Figure 13 Normalized Maximum Transient Thermal Impedance

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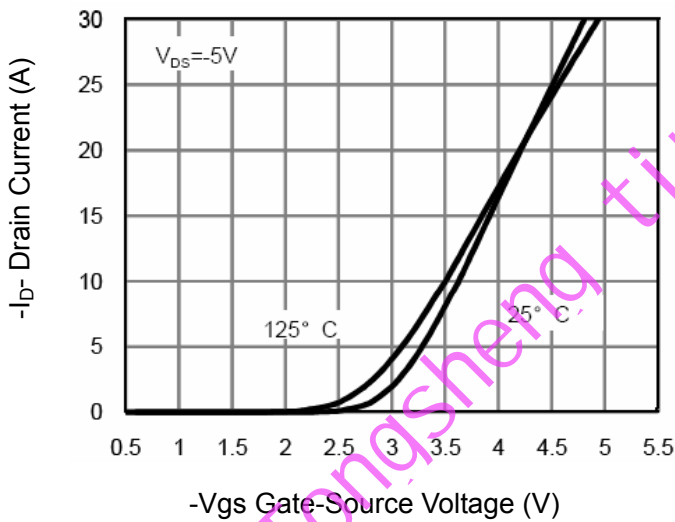
**P- Channel Typical Electrical and Thermal Characteristics (Curves)**



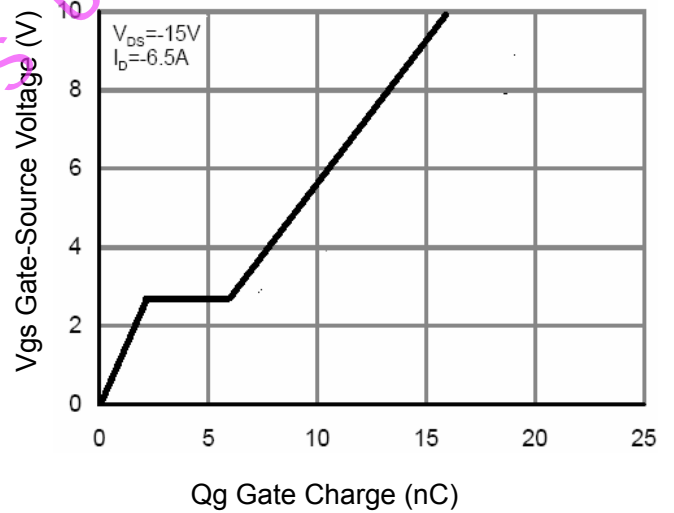
**Figure 1 Output Characteristics**



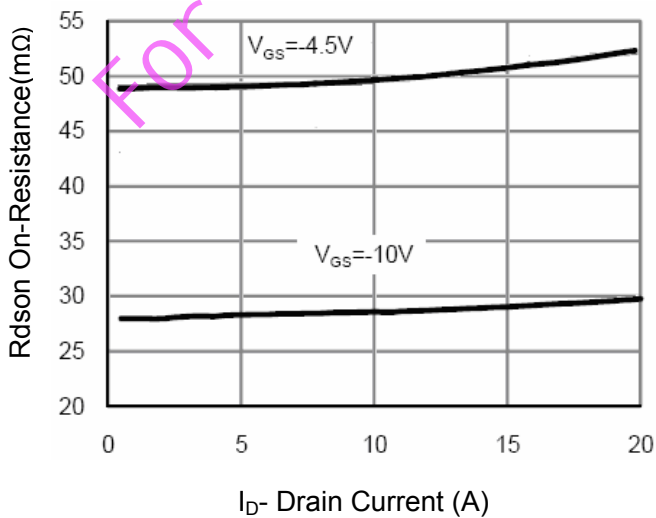
**Figure 4 Rdson-Junction Temperature**



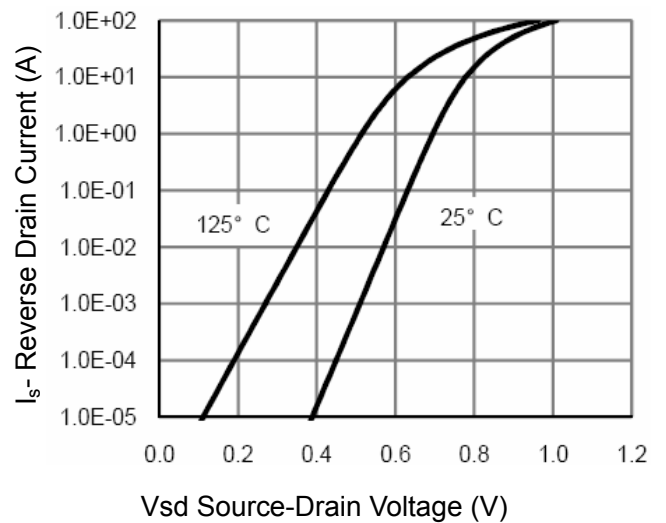
**Figure 2 Transfer Characteristics**



**Figure 5 Gate Charge**



**Figure 3 Rdson- Drain Current**



**Figure 6 Source- Drain Diode Forward**

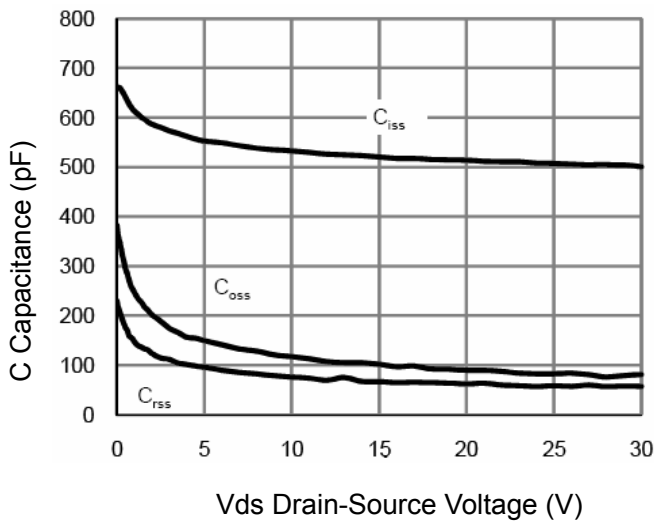


Figure 7 Capacitance vs Vds

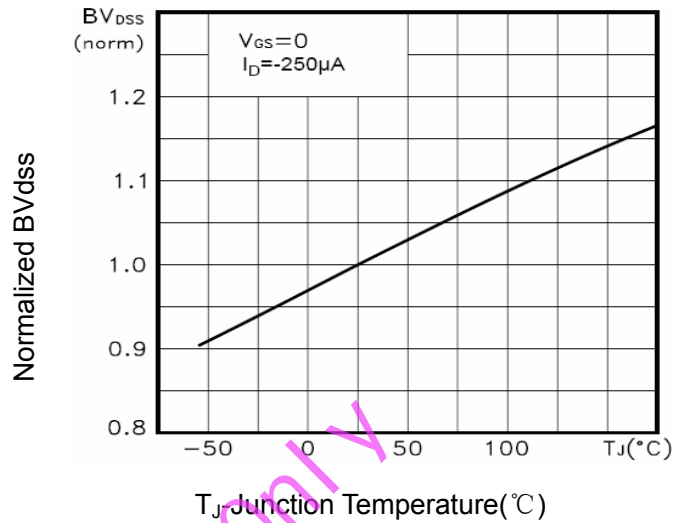


Figure 9  $BV_{DSS}$  vs Junction Temperature

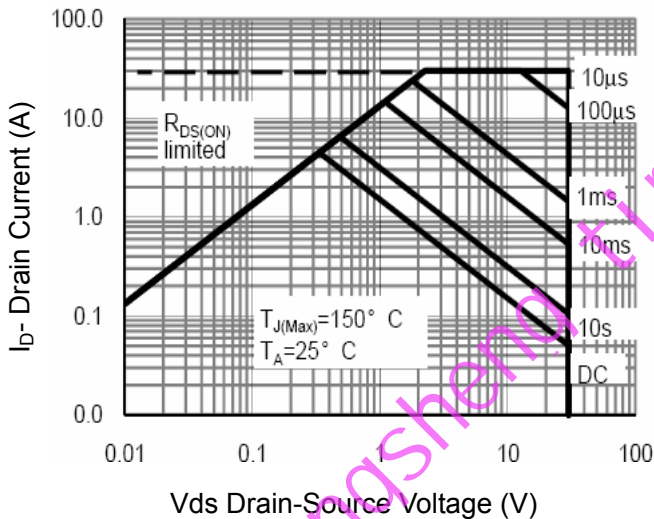


Figure 8 Safe Operation Area

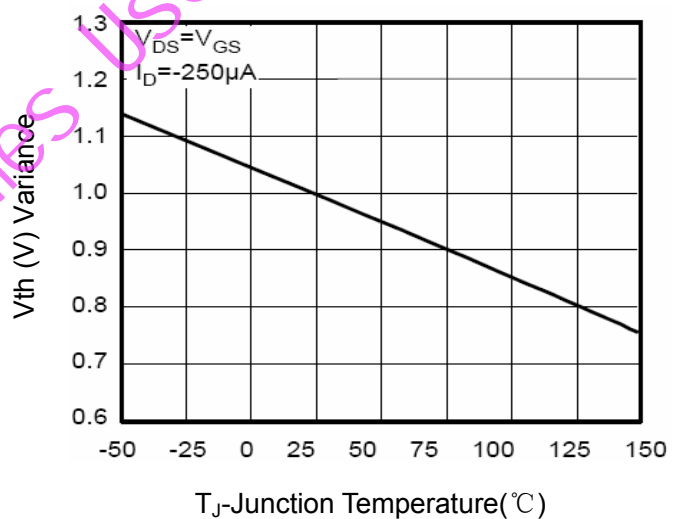


Figure 10  $V_{GS(th)}$  vs Junction Temperature

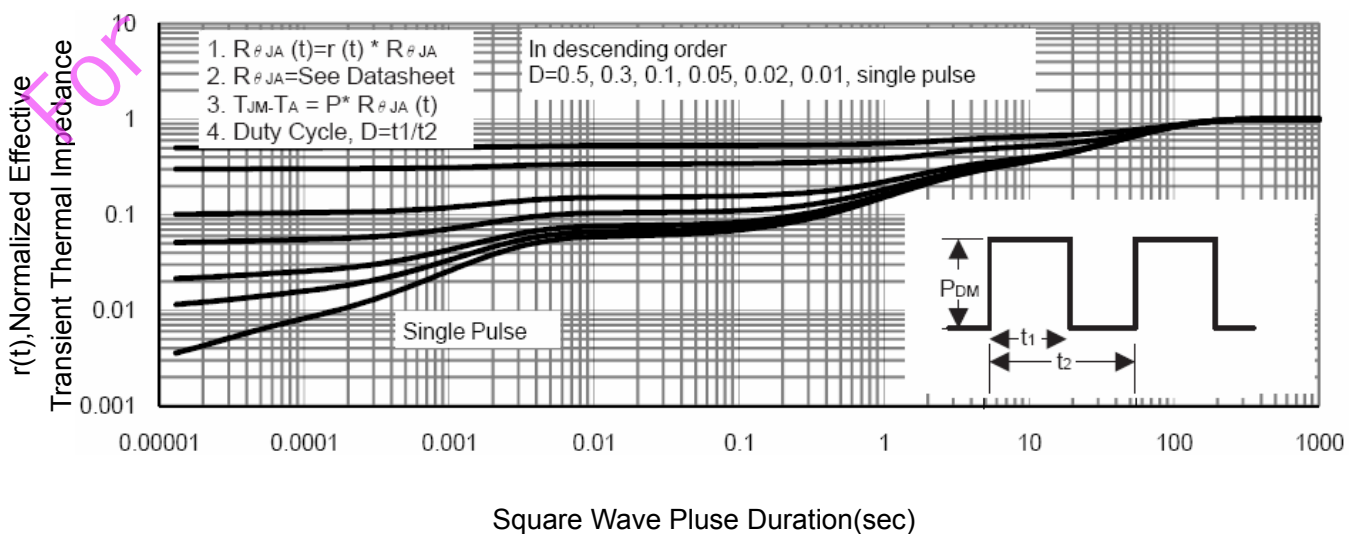
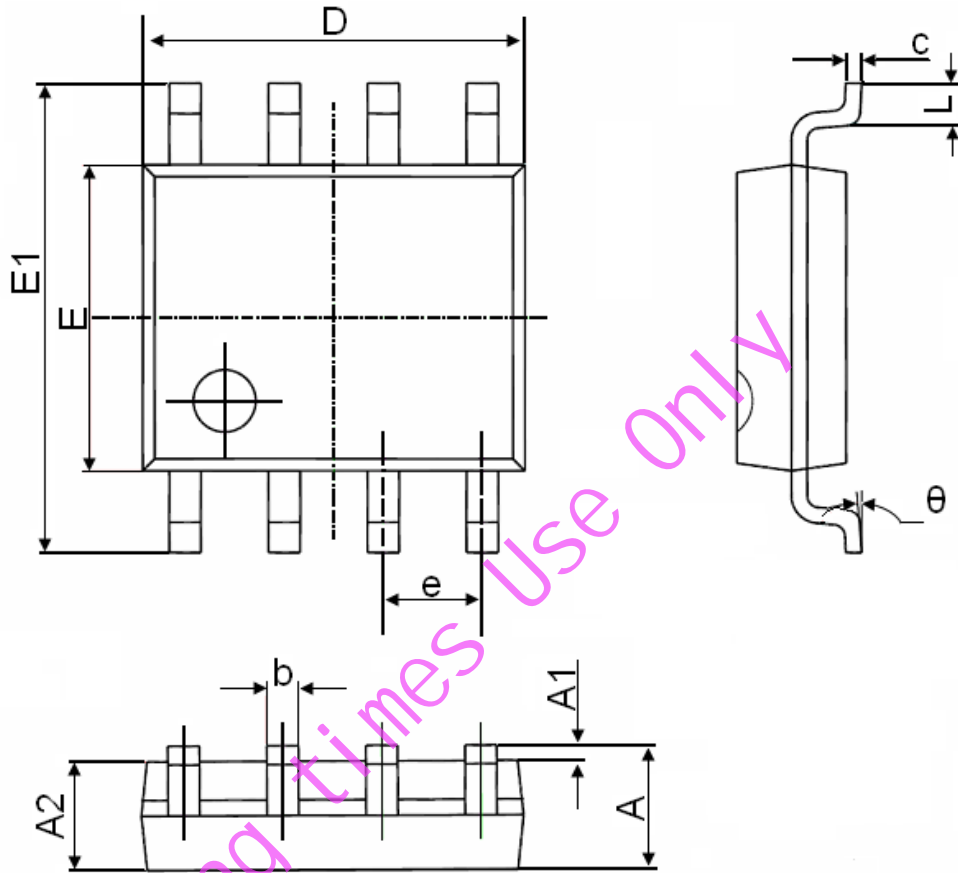


Figure 11 Normalized Maximum Transient Thermal Impedance



## SOP-8 Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D	4.700	5.100	0.185	0.200
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270(BSC)		0.050(BSC)	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°

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