



NCE P-Channel Enhancement Mode Power MOSFET

Description

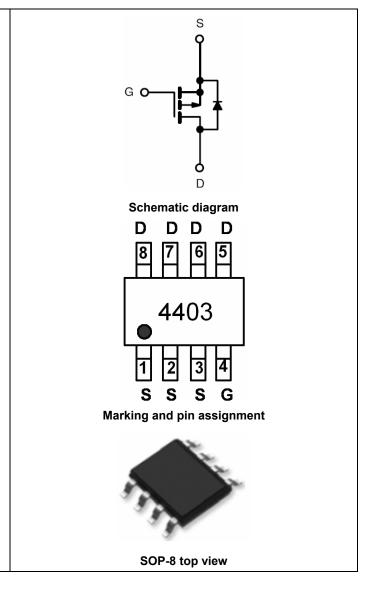
The NCE4403 uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications.

General Features

- $V_{DS} = -30V, I_D = -6.1A$ $R_{DS(ON)} < 46m\Omega @ V_{GS} = -10V$ $R_{DS(ON)} < 61m\Omega @ V_{GS} = -4.5V$ $R_{DS(ON)} < 117m\Omega @ V_{GS} = -2.5V$
- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Fast switching speed

Application

- Power switching application
- Hard switched and high frequency circuits
- Uninterruptible power supply



Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
4403	NCE4403	SOP-8	Ø330mm	12mm	2500 units

Absolute Maximum Ratings (T_A=25[°]Cunless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	Vds	-30	V
Gate-Source Voltage	Vgs	±12	V
Drain Current-Continuous	Ι _D	-6.1	А
Drain Current-Continuous(T _C =100 ℃)	I _D (100℃)	-4.3	А
Pulsed Drain Current	I _{DM}	30	А
Maximum Power Dissipation	PD	2.5	W
Operating Junction and Storage Temperature Range	T _J ,T _{STG}	-55 To 150	°C



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°C/W

Thermal Characteristic

Thermal Resistance, Junction-to-Ambient(Note 2)	$R_{ extsf{ heta}JA}$	50	

Electrical Characteristics (T_A=25 $^{\circ}$ C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit	
Off Characteristics							
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =-250µA	-30	-33	-	V	
Zero Gate Voltage Drain Current	I _{DSS}	V_{DS} =-30V, V_{GS} =0V	-	-	-1	μA	
Gate-Body Leakage Current	I _{GSS}	V_{GS} =±12V, V_{DS} =0V	-	-	±100	nA	
On Characteristics (Note 3)			•				
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_{D}=-250\mu A$	-0.7	-1	-1.3	V	
		V _{GS} =-10V, I _D =-5A	-	38	46		
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =-4.5V, I _D =-4.5A	-	49	61	mΩ	
		V _{DS} =-2.5V,I _D =-3A	-	-76	117	1	
Forward Transconductance	g fs	V _{DS} =-15V,I _D =-5A	14	-	-	S	
Dynamic Characteristics (Note4)			L				
Input Capacitance	C _{lss}		-	1022	-	PF	
Output Capacitance	C _{oss}	V_{DS} =-15V, V_{GS} =0V,	-	259	-	PF	
Reverse Transfer Capacitance	C _{rss}	F=1.0MHz	-	219	-	PF	
Switching Characteristics (Note 4)							
Turn-on Delay Time	t _{d(on)}		-	8	-	nS	
Turn-on Rise Time	tr	V _{DD} =-15V,I _D =-1A	-	12	-	nS	
Turn-Off Delay Time	t _{d(off)}	V_{GEN} =-10V, R_{G} =6 Ω	-	32	-	nS	
Turn-Off Fall Time	t _f		-	20	-	nS	
Total Gate Charge	Qg		-	16	-	nC	
Gate-Source Charge	Q _{gs}	V_{DS} =-15V,I _D =-4.5A,	-	2.7	-	nC	
Gate-Drain Charge	Q _{gd}	V _{GS} =-10V	-	4.1	-	nC	
Drain-Source Diode Characteristics							
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =-1.1A	-	-	-1.2	V	
Diode Forward Current (Note 2)	Is		-	-	-6.1	Α	

Notes

1. Repetitive Rating: Pulse width limited by maximum junction temperature.

2. Surface Mounted on FR4 Board, $t \le 10$ sec.

3. Pulse Test: Pulse Width \leq 300µs, Duty Cycle \leq 2%.

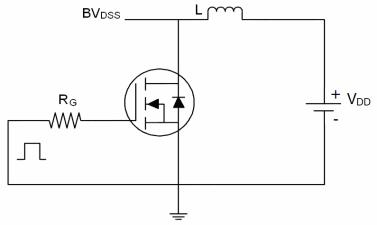
4. Guaranteed by design, not subject to production



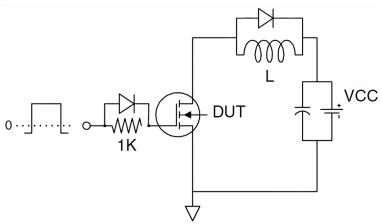
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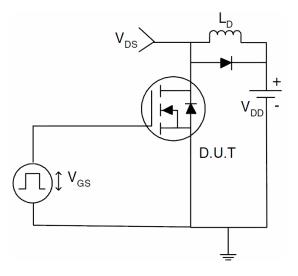
Test Circuit 1) E_{AS} Test Circuit



2) Gate Charge Test Circuit



3) Switch Time Test Circuit



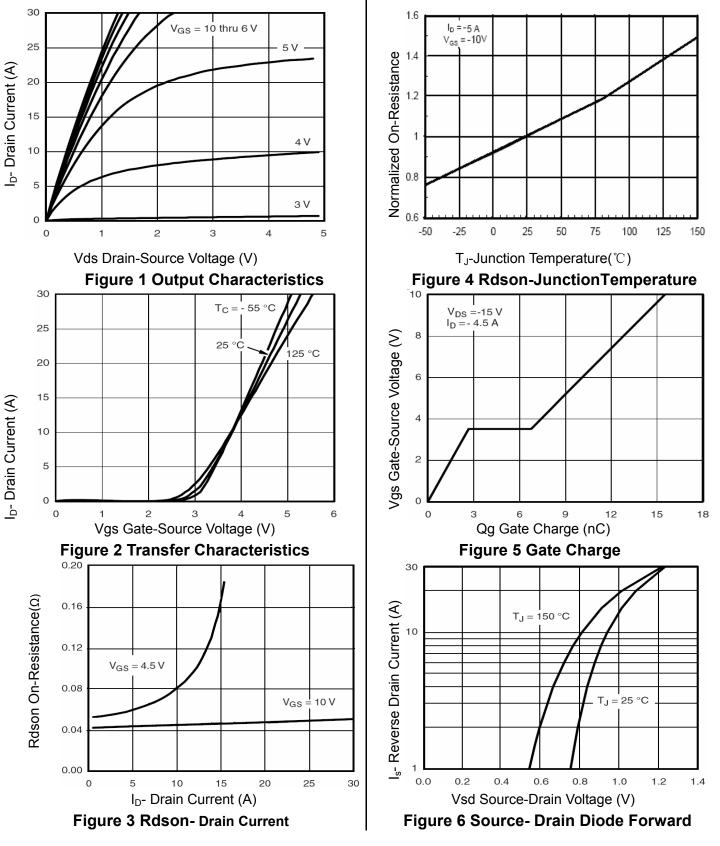


NCE4403



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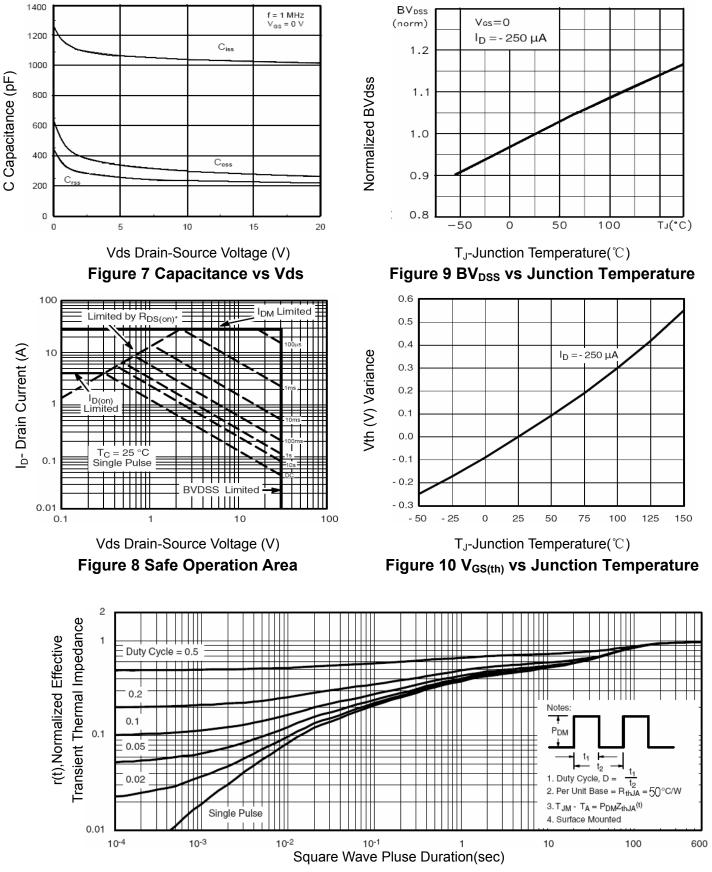








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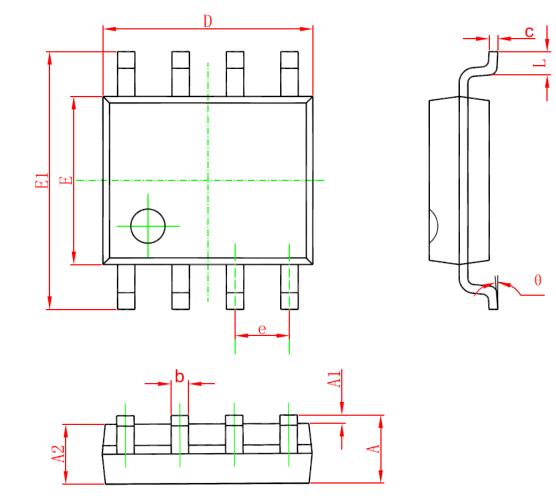




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SOP-8 Package Information



Sumbal	Dimensions Ir	n Millimeters	Dimensions In Inches		
Symbol	Min	Max	Min	Max	
A	1. 350	1. 750	0. 053	0. 069	
A1	0. 100	0. 250	0.004	0. 010	
A2	1.350	1. 550	0. 053	0. 061	
b	0. 330	0.510	0.013	0. 020	
С	0. 170	0. 250	0.006	0. 010	
D	4. 700	5. 100	0. 185	0. 200	
E	3.800	4.000	0. 150	0. 157	
E1	5. 800	6. 200	0. 228	0. 244	
е	1. 270 (BSC)		0. 050 (BSC)		
Ĺ	0. 400	1. 270	0.016	0. 050	
θ	0°	8°	0°	8°	







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