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### NCE P-Channel Enhancement Mode Power MOSFET

#### **DESCRIPTION**

The NCE4953A uses advanced trench technology to provide excellent  $R_{\rm DS(ON)}$ , low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a load switch or in PWM applications.

#### **GENERAL FEATURES**

•  $V_{DS} = -30V, I_{D} = -5.3A$ 

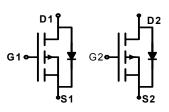
 $R_{DS(ON)}$  < 100m $\Omega$  @  $V_{GS}$ =-4.5V

 $R_{DS(ON)}$  < 49m $\Omega$  @  $V_{GS}$ =-10V

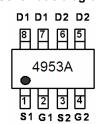
- High Power and current handing capability
- Lead free product is acquired
- Surface Mount Package

## **Application**

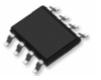
- PWM applications
- Load switch
- Power management



#### Schematic diagram



#### Marking and pin Assignment



SOP-8 top view

#### **Package Marking And Ordering Information**

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
4953A	NCE4953A	SOP-8	Ø330mm	12mm	2500 units

#### Absolute Maximum Ratings (TA=25℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V <sub>DS</sub>	-30	V
Gate-Source Voltage	V <sub>G</sub> S	±20	V
Drain Current-Continuous	I <sub>D</sub>	-5.3	Α
Drain Current-Pulsed (Note 1)	I <sub>DM</sub>	-20	Α
Maximum Power Dissipation	P <sub>D</sub>	2.6	W
Operating Junction and Storage Temperature Range	$T_{J}$ , $T_{STG}$	-55 To 150	$^{\circ}$ C

#### **Thermal Characteristic**

Thermal Resistance, Junction-to-Ambient (Note 2)	R <sub>0JA</sub>	49	°C/W
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#### Electrical Characteristics (TA=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =-250μA	-30	-33	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-24V,V <sub>GS</sub> =0V	-	-	-1	μΑ



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# NCE4953A

Cate Pedy Lookage Current	1	\/ -+30\/\/ -0\/			±100	nA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V,V <sub>DS</sub> =0V			±100	IIA
On Characteristics (Note 3)						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS},I_{D}=-250\mu A$		-1.6	-3	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =-10V, I <sub>D</sub> =-5.3A	-	43	49	mΩ
Dialii-Source Oil-State Resistance		V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-4.2A	-	68	100	mΩ
Forward Transconductance	<b>g</b> FS	V <sub>DS</sub> =-15V,I <sub>D</sub> =-4.5A	4	7	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C <sub>lss</sub>	\/ - 45\/\/ -0\/	-	540	-	PF
Output Capacitance	C <sub>oss</sub>	V <sub>DS</sub> =-15V,V <sub>GS</sub> =0V, F=1.0MHz	-	150	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>	F-1.UNIAZ	-	75	-	PF
Switching Characteristics (Note 4)	·					
Turn-on Delay Time	t <sub>d(on)</sub>		-	8	-	nS
Turn-on Rise Time	t <sub>r</sub>	V <sub>DD</sub> =-15V, ID=-1A,	-	14	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>	$V_{GS}$ =-10 $V$ , $R_{GEN}$ =6 $\Omega$	-	18	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	10	-	nS
Total Gate Charge	Qg		-	10	-	nC
Gate-Source Charge	Q <sub>gs</sub>	V <sub>DS</sub> =-15V,I <sub>D</sub> =-5.3A,V <sub>GS</sub> =-10V	-	2.4	-	nC
Gate-Drain Charge	$Q_{gd}$		-	3.2	-	nC
Drain-Source Diode Characteristics	<u>.</u>			•	-	
Diode Forward Voltage (Note 3)	V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>S</sub> =-1.7A	-	-	-1.2	V
		,				

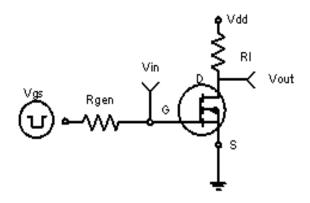
#### Notes:

- $\textbf{1.} \ \textbf{Repetitive Rating: Pulse width limited by maximum junction temperature.}$
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- 3. Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
- 4. Guaranteed by design, not subject to production

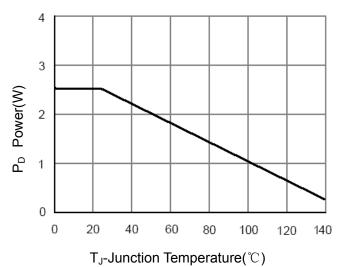
**Pb Free Product** 

# **NCE4953A**

#### TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS



**Figure 1:Switching Test Circuit** 



**Figure 3 Power Dissipation** 

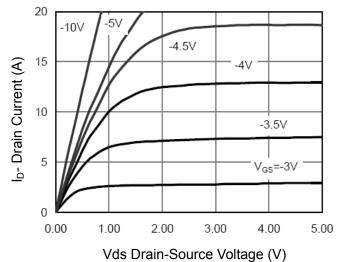


Figure 5 Output CHARACTERISTICS

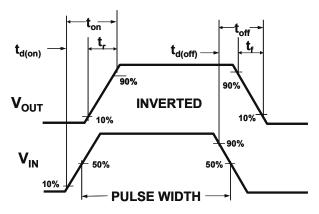
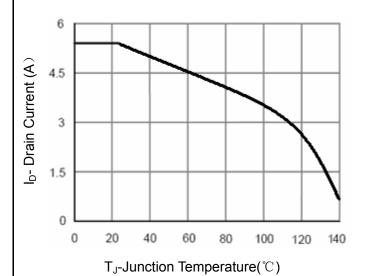


Figure 2:Switching Waveforms



**Figure 4 Drain Current** 

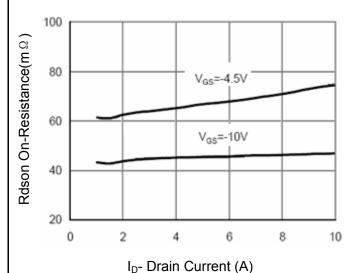
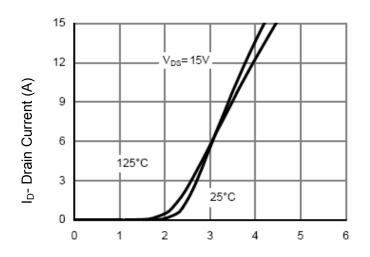
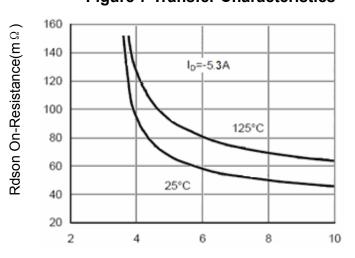


Figure 6 Drain-Source On-Resistance





Vgs Gate-Source Voltage (V)
Figure 7 Transfer Characteristics



Vgs Gate-Source Voltage (V)
Figure 9 Rdson vs Vgs

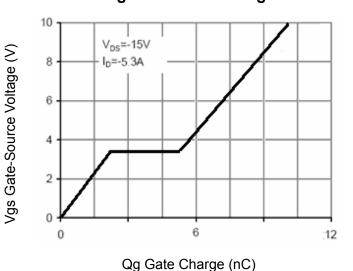
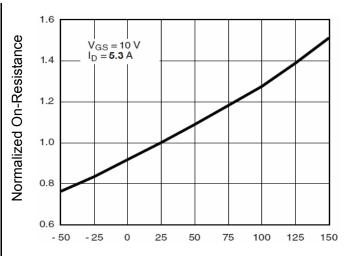


Figure 11 Gate Charge



 $T_J$ -Junction Temperature( ${}^{\circ}$ C) Figure 8 Drain-Source On-Resistance

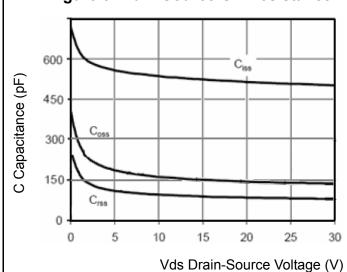


Figure 10 Capacitance vs Vds

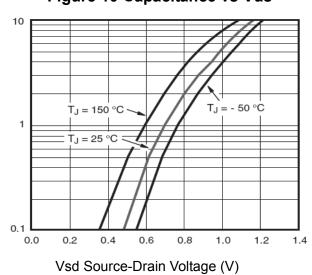


Figure 12 Source- Drain Diode Forward

Is- Reverse Drain Current (A)



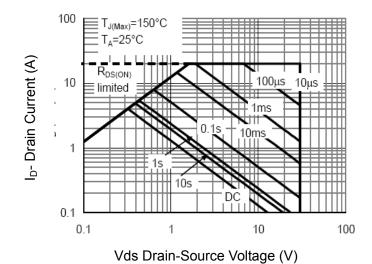
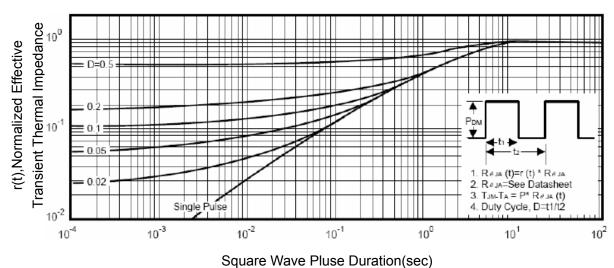


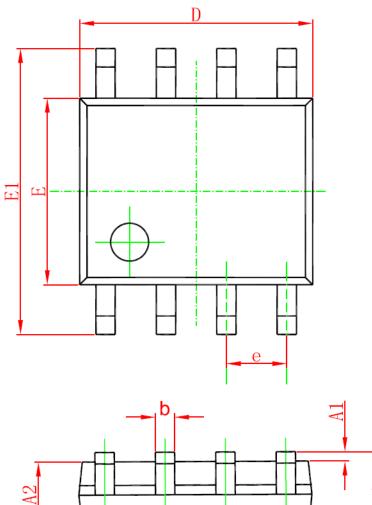
Figure 13 Safe Operation Area

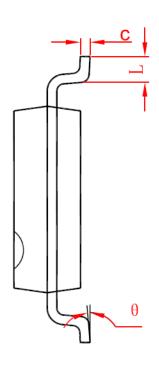


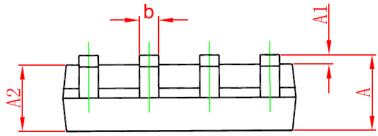
**Figure 14 Normalized Maximum Transient Thermal Impedance** 



# **SOP-8 PACKAGE IN FORMATION**







Ch l	Dimensions In	n Millimeters	Dimensions In Inches		
Symbol	Min	Max	Min	Max	
A	1. 350	1. 750	0. 053	0. 069	
A1	0. 100	0. 250	0. 004	0. 010	
A2	1. 350	1. 550	0. 053	0. 061	
b	0. 330	0. 510	0. 013	0. 020	
С	0. 170	0. 250	0. 006	0. 010	
D	4. 700	5. 100	0. 185	0. 200	
Е	3. 800	4. 000	0. 150	0. 157	
E1	5. 800	6. 200	0. 228	0. 244	
е	1. 270	(BSC)	0. 050 (BSC)		
L	0. 400	1. 270	0. 016	0. 050	
θ	0°	8°	0°	8°	



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