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NCE6020I

NCE N-Channel Enhancement Mode Power MOSFET

Description

The NCE6020I uses advanced trench technology and design to provide excellent R_{DS(ON)} with low gate charge. It can be used in a wide variety of applications.

Genera Features

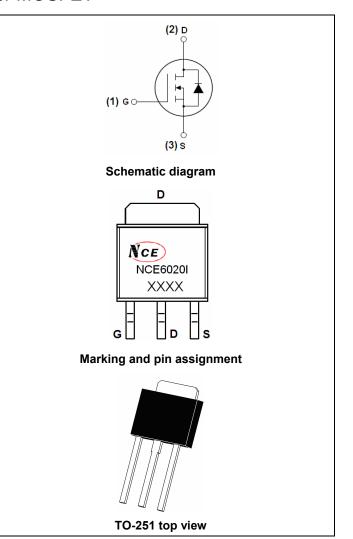
- V_{DS} =60V,I_D =20A $R_{DS(ON)}$ <44m Ω @ V_{GS} =10V
- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Good stability and uniformity with high EAS
- Excellent package for good heat dissipation
- Special process technology for high ESD capability

Application

- Power switching application
- Hard switched and high frequency circuits
- Uninterruptible power supply

100% UIS TESTED!

100% AVds TESTED!



Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCE6020I	NCE6020I	TO-251	-	-	-

Absolute Maximum Ratings (T_C=25 ℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	60	V
Gate-Source Voltage	V _G S	±20	V
Drain Current-Continuous	I _D	20	Α
Drain Current-Continuous(T _C =100 °C)	I _D (100℃)	14	Α
Pulsed Drain Current	I _{DM}	45	Α
Maximum Power Dissipation	P _D	30	W
Derating factor		0.2	W /℃
Single pulse avalanche energy (Note 5)	E _{AS}	72	mJ
Operating Junction and Storage Temperature Range	T_{J}, T_{STG}	-55 To 175	$^{\circ}$

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NCE6020I

Thermal Characteristic

Thermal Resistance, Junction-to-Case(Note 2)	R _{eJC}	5	°C/W
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Electrical Characteristics (T_C=25°C unless otherwise noted)

Parameter Symbol Condition		Min	Тур	Max	Unit	
Off Characteristics	•		•			
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250μA 6		-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =60V,V _{GS} =0V	-	-	1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} ,I _D =250μA	1.2	2.0	2.5	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =10A	-	37	44	mΩ
Forward Transconductance	g FS	V _{DS} =5V,I _D =4.5A	11	-	-	S
Dynamic Characteristics (Note4)	•					
Input Capacitance	C _{lss}	\/ -20\/\/ -0\/	-	500	-	PF
Output Capacitance	Coss	V_{DS} =30V, V_{GS} =0V, F=1.0MHz	-	60	-	PF
Reverse Transfer Capacitance	C _{rss}	F=1.UIVIΠ2	-	25	-	PF
Switching Characteristics (Note 4)	•					
Turn-on Delay Time	t _{d(on)}		-	5	-	nS
Turn-on Rise Time	t _r	V_{DD} =30V, I_{D} =2A, R_{L} =6.7 Ω	-	2.6	-	nS
Turn-Off Delay Time	t _{d(off)}	V_{GS} =10 V , R_{G} =3 Ω	-	16.1	-	nS
Turn-Off Fall Time	t _f		-	2.3	-	nS
Total Gate Charge	Qg	\/ -20\/ -4.54	-	14		nC
Gate-Source Charge	Q _{gs}	V_{DS} =30V, I_{D} =4.5A, V_{GS} =10V	-	2.9		nC
Gate-Drain Charge	Q_{gd}	V _{GS} -10V	-	5.2		nC
Drain-Source Diode Characteristics	•					
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =20A	-		1.2	V
Diode Forward Current (Note 2)	Is		-	-	20	Α
Reverse Recovery Time	t _{rr}	TJ = 25°C, IF =20A	-	35	-	nS
Reverse Recovery Charge	Qrr	di/dt = 100A/µs(Note3) - 53 -		-	nC	
Forward Turn-On Time	t _{on}	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD)				

Notes:

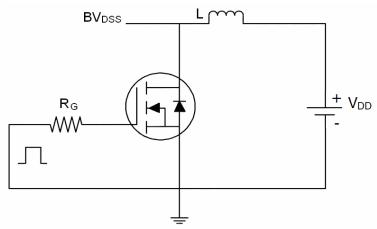
- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- **2.** Surface Mounted on FR4 Board, $t \le 10$ sec.
- **3.** Pulse Test: Pulse Width ≤ $300\mu s$, Duty Cycle ≤ 2%.
- 4. Guaranteed by design, not subject to production
- **5.** EAS condition:Tj=25 $^{\circ}$ C,V_{DD}=30V,V_G=10V,L=0.5mH,Rg=25 Ω

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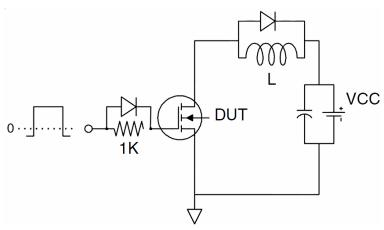


Test Circuit

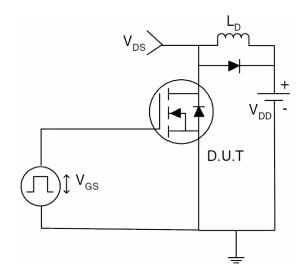
1) E_{AS} test Circuit



2) Gate charge test Circuit



3) Switch Time Test Circuit







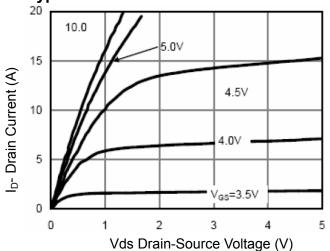


Figure 1 Output Characteristics

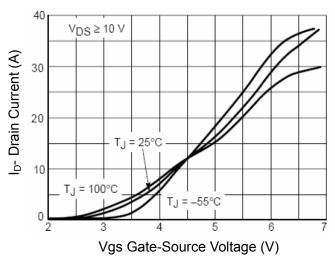
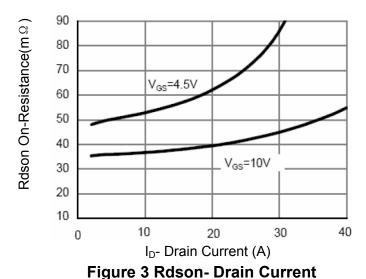


Figure 2 Transfer Characteristics



3.0 ID = 20A

2.5

2.0

1.5

0.0

-60 -40 -20 0 20 40 60 80 100 120 140 160 180

TJ-Junction Temperature(°C)

Figure 4 Rdson-JunctionTemperature

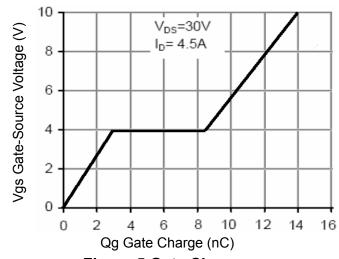


Figure 5 Gate Charge

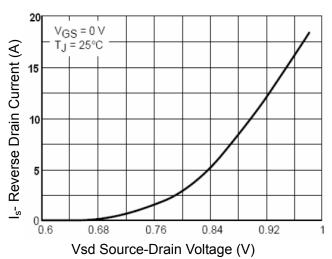


Figure 6 Source- Drain Diode Forward



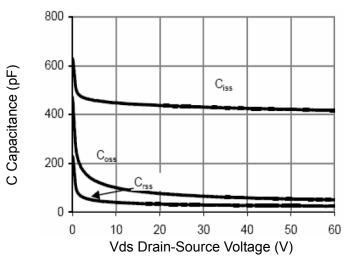


Figure 7 Capacitance vs Vds

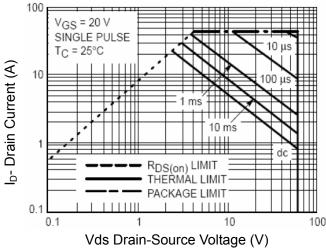


Figure 8 Safe Operation Area

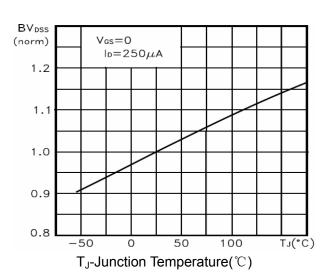


Figure 9 BV_{DSS} vs Junction Temperature

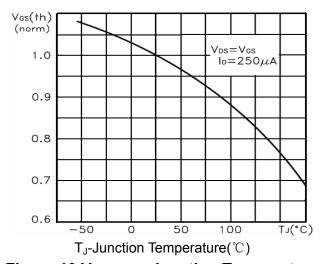


Figure 10 V_{GS(th)} vs Junction Temperature

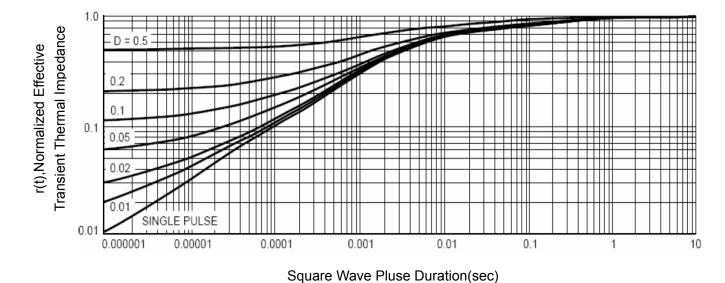


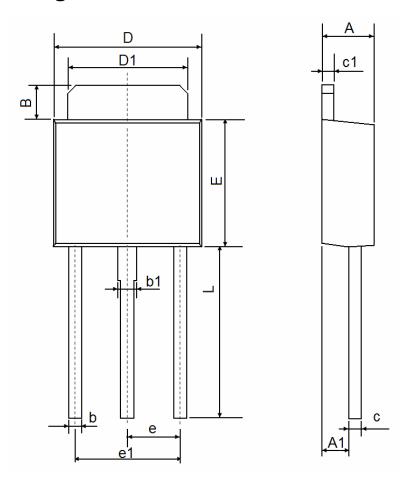
Figure 11 Normalized Maximum Transient Thermal Impedance

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TO-251 Package Information



Symbol	Dimensions	In Millimeters	Dimensions In Inches		
	Min.	Max.	Min.	Max.	
Α	2.200	2.400	0.087	0.094	
A1	1.050	1.350	0.042	0.054	
В	1.350	1.650	0.053	0.065	
b	0.500	0.700	0.020	0.028	
b1	0.700	0.900	0.028	0.035	
С	0.430	0.580	0.017	0.023	
c1	0.430	0.580	0.017	0.023	
D	6.350	6.650	0.250	0.262	
D1	5.200	5.400	0.205	0.213	
E	5.400	5.700	0.213	0.224	
е	2.300 TYP		0.091	TYP	
e1	4.500	4.700	0.177	0.185	
L	7.500	7.900	0.295	0.311	



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