



# NCE N-Channel Enhancement Mode Power MOSFET

# DESCRIPTION

The NCE6075K uses advanced trench technology and design to provide excellent  $R_{DS(ON)}$  with low gate charge. It can be used in a wide variety of applications.

# **GENERAL FEATURES**

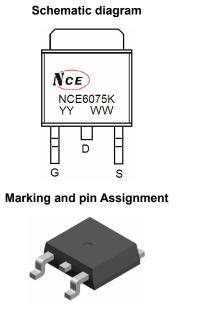
- $V_{DS} = 60V, I_D = 75A$  $R_{DS(ON)} < 11.5m\Omega @ V_{GS} = 10V$  (Typ:9.1m $\Omega$ )
- High density cell design for ultra low Rdson
- Fully characterized Avalanche voltage and current
- Good stability and uniformity with high E<sub>AS</sub>
- Excellent package for good heat dissipation
- Special process technology for high ESD capability

# Application

- Power switching application
- Hard Switched and High Frequency Circuits
- Uninterruptible Power Supply

100% UIS TESTED!

100% ΔVds TESTED!



(2) D

(3) s

(1) GO

TO-252-2L top view

#### Package Marking And Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCE6075K	NCE6075K	TO-252-2L	-	-	-

#### Absolute Maximum Ratings (TA=25°C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	Vds	60	V
Gate-Source Voltage	Vgs	±20	V
Drain Current-Continuous	Ι <sub>D</sub>	75	А
Drain Current-Continuous(T <sub>C</sub> =100℃)	I <sub>D</sub> (100℃)	50	А
Pulsed Drain Current	I <sub>DM</sub>	300	А
Maximum Power Dissipation	PD	110	W
Derating factor		0.73	W/℃
Single pulse avalanche energy (Note 5)	E <sub>AS</sub>	450	mJ
Operating Junction and Storage Temperature Range	T <sub>J</sub> ,T <sub>STG</sub>	-55 To 175	°C

#### **Thermal Characteristic**

Thermal Resistance, Junction-to-Case(Note 2)	R <sub>eJC</sub>	1.36	°C <b>/W</b>	
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#### Electrical Characteristics (TA=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Мах	Unit
Off Characteristics	·			•		
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250µA			-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =60V,V <sub>GS</sub> =0V	-	-	1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	$V_{GS}$ =±20V, $V_{DS}$ =0V	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> ,I <sub>D</sub> =250µA	2	3	4	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =30A	-	9.1	11.5	mΩ
Forward Transconductance	<b>g</b> fs	V <sub>DS</sub> =25V,I <sub>D</sub> =30A	20	-	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C <sub>lss</sub>		-	2350	-	PF
Output Capacitance	C <sub>oss</sub>	V <sub>DS</sub> =25V,V <sub>GS</sub> =0V, F=1.0MHz	-	237	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>		-	205	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t <sub>d(on)</sub>		-	16	-	nS
Turn-on Rise Time	tr	$V_{DD}$ =30V, $I_D$ =2A, $R_L$ =15 $\Omega$	-	10	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>	V <sub>GS</sub> =10V,R <sub>G</sub> =2.5Ω	-	45	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	12	-	nS
Total Gate Charge	Qg	)/ _20)/   _20)	-	50	-	nC
Gate-Source Charge	Q <sub>gs</sub>	V <sub>DS</sub> =30V,I <sub>D</sub> =30A, V <sub>GS</sub> =10V	-	12	-	nC
Gate-Drain Charge	Q <sub>gd</sub>	v <sub>GS</sub> -10v	-	16	-	nC
Drain-Source Diode Characteristics	·			•		
Diode Forward Voltage (Note 3)	V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>S</sub> =30A	-	-	1.2	V
Diode Forward Current (Note 2)	I <sub>S</sub>		-	-	75	А
Reverse Recovery Time	t <sub>rr</sub>	TJ = 25°C, IF =75A	-	28		nS
Reverse Recovery Charge	Qrr	di/dt = 100A/µs(Note3)	-	49		nC
Forward Turn-On Time	t <sub>on</sub>	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD)				

#### Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.

**2.** Surface Mounted on FR4 Board,  $t \le 10$  sec.

**3.** Pulse Test: Pulse Width  $\leq$  300µs, Duty Cycle  $\leq$  2%.

4. Guaranteed by design, not subject to production

5. EAS condition: Tj=25 $^\circ \!\! \mathbb{C}, V_{DD} \!\! = \!\! 30V, V_G \!\! = \!\! 10V, L \!\! = \!\! 0.5mH, Rg \!\! = \!\! 25\Omega$ 



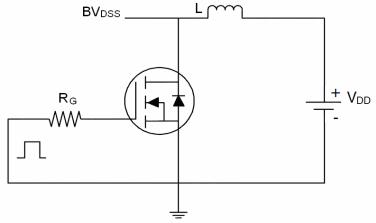
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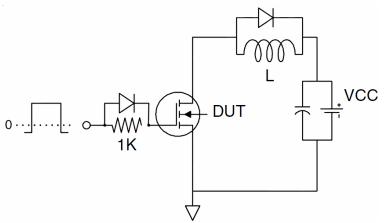


# **Test circuit**

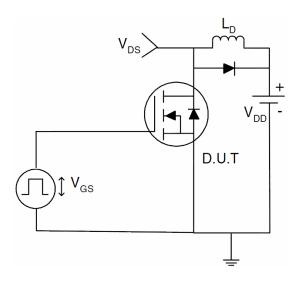
1)  $E_{AS}$  test Circuits



2) Gate charge test Circuit:



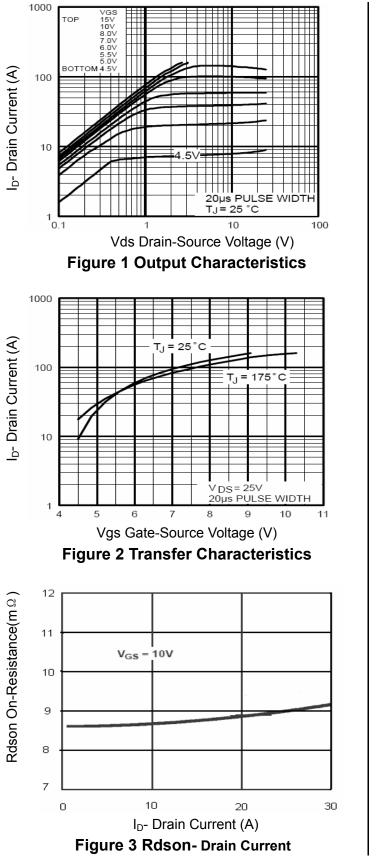
3) Switch Time Test Circuit:





NCE6075K

# TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS (Curves)



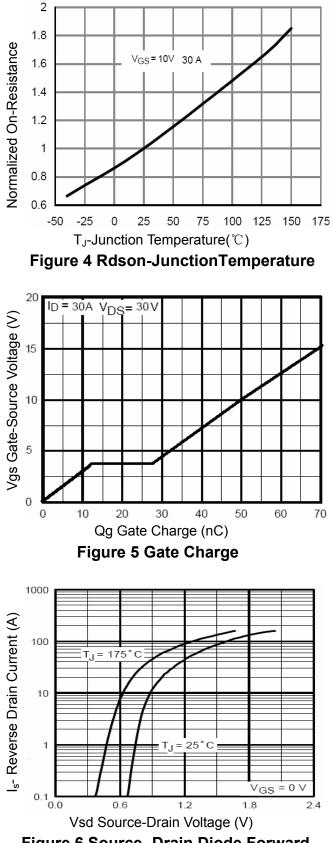
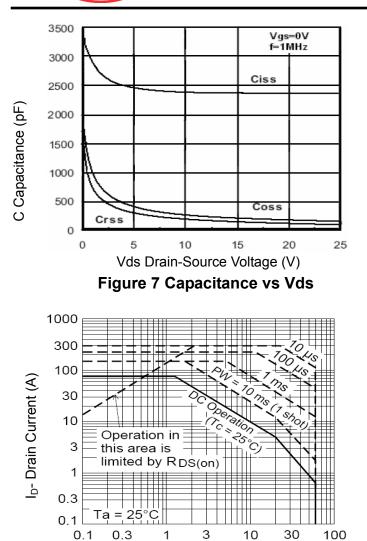


Figure 6 Source- Drain Diode Forward



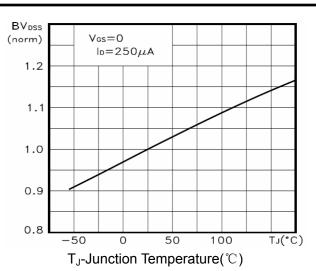
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Vds Drain-Source Voltage (V)

Figure 8

Safe Operation Area



**Pb Free Product** 

NCE6075K



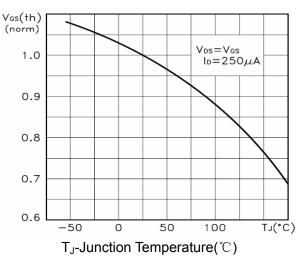


Figure 10 V<sub>GS(th)</sub> vs Junction Temperature

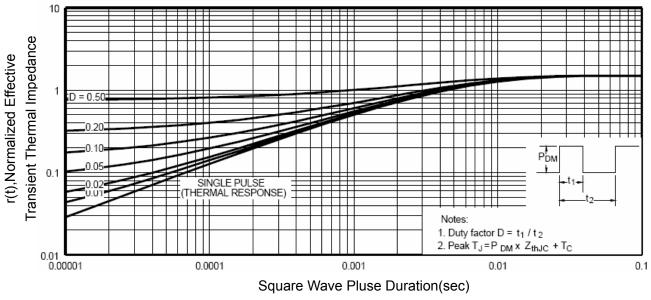
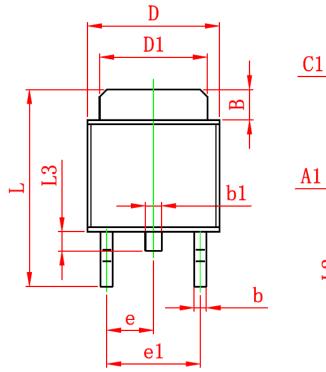


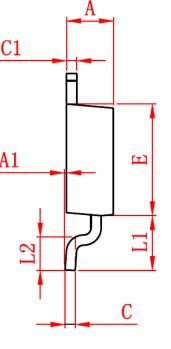
Figure 11 Normalized Maximum Transient Thermal Impedance

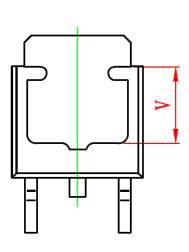
Pb Free Product NCE6075K



# **TO-252-2L Package Information**





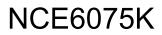


Symbol	Dimensions	In Millimeters	Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
A	2.200	2.400	0.087	0.094	
A1	0.000	0.127	0.000	0.005	
В	1.350	1.650	0.053	0.065	
b	0.500	0.700	0.020	0.028	
b1	0.700	0.900	0.028	0.035	
С	0.430	0.580	0.017	0.023	
c1	0.430	0.580	0.017	0.023	
D	6.350	6.650	0.250	0.262	
D1	5.200	5.400	0.205	0.213	
E	5.400	5.700	0.213	0.224	
е	2.300	TYP.	0.091 TYP.		
e1	4.500	4.700	0.177	0.185	
L	9.500	9.900	0.374	0.390	
L1	2.550	2.900	0.100	0.114	
L2	1.400	1.780	0.055	0.070	
L3	0.600	0.900	0.024	0.035	
V	3.800	REF.	0.150 REF.		



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