

# NCE N-Channel Enhancement Mode Power MOSFET

#### Description

The NCE60H10A uses advanced trench technology and design to provide excellent  $R_{DS(ON)}$  with low gate charge. This device is suitable for use in PWM, load switching and general purpose applications.

#### **General Features**

V<sub>DS</sub> =60V,I<sub>D</sub> =100A
R<sub>DS(ON)</sub> < 5.5 mΩ @ V<sub>GS</sub>=10V (Typ:4.8mΩ)

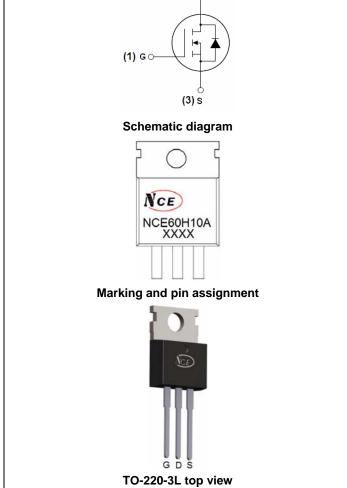
- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Special designed for convertors and power controls
- Good stability and uniformity with high E<sub>AS</sub>
- Excellent package for good heat dissipation
- Special process technology for high ESD capability

### Application

- Power switching application
- Hard switched and High frequency circuits
- Uninterruptible power supply

#### 100% UIS TESTED!

**100% ΔVds TESTED!** 



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#### Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCE60H10A	NCE60H10A	TO-220-3L	-	-	-

#### Absolute Maximum Ratings (T<sub>A</sub>=25℃ unless otherwise noted)

Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	VDS	60	V	
Gate-Source Voltage	Vgs	±20	V	
Drain Current-Continuous	I <sub>D</sub>	100	A	
Drain Current-Continuous(Tc=100℃)	I <sub>D</sub> (100℃)	70	А	
Pulsed Drain Current	I <sub>DM</sub>	320	Α	
Maximum Power Dissipation	PD	170	W	
Derating factor		1.13	W/℃	
Single pulse avalanche energy (Note 5)	E <sub>AS</sub>	812	mJ	
Operating Junction and Storage Temperature Range	T <sub>J</sub> ,T <sub>STG</sub>	-55 To 175	°C	





#### **Thermal Characteristic**

Thermal Resistance, Junction-to-Case <sup>(Note 2)</sup>	R <sub>θJC</sub>	0.88	°C/W
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## Electrical Characteristics (T<sub>A</sub>=25<sup>°</sup>C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics	·					
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250µA	60	-	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =60V,V <sub>GS</sub> =0V	-	-	1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V,V <sub>DS</sub> =0V	-	-	±100	nA
On Characteristics (Note 3)	·	·				
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> ,I <sub>D</sub> =250µA	2	2.85	4	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =20A	-	4.8	5.5	mΩ
Forward Transconductance	<b>g</b> fs	V <sub>DS</sub> =5V,I <sub>D</sub> =20A	-	50	-	S
Dynamic Characteristics (Note4)	·	·				
Input Capacitance	C <sub>lss</sub>		-	4900	-	PF
Output Capacitance	C <sub>oss</sub>	V <sub>DS</sub> =25V,V <sub>GS</sub> =0V,	-	380	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>	F=1.0MHz	-	290	-	PF
Switching Characteristics (Note 4)	·	·				
Turn-on Delay Time	t <sub>d(on)</sub>		-	17	-	nS
Turn-on Rise Time	tr	VDD=35V,RL=15Ω	-	11	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>	RG=2.5Ω,VGS=10V	-	55	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	15	-	nS
Total Gate Charge	Qg	)/ <u>20)//</u> 00A	-	100	-	nC
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}=30V,I_{D}=20A,$	-	21	-	nC
Gate-Drain Charge	Q <sub>gd</sub>	V <sub>GS</sub> =10V	-	30	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>S</sub> =20A	-	-	1.2	V
Diode Forward Current (Note 2)	I <sub>S</sub>		-	-	100	А
Reverse Recovery Time	t <sub>rr</sub>	Tj=25℃,I <sub>F</sub> =100A	-		37	nS
Reverse Recovery Charge	Qrr	di/dt=100A/µs <sup>(Note3)</sup>	-		58	nC
Forward Turn-On Time	t <sub>on</sub>	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD				y LS+LD)

#### Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- **2.** Surface Mounted on FR4 Board,  $t \le 10$  sec.
- **3.** Pulse Test: Pulse Width  $\leq$  300µs, Duty Cycle  $\leq$  2%.
- 4. Guaranteed by design, not subject to production
- **5.** EAS condition: Tj=25 $^{\circ}$ C,V<sub>DD</sub>=35V,V<sub>G</sub>=10V,L=0.5mH,Rg=25 $\Omega$



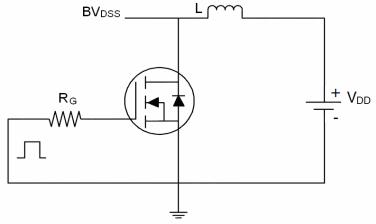
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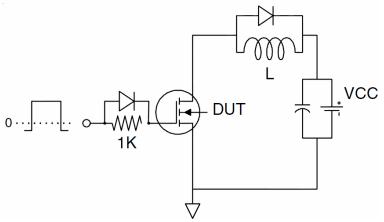


# **Test Circuit**

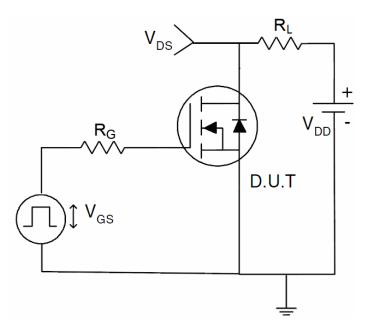
1) E<sub>AS</sub> Test Circuits



## 2) Gate Charge Test Circuit



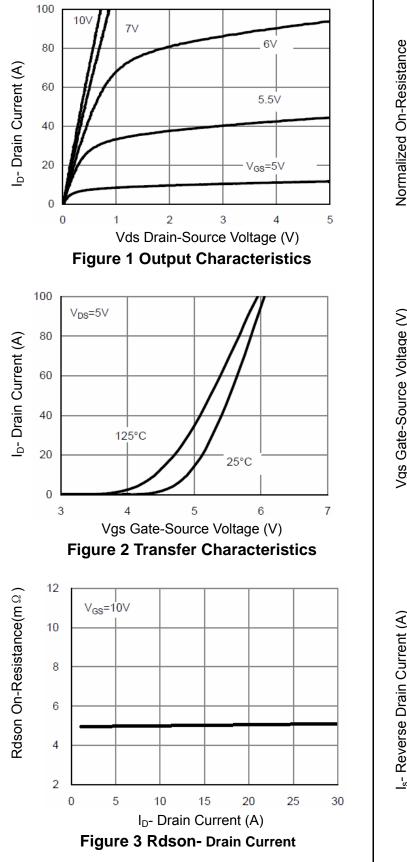
## 3) Switch Time Test Circuit

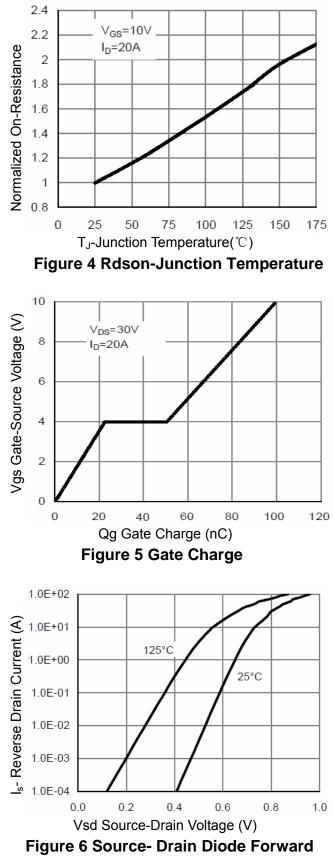






## **Typical Electrical and Thermal Characteristics (Curves**







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NCE60H10A

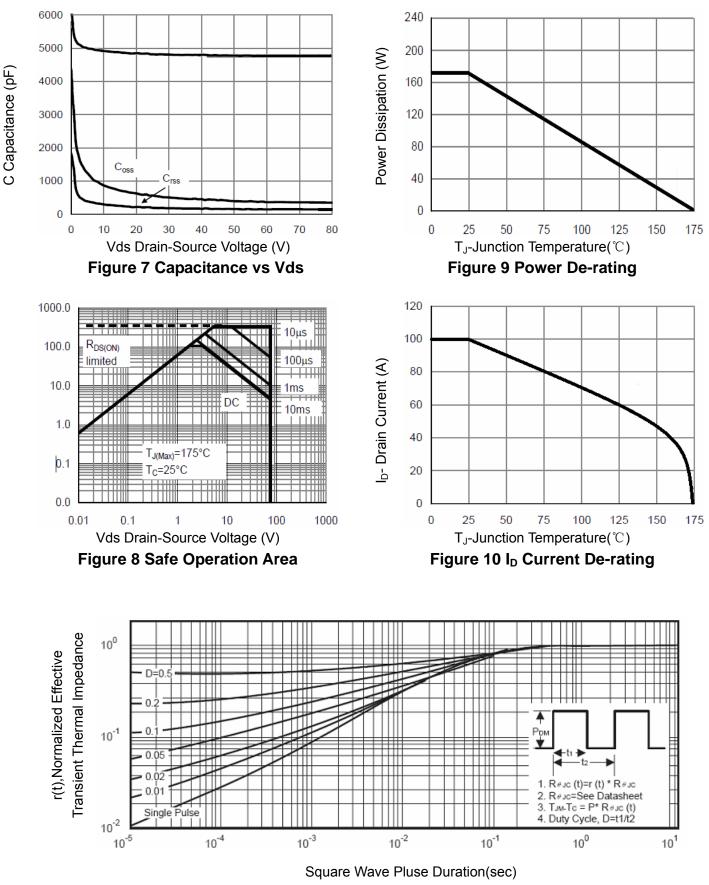


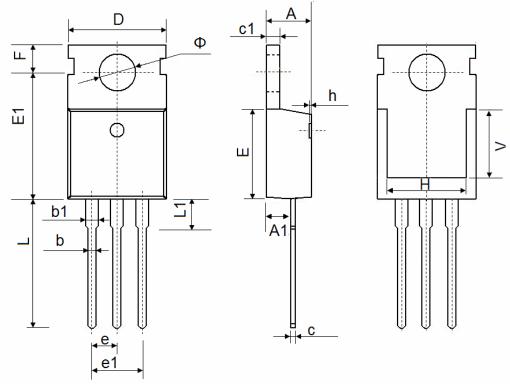
Figure 11 Normalized Maximum Transient Thermal Impedance



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## TO-220-3L Package Information



Symbol	Dimensions	In Millimeters	Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
А	4.400	4.600	0.173	0.181	
A1	2.250	2.550	0.089	0.100	
b	0.710	0.910	0.028	0.036	
b1	1.170	1.370	0.046	0.054	
С	0.330	0.650	0.013	0.026	
c1	1.200	1.400	0.047	0.055	
D	9.910	10.250	0.390	0.404	
E	8.9500	9.750	0.352	0.384	
E1	12.650	12.950	0.498	0.510	
e	2.54	0 TYP.	0.100 TYP.		
e1	4.980	5.180	0.196	0.204	
F	2.650	2.950	0.104	0.116	
Н	7.900	8.100	0.311	0.319	
h	0.000	0.300	0.000	0.012	
L	12.900	13.400	0.508	0.528	
L1	2.850	3.250	0.112	0.128	
V	7.50	0 REF.	0.295 REF.		
Φ	3.400	3.800	0.134	0.150	







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