

# NCE N-Channel Enhancement Mode Power MOSFET

# Description

The NCE75H26T uses advanced trench technology and design to provide excellent  $R_{DS(ON)}$  with low gate charge. It can be used in automotive applications and a wide variety of other applications.

### **General Features**

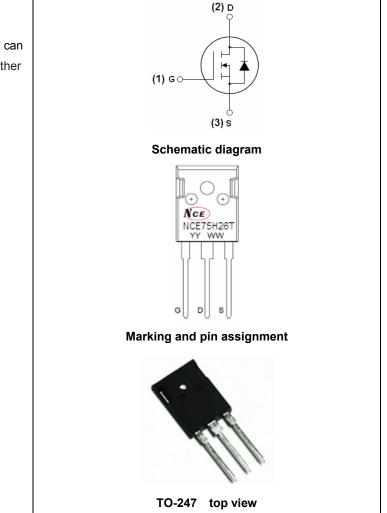
- $V_{DSS} = 75V, I_D = 260A$  $R_{DS(ON)} < 3m\Omega @ V_{GS} = 10V$  (Typ: 2.3 m $\Omega$ )
- Good stability and uniformity with high E<sub>AS</sub>
- Special process technology for high ESD capability
- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Excellent package for good heat dissipation

# Application

- Automotive applications
- Hard switched and high frequency circuits
- Uninterruptible power supply

100% UIS TESTED!

100% ΔVds TESTED!



# Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCE75H26T	NCE75H26T	TO-247	-	-	-

# Absolute Maximum Ratings (T<sub>c</sub>=25℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	VDSS	75	V
Gate-Source Voltage	Vgs	±20	V
Drain Current-Continuous	Ι <sub>D</sub>	260	А
Drain Current-Continuous(T <sub>C</sub> =100℃)	I <sub>D</sub> (100℃)	200	А
Pulsed Drain Current	I <sub>DM</sub>	1060	А
Maximum Power Dissipation	PD	385	W
Derating factor		2.57	W/℃
Single pulse avalanche energy (Note 3)	E <sub>AS</sub>	2200	mJ
Peak Diode Recovery dv/dt (Note 4)	dv/dt	13	V/ns



**Pb Free Product** 

NCE75H26T

Operating Junction and Storage Temperature Range	T <sub>J</sub> ,T <sub>STG</sub>	-55 To 175	°C

# **Thermal Characteristic**

Thermal Resistance, Junction-to-Case (Note 1)	R <sub>eJC</sub>	0.39	°C/W
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#### Electrical Characteristics (T<sub>c</sub>=25<sup>°</sup>C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics	·					
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250µA	75	86	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS}$ =75V, $V_{GS}$ =0V	-	-	1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	$V_{GS}$ =±20V, $V_{DS}$ =0V	-	-	±200	nA
On Characteristics						
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS}=V_{GS}$ , $I_{D}=250\mu A$	2	3	4	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	$V_{GS}$ =10V, I <sub>D</sub> =40A	-	2.3	3	mΩ
Forward Transconductance	<b>g</b> fs	V <sub>DS</sub> =25V,I <sub>D</sub> =40A	260	-	-	S
Dynamic Characteristics			•			
Input Capacitance	C <sub>lss</sub>		-	15700	-	PF
Output Capacitance	C <sub>oss</sub>	V <sub>DS</sub> =30V,V <sub>GS</sub> =0V, F=1.0MHz	-	2410	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>		-	1240	-	PF
Switching Characteristics						
Turn-on Delay Time	t <sub>d(on)</sub>	)/ _20)// _404	-	17	-	nS
Turn-on Rise Time	tr	V <sub>DD</sub> =38V,I <sub>D</sub> =40A V <sub>GS</sub> =10V,R <sub>GEN</sub> =1.2Ω	-	80	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>	(Note2)	-	100	-	nS
Turn-Off Fall Time	t <sub>f</sub>	(NOLEZ)	-	62	-	nS
Total Gate Charge	Qg	V <sub>DS</sub> =38V,I <sub>D</sub> =160A,	-	160	-	nC
Gate-Source Charge	Q <sub>gs</sub>	V <sub>DS</sub> =30V,I <sub>D</sub> =160A, V <sub>GS</sub> =10V(Note2)	-	35	-	nC
Gate-Drain Charge	Q <sub>gd</sub>	$v_{GS} = 10 v (100 lez)$	-	55	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>S</sub> =40A	-	-	1.2	V
Reverse Recovery Time	trr	TJ = 25°C, IF = 40A	-	52	-	nS
Reverse Recovery Charge	Qrr	di/dt = 100A/µs(Note2)	-	110	-	nC
Forward Turn-On Time	t <sub>on</sub>	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD)				

#### Notes:

- 1. Surface Mounted on FR4 Board, t  $\leq$  10 sec.
- 2. Pulse Test: Pulse Width ≤ 400 $\mu$ s, Duty Cycle ≤ 2%.
- 3. EAS condition: Tj=25 $^\circ C$  ,V\_{DD}=37.5V ,V\_G=10V ,L=2mH ,Rg=25\Omega ,I\_{AS}=37A

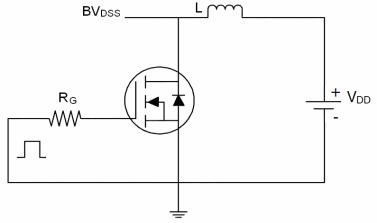




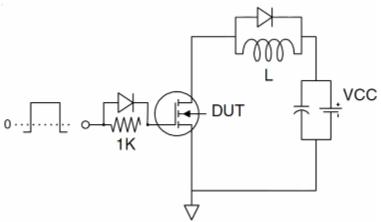


# Test circuit

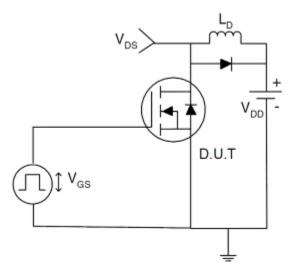
1) E<sub>AS</sub> test Circuit



# 2) Gate charge test Circuit



3) Switch Time Test Circuit



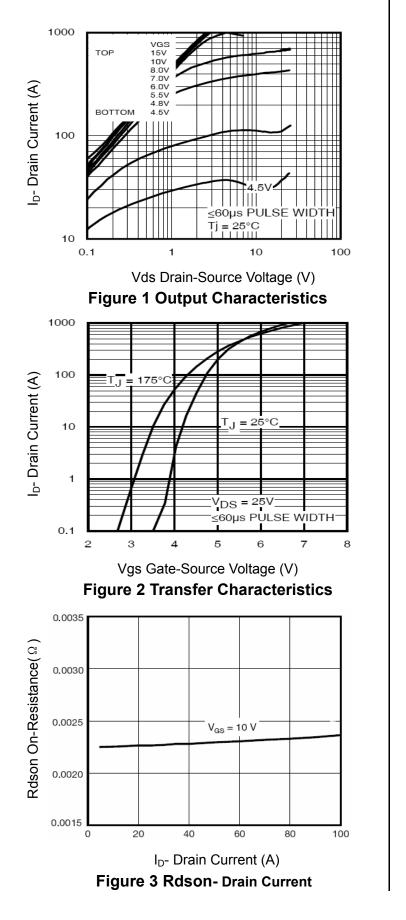


2.5





# **Typical Electrical and Thermal Characteristics**



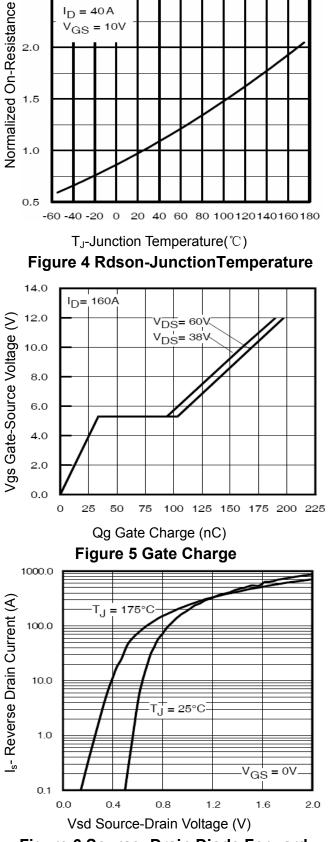
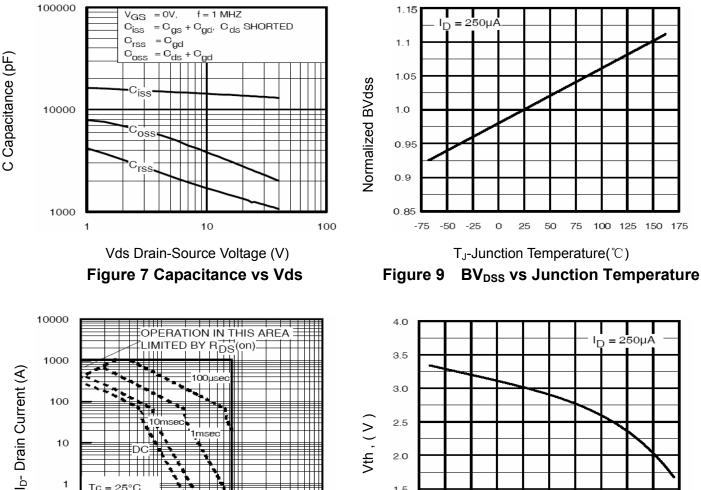


Figure 6 Source- Drain Diode Forward







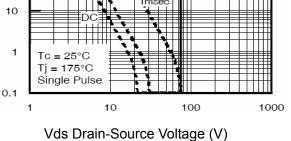


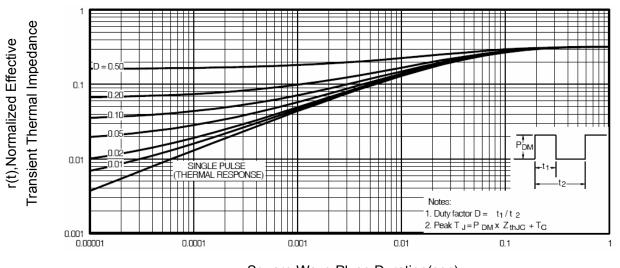
Figure 8 Safe Operation Area

T<sub>J</sub>-Junction Temperature ( $^{\circ}C$ )

100 125 150 175

Figure 10 V<sub>GS(th)</sub> vs Junction Temperature

25 50 75



1.5

1.0

-75 -50 -25 0

Square Wave Pluse Duration(sec) Figure 11 Normalized Maximum Transient Thermal Impedance

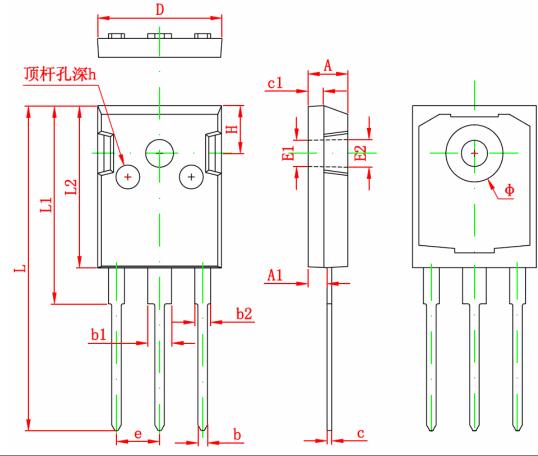
Page 5



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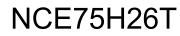
# **TO-247 Package Information**



Symbol	Dimensions I	n Millimeters	Dimensions In Inches		
Symbol	Min	Max	Min	Max	
Α	4.850	5.150	0.191	0.200	
A1	2.200	2.600	0.087	0.102	
b	1.000	1.400	0.039	0.055	
b1	2.800	3.200	0.110	0.126	
b2	1.800	2.200	0.071	0.087	
c	0.500	0.700	0.020	0.028	
c1	1.900	2.100	0.075	0.083	
D	15.450	15.750	0.608	0.620	
E1	3.500REF		0.138REF		
E2	3.600REF		0.142REF		
L	40.900	41.300	1.610	1.626	
L1	24.800	25.100	0.976	0.988	
L2	20.300	20.600	0.799	0.811	
Ф	7.100	7.300	0.280	0.287	
e	5.450TYP		0.215TYP		
Н	5.980TYP		0.235 REF		
h	0.000	0.300	0.000	0.012	







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