



NCE N-Channel Super Trench Power MOSFET

Description

The NCEP6080AG uses **Super Trench** technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of $R_{\text{DS(ON)}}$ and Q_g . This device is ideal for high-frequency switching and synchronous rectification.

General Features

V_{DS} =60V,I_D =80A

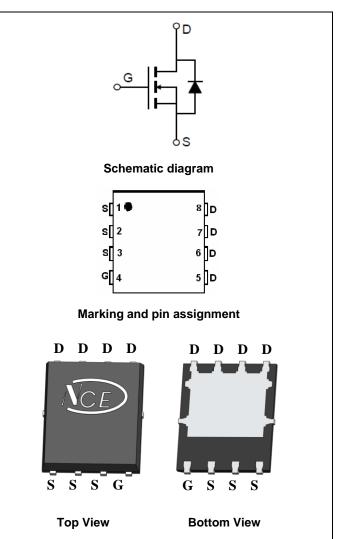
 $R_{DS(ON)} < 4.0 m\Omega$ @ $V_{GS} = 10V$ (Typ:3.5 m Ω) $R_{DS(ON)} < 5.0 m\Omega$ @ $V_{GS} = 4.5V$ (Typ:4.0 m Ω)

- Excellent gate charge x R_{DS(on)} product
- Very low on-resistance R_{DS(on)}
- 150 °C operating temperature
- Pb-free lead plating
- 100% UIS tested

Application

- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification

100% UIS TESTED! 100% ΔVds TESTED!



Package Marking and Ordering Information

| Device Marking | Device | Device Package | Reel Size | Tape width | Quantity |
|-----------------------|------------|----------------|-----------|------------|----------|
| NCEP6080AG | NCEP6080AG | DFN5X6-8L | - | - | - |

Absolute Maximum Ratings (T_C=25 ℃unless otherwise noted)

| Parameter | Symbol | Limit | Unit |
|--|------------------------|------------|------------|
| Drain-Source Voltage | V _{DS} | 60 | V |
| Gate-Source Voltage | V _G s | ±20 | V |
| Drain Current-Continuous (Silicon Limited) | I _D | 80 | А |
| Drain Current-Continuous(T _C =100℃) | I _D (100°C) | 58 | Α |
| Pulsed Drain Current | I _{DM} | 320 | А |
| Maximum Power Dissipation | P _D | 85 | W |
| Derating factor | | 0.68 | W/℃ |
| Single pulse avalanche energy (Note 5) | E _{AS} | 400 | mJ |
| Operating Junction and Storage Temperature Range | T_{J}, T_{STG} | -55 To 150 | $^{\circ}$ |



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NCEP6080AG

Thermal Characteristic

Electrical Characteristics (T_C=25 ℃ unless otherwise noted)

| Parameter | Symbol | Condition | Min | Тур | Max | Unit |
|------------------------------------|---------------------|--|-----|------|------|------|
| Off Characteristics | | | | | | |
| Drain-Source Breakdown Voltage | BV _{DSS} | V _{GS} =0V I _D =250μA | 60 | | - | V |
| Zero Gate Voltage Drain Current | I _{DSS} | V _{DS} =60V,V _{GS} =0V | - | - | 1 | μΑ |
| Gate-Body Leakage Current | I _{GSS} | $V_{GS}=\pm20V, V_{DS}=0V$ | - | - | ±100 | nA |
| On Characteristics (Note 3) | | | | | | |
| Gate Threshold Voltage | V _{GS(th)} | $V_{DS}=V_{GS}$, $I_{D}=250\mu A$ | 1.0 | 1.7 | 2.4 | V |
| Danie Course On Otata Danietana | 5 | V _{GS} =10V, I _D =40A | - | 3.5 | 4.0 | mΩ |
| Drain-Source On-State Resistance | R _{DS(ON)} | V _{GS} =4.5V, I _D =40A | - | 4.0 | 5.0 | mΩ |
| Forward Transconductance | G FS | V _{DS} =10V,I _D =40A | 40 | - | - | S |
| Dynamic Characteristics (Note4) | | | | | | |
| Input Capacitance | C _{lss} | | - | 4000 | - | PF |
| Output Capacitance | Coss | $V_{DS}=30V, V_{GS}=0V,$ | - | 680 | - | PF |
| Reverse Transfer Capacitance | C _{rss} | F=1.0MHz | - | 23 | - | PF |
| Switching Characteristics (Note 4) | | | | | | |
| Turn-on Delay Time | t _{d(on)} | | - | 11 | - | nS |
| Turn-on Rise Time | t _r | V_{DD} =30 V , I_D =40 A | - | 5 | - | nS |
| Turn-Off Delay Time | t _{d(off)} | $V_{GS}{=}10V, R_{G}{=}4.7\Omega$ | - | 56 | - | nS |
| Turn-Off Fall Time | t _f | | - | 12 | - | nS |
| Total Gate Charge | Qg | V 00V/ 1 40A | - | 67 | | nC |
| Gate-Source Charge | Q _{gs} | $V_{DS}=30V, I_{D}=40A,$ | - | 12 | | nC |
| Gate-Drain Charge | Q_{gd} | V _{GS} =10V | - | 8.5 | | nC |
| Drain-Source Diode Characteristics | | | • | | | |
| Diode Forward Voltage (Note 3) | V_{SD} | V _{GS} =0V,I _S =80A | - | | 1.2 | V |
| Diode Forward Current (Note 2) | Is | | - | - | 80 | А |
| Reverse Recovery Time | t _{rr} | $T_J = 25$ °C, $I_F = I_S$ | - | 48 | | nS |
| Reverse Recovery Charge | Qrr | $di/dt = 100A/\mu s^{(Note3)}$ | - | 60 | | nC |

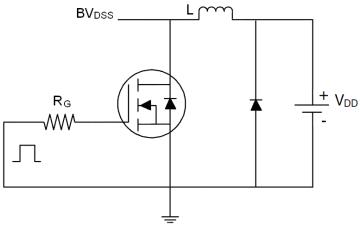
Notes:

- ${\bf 1.}\ {\bf Repetitive}\ {\bf Rating:}\ {\bf Pulse}\ {\bf width}\ {\bf limited}\ {\bf by}\ {\bf maximum}\ {\bf junction}\ {\bf temperature}.$
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- 3. Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
- 4. Guaranteed by design, not subject to production
- 5. EAS condition : Tj=25 $^{\circ}\text{C}$,VDD=30V,VG=10V,L=0.5mH,Rg=25 Ω

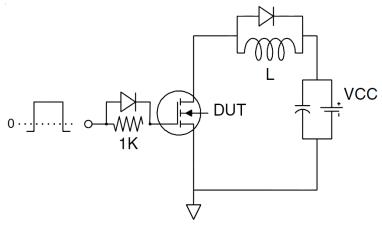


Test Circuit

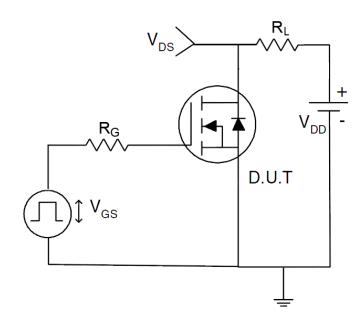
1) E_{AS} test Circuit



2) Gate charge test Circuit



3) Switch Time Test Circuit





Typical Electrical and Thermal Characteristics

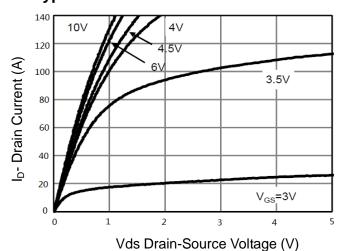


Figure 1 Output Characteristics

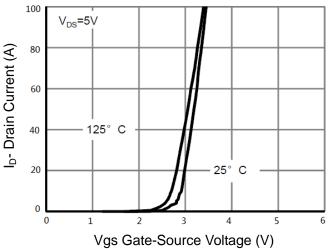


Figure 2 Transfer Characteristics

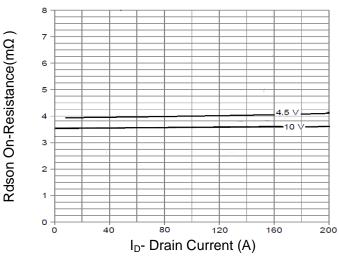


Figure 3 Rdson- Drain Current

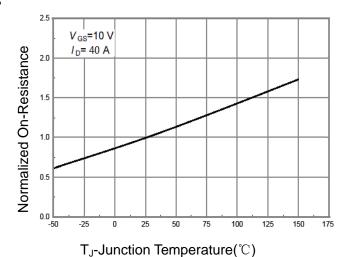


Figure 4 Rdson-JunctionTemperature

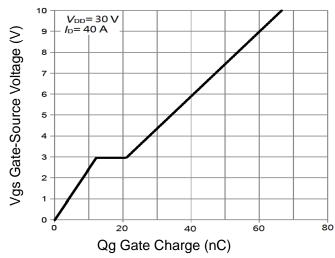


Figure 5 Gate Charge

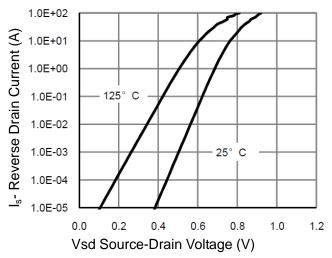


Figure 6 Source- Drain Diode Forward



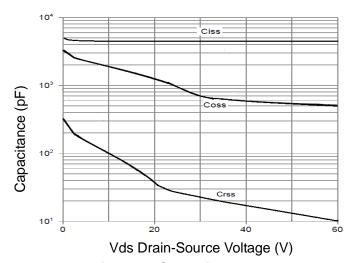


Figure 7 Capacitance vs Vds

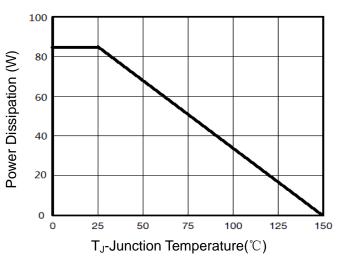


Figure 9 Power De-rating

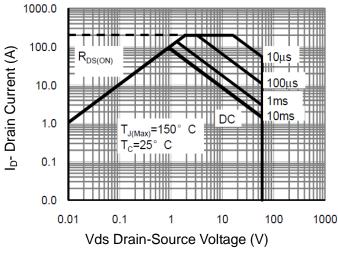


Figure 8 Safe Operation Area

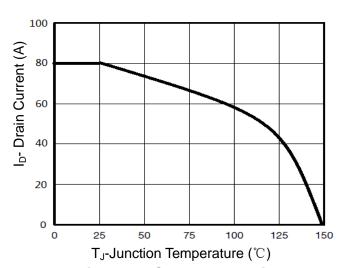


Figure 10 Current De-rating

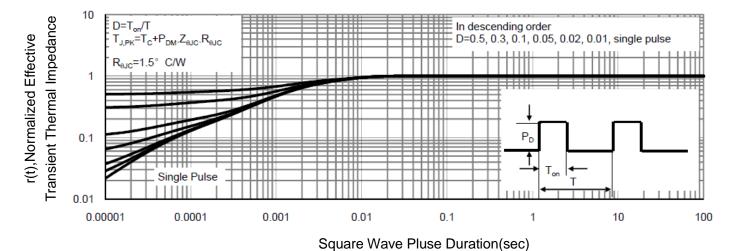
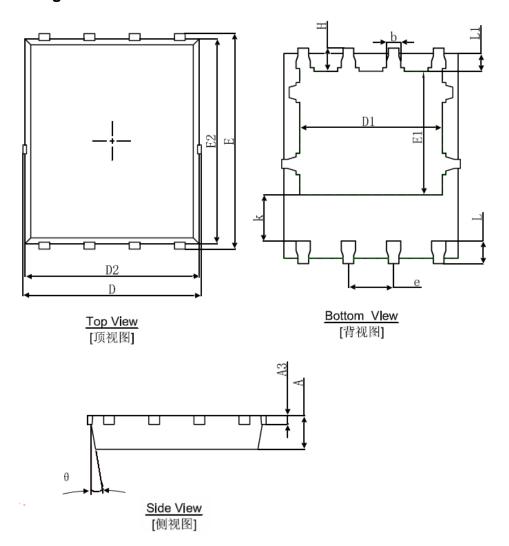


Figure 11 Normalized Maximum Transient Thermal Impedance



DFN5X6-8L Package Information



| C) male of | Dimensions In Millimeters | | Dimensions In Inches | | |
|------------|---------------------------|-------|----------------------|-------|--|
| Symbol | Min. | Max. | Min. | Max. | |
| Α | 0.900 | 1.000 | 0.035 | 0.039 | |
| A3 | 0.254REF. | | 0.010REF. | | |
| D | 4.944 | 5.096 | 0.195 | 0.201 | |
| E | 5.974 | 6.126 | 0.235 | 0.241 | |
| D1 | 3.910 | 4.110 | 0.154 | 0.162 | |
| E1 | 3.375 | 3.575 | 0.133 | 0.141 | |
| D2 | 4.824 | 4.976 | 0.190 | 0.196 | |
| E2 | 5.674 | 5.826 | 0.223 | 0.229 | |
| k | 1.190 | 1.390 | 0.047 | 0.055 | |
| b | 0.350 | 0.450 | 0.014 | 0.018 | |
| е | 1.270TYP. | | 0.050TYP. | | |
| L | 0.559 | 0.711 | 0.022 | 0.028 | |
| L1 | 0.424 | 0.576 | 0.017 | 0.023 | |
| Н | 0.574 | 0.726 | 0.023 | 0.029 | |
| θ | 8° | 12° | 8° | 12° | |



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