

# N-Channel Enhancement Mode Field Effect Transistor

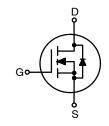
# 2N7000, 2N7002, NDS7002A

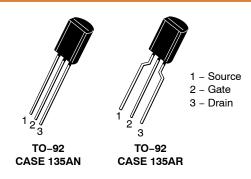
#### **Description**

These N-channel enhancement mode field effect transistors are produced using **onsemi**'s proprietary, high cell density, DMOS technology. These products have been designed to minimize on-state resistance while providing rugged, reliable, and fast switching performance. These products are particularly suited for low-voltage, low-current applications, such as small servo motor control, power MOSFET gate drivers, and other switching applications.

#### **Features**

- High Density Cell Design for Low R<sub>DS(on)</sub>
- Voltage Controlled Small Signal Switch
- Rugged and Reliable
- High Saturation Current Capability
- ESD Protection Level: HBM > 100 V, CDM > 2 kV
- This Device is Pb-Free and Halogen Free





#### MARKING DIAGRAM



\$Y = **onsemi** Logo &Z = Assembly Plant Code

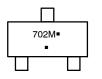
&3 = Date Code

2N7000 = Specific Device Code



SOT-23 CASE 318-08

- 1 Gate
- 2 Source 3 – Drain



MARKING DIAGRAM

702 = Specific Device Code

M = Date Code

= Pb-Free Package

(Note: Microdot may be in either location)

#### **ORDERING INFORMATION**

See detailed ordering and shipping information on page 7 of this data sheet.

# **ABSOLUTE MAXIMUM RATINGS** Values are at $T_C = 25^{\circ}C$ unless otherwise noted.

		Value			
Symbol	Parameter	2N7000	2N7002	NDS7002A	Unit
V <sub>DSS</sub>	Drain-to-Source Voltage	60			V
$V_{DGR}$	Drain-Gate Voltage (R <sub>GS</sub> ≤ 1 MW)	60			V
V <sub>GSS</sub>	Gate-Source Voltage - Continuous	±20			V
	Gate-Source Voltage - Non Repetitive (tp < 50 ms)	±40			
I <sub>D</sub>	Maximum Drain Current - Continuous	200	115	280	mA
	Maximum Drain Current - Pulsed	500	800	1500	
P <sub>D</sub>	Maximum Power Dissipation Derated above 25°C	400	200	300	mW
		3.2	1.6	2.4	mW/°C
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temperature Range	-55 to 150 -65 to 150		°C	
TL	Maximum Lead Temperature for Soldering Purposes, 1/16-inch from Case for 10 s	300			°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

# **THERMAL CHARACTERISTICS** Values are at $T_C = 25$ °C unless otherwise noted.

		Value			
Symbol	Parameter	2N7000	2N7002	NDS7002A	Unit
$R_{ heta JA}$	Thermal Resistance, Junction to Ambient	312.5	625	417	°C/W

### **ELECTRICAL CHARACTERISTICS**

Values are at  $T_C$  = 25°C unless otherwise noted.

Symbol	Parameter	Conditions	Туре	Min	Тур	Max	Unit
FF CHARA	CTERISTICS						
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 10 μA	All	60	-	-	٧
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 48 V, V <sub>GS</sub> = 0 V	2N7000	-	-	1	μΑ
		V <sub>DS</sub> = 48 V, V <sub>GS</sub> = 0 V, T <sub>C</sub> = 125°C		-	-	1	mA
		V <sub>DS</sub> = 60 V, V <sub>GS</sub> = 0 V	2N7002	-	-	1	μΑ
		$V_{DS} = 60 \text{ V}, V_{GS} = 0 \text{ V},$ $T_C = 125^{\circ}\text{C}$	NDS7002A	-	-	0.5	mA
I <sub>GSSF</sub>	Gate – Body Leakage, Forward	V <sub>GS</sub> = 15 V, V <sub>DS</sub> = 0 V	2N7000	-	-	10	nA
		V <sub>GS</sub> = 20 V, V <sub>DS</sub> = 0 V	2N7002 NDS7002A	-	-	100	
I <sub>GSSR</sub>	Gate – Body Leakage, Reverse	$V_{GS} = -15 \text{ V}, V_{DS} = 0 \text{ V}$	2N7000	-	-	-10	nA
		$V_{GS} = -20 \text{ V}, V_{DS} = 0 \text{ V}$	2N7002 NDS7002A	-	-	-100	
N CHARAC	CTERISTICS		-				-
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{DS} = V_{GS}$ , $I_D = 1 \text{ mA}$	2N7000	0.8	2.1	3	V
		$V_{DS} = V_{GS}$ , $I_D = 250 \mu A$	2N7002 NDS7002A	1	2.1	2.5	

# **ELECTRICAL CHARACTERISTICS** (continued)

Values are at  $T_C = 25^{\circ}C$  unless otherwise noted.

Symbol	Parameter	Conditions	Туре	Min	Тур	Max	Unit
N CHARAC	TERISTICS						
R <sub>DS(on)</sub>	Static Drain-Source On-Resistance	$V_{GS} = 10 \text{ V}, I_D = 500 \text{ mA}$	2N7000	-	1.2	5	Ω
	On-nesistance	$V_{GS} = 10 \text{ V}, I_D = 500 \text{ mA},$ $T_C = 125^{\circ}\text{C}$		-	1.9	9	
		V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 75 mA		-	1.8	5.3	
		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 500 mA	2N7002	-	1.2	7.5	
		$V_{GS} = 10 \text{ V}, I_D = 500 \text{ mA}, T_C = 100^{\circ}\text{C}$	] [	-	1.7	13.5	
		V <sub>GS</sub> = 5 V, I <sub>D</sub> = 50 mA		_	1.7	7.5	
		$V_{GS} = 5 \text{ V, I}_{D} = 50 \text{ mA,}$ $T_{C} = 100^{\circ}\text{C}$	] [	-	2.4	13.5	
		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 500 mA	NDS7002A	-	1.2	2	
		$V_{GS} = 10 \text{ V}, I_D = 500 \text{ mA},$ $T_C = 125^{\circ}\text{C}$		-	2	3.5	
		V <sub>GS</sub> = 5 V, I <sub>D</sub> = 50 mA		_	1.7	3	
		$V_{GS} = 5 \text{ V, } I_D = 50 \text{ mA,}$ $T_C = 125^{\circ}\text{C}$	] [	-	2.8	5	
V <sub>DS(on)</sub>	Drain-Source On-Voltage	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 500 mA	2N7000	-	0.6	2.5	V
		$V_{GS} = 4.5 \text{ V}, I_D = 75 \text{ mA}$		-	0.14	0.4	
		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 500 mA	2N7002	-	0.6	3.75	
		$V_{GS} = 5.0 \text{ V}, I_D = 50 \text{ mA}$		_	0.09	1.5	
		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 500 mA	NDS7002A	-	0.6	1	
		$V_{GS} = 5.0 \text{ V}, I_D = 50 \text{ mA}$		-	0.09	0.15	
9FS	Forward Transconductance	$V_{DS} = 10 \text{ V}, I_D = 200 \text{ mA}$	2N7000	100	320	-	mS
		$V_{DS} \ge 2 V_{DS(on)}, I_D = 200 \text{ mA}$	2N7002	80	320	-	
		$V_{DS} \ge 2 V_{DS(on)}$ , $I_D = 200 \text{ mA}$	NDS7002A	80	320	-	
YNAMIC CI	HARACTERISTICS						
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 0 V, f = 1.0 MHz	All	-	20	50	pF
C <sub>oss</sub>	Output Capacitance	1 - 1.0 WILL	All	_	11	25	
C <sub>rss</sub>	Reverse Transfer Capacitance		All	-	4	5	
t <sub>on</sub>	Turn-On Time	$\begin{aligned} &V_{DD}=15\text{ V, R}_{L}=25\ \Omega,\\ &I_{D}=500\text{ mA, V}_{GS}=10\text{ V,}\\ &R_{GEN}=25\ \Omega \end{aligned}$	2N7000	-	_	10	ns
		$\begin{split} &V_{DD}=30~\text{V, R}_{L}=150~\Omega,\\ &I_{D}=200~\text{mA, V}_{GS}=10~\text{V,}\\ &R_{GEN}=25~\Omega \end{split}$	2N7002 NDS7002A	-	-	20	
t <sub>off</sub>	Turn-Off Time	$V_{DD}$ = 15 V, $R_{L}$ = 25 $\Omega$ , $I_{D}$ = 500 mA, $V_{GS}$ = 10 V, $R_{GEN}$ = 25 $\Omega$	2N7000	-	_	10	ns
		$\begin{split} &V_{DD}=30 \text{ V, R}_{L}=150 \Omega,\\ &I_{D}=200 \text{ mA, V}_{GS}=10 \text{ V,}\\ &R_{GEN}=25 \Omega \end{split}$	2N7002 NDS7002A	-	_	20	
RAIN-SOU	RCE DIODE CHARACTERISTIC	S AND MAXIMUM RATINGS					
I <sub>S</sub>	Maximum Continuous Drain-S	ource Diode Forward Current	2N7002	_	_	115	mA
			NDS7002A	_	_	280	]

#### **ELECTRICAL CHARACTERISTICS** (continued)

Values are at  $T_C = 25^{\circ}C$  unless otherwise noted.

Symbol	Parameter	Conditions	Туре	Min	Тур	Max	Unit	
DRAIN-SOU	DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS							
I <sub>SM</sub>	Maximum Pulsed Drain-Source Diode Forward Current		2N7002	-	_	8.0	Α	
			NDS7002A	-	_	1.5		
V <sub>SD</sub>	Drain-Source Diode Forward Voltage	V <sub>GS</sub> = 0 V, I <sub>S</sub> = 115 mA (Note 1)	2N7002	-	0.88	1.5	V	
		V <sub>GS</sub> = 0 V, I <sub>S</sub> = 400 mA (Note 1)	NDS7002A	-	0.88	1.2		

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

#### TYPICAL PERFORMANCE CHARACTERISTICS

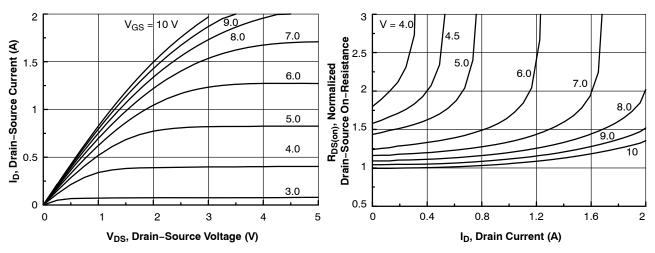


Figure 1. On-Region Characteristics

Figure 2. On-Resistance Variation with Gate Voltage and Drain Current

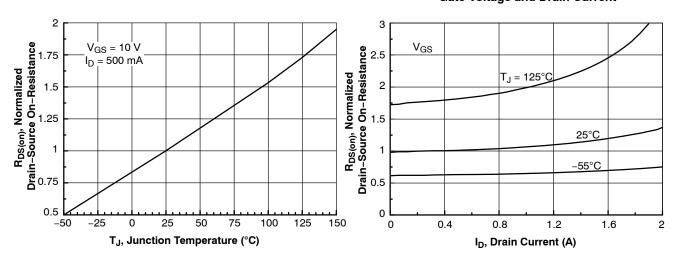


Figure 3. On–Resistance Variation with Temperature

Figure 4. On–Resistance Variation with Drain Current and Temperature

<sup>1.</sup> Pulse test: Pulse Width ≤[300 μs, Duty Cycel ≤[2 %

#### TYPICAL PERFORMANCE CHARACTERISTICS (CONTINUED)

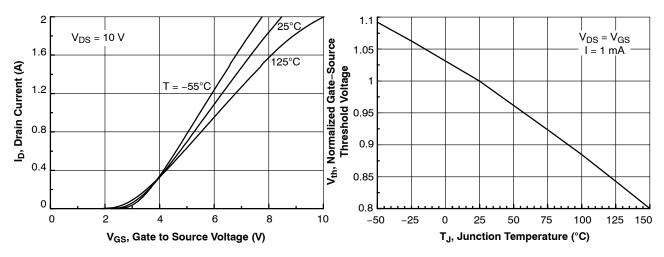


Figure 5. Transfer Characteristics

Figure 6. Gate Threshold Variation with Temperature

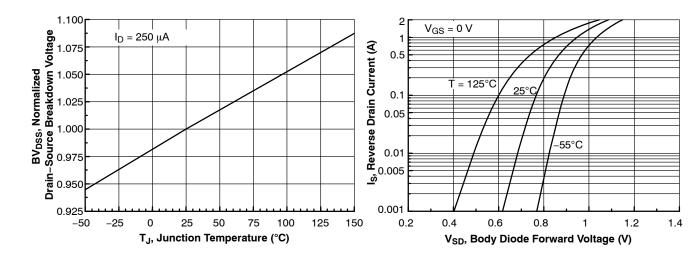


Figure 7. Breakdown Voltage Variation with Temperature

Figure 8. Body Diode Forward Voltage Variation with

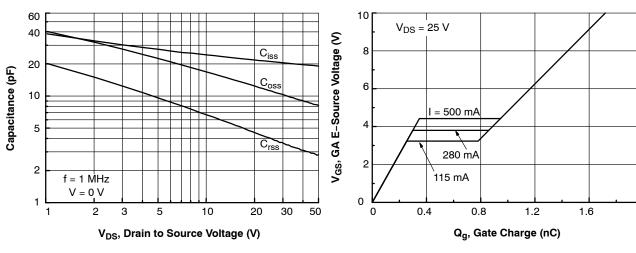


Figure 9. Capacitance Characteristics

Figure 10. Gate Charge Characteristics

2

### TYPICAL PERFORMANCE CHARACTERISTICS (CONTINUED)

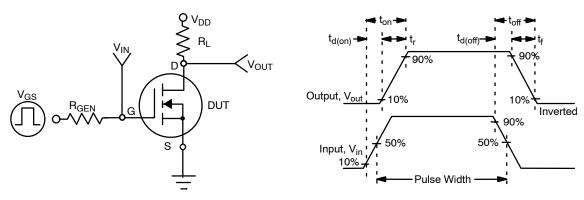


Figure 11. Switching Test Circuit

Figure 12. Switching Waveforms

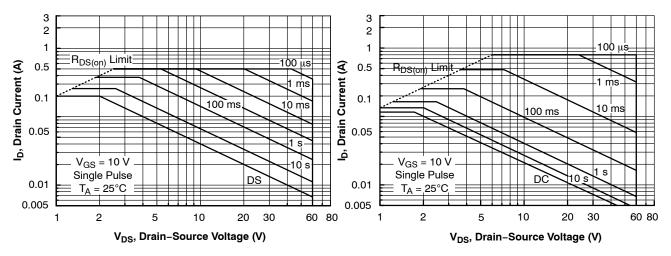


Figure 13. 2N7000 Maximum Safe Operating Area

Figure 14. 2N7002 Maximum Safe Operating Area

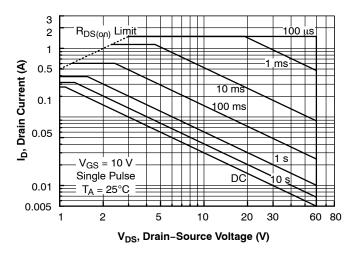


Figure 15. NDS7000A Maximum Safe Operating Area

#### TYPICAL PERFORMANCE CHARACTERISTICS (CONTINUED)

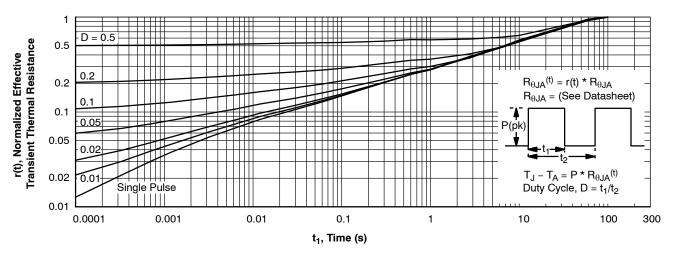


Figure 16. TO-92, 2N7000 Transient Thermal Response Curve

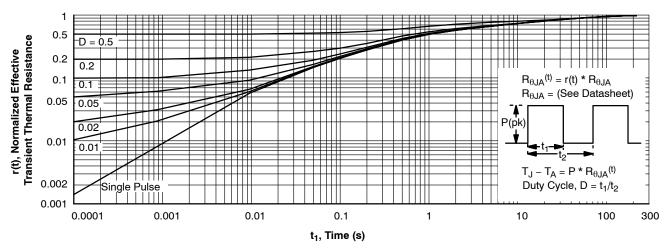


Figure 17. SOT-23, 2N7002 / NDS7002A Transient Thermal Response Curve

#### **ORDERING INFORMATION**

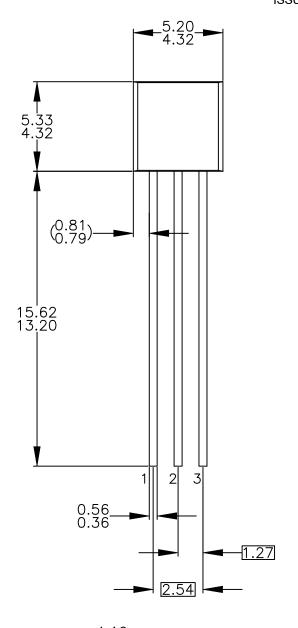
Part Number	Marking	Package	Packing Method <sup>†</sup>	Min Order Qty / Immediate Pack Qty
2N7000	2N7000	TO-92 3L	Bulk	10000 / 1000
2N7000-D74Z		(Pb-Free)	Ammo	2000 / 2000
2N7000-D75Z			Tape and Reel	2000 / 2000
2N7000-D26Z				2000 / 2000
2N7002	702	SOT-23 3L	Tape and Reel	3000 / 3000
NDS7002A	712	(Pb-Free)		3000 / 3000

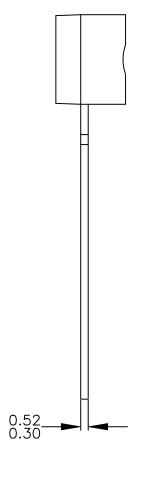
<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, <a href="https://example.com/BRD8011/D">BRD8011/D</a>.



#### TO-92 3 4.825x4.76 CASE 135AN ISSUE O

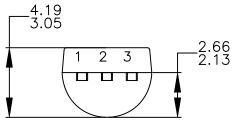
**DATE 31 JUL 2016** 





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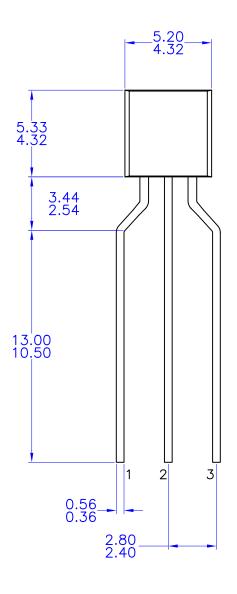


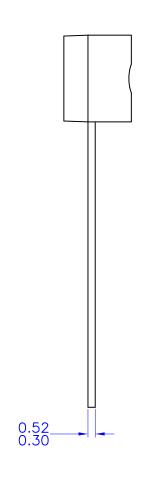


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CASE 135AR ISSUE O

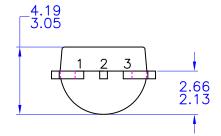
**DATE 30 SEP 2016** 





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