

Silicon Carbide (SiC) Schottky Diode – EliteSiC, 30 A, 1200 V, D3, TO-247-3L

NDSH30120CDN

Description

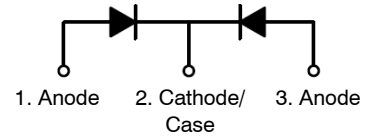
Silicon Carbide (SiC) Schottky Diodes use a completely new technology that provides superior switching performance and higher reliability compared to Silicon. No reverse recovery current, temperature independent switching characteristics, and excellent thermal performance sets Silicon Carbide as the next generation of power semiconductor. System benefits include highest efficiency, faster operating frequency, increased power density, reduced EMI, and reduced system size and cost.

Features

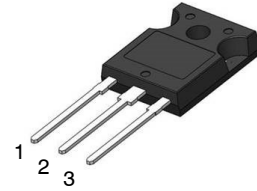
- Max Junction Temperature 175°C
- Avalanche Rated 110 mJ
- High Surge Current Capacity
- Positive Temperature Coefficient
- Ease of Paralleling
- No Reverse Recovery / No Forward Recovery
- This Device is Halide Free and RoHS Compliant with Exemption 7a, Pb-Free 2LI (on second level interconnection)

Applications

- General Purpose
- SMPS, Solar Inverter, UPS
- Power Switching Circuits



Schottky Diode



TO-247-3LD
 CASE 340CX

MARKING DIAGRAM



DSH30120CDN	= Specific Device Code
A	= Assembly Plant Code
YWW	= Date Code (Year & Week)
ZZ	= Lot Code

ORDERING INFORMATION

See detailed ordering and shipping information on page 2 of this data sheet.

NDSH30120CDN

ABSOLUTE MAXIMUM RATINGS ($T_J = 25^\circ\text{C}$ unless otherwise noted) (per leg)

Symbol	Parameter	Value	Unit	
V_{RRM}	Peak Repetitive Reverse Voltage	1200	V	
E_{AS}	Single Pulse Avalanche Energy (Note 1)	110	mJ	
I_F	Continuous Rectified Forward Current @ $T_C < 147^\circ\text{C}$	15*/30**	A	
	Continuous Rectified Forward Current @ $T_C < 135^\circ\text{C}$	19*/38**		
$I_{F, Max}$	Non-Repetitive Peak Forward Surge Current	$T_C = 25^\circ\text{C}$, 10 μs	749	A
		$T_C = 150^\circ\text{C}$, 10 μs	735	A
$I_{F, SM}$	Non-Repetitive Forward Surge Current	Half-Sine Pulse, $t_p = 8.3 \text{ ms}$	91	A
$I_{F, RM}$	Repetitive Forward Surge Current	Half-Sine Pulse, $t_p = 8.3 \text{ ms}$	39	A
P_{tot}	Power Dissipation	$T_C = 25^\circ\text{C}$	158	W
		$T_C = 150^\circ\text{C}$	26	W
T_J, T_{STG}	Operating and Storage Temperature Range	-55 to +175	$^\circ\text{C}$	

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

*Per leg.

** Per device.

1. E_{AS} of 110 mJ is based on starting $T_J = 25^\circ\text{C}$, $L = 0.5 \text{ mH}$, $I_{AS} = 21 \text{ A}$, $V = 50 \text{ V}$.

THERMAL CHARACTERISTICS (per leg)

Symbol	Parameter	Value	Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case, Max	0.95*/0.46**	$^\circ\text{C}/\text{W}$
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient, Max	40	$^\circ\text{C}/\text{W}$

*Per leg.

** Per device.

ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$ unless otherwise noted) (per leg)

Symbol	Parameter	Test Condition	Min	Typ	Max	Unit
V_F	Forward Voltage	$I_F = 15 \text{ A}$, $T_J = 25^\circ\text{C}$	-	1.40	1.75	V
		$I_F = 15 \text{ A}$, $T_J = 125^\circ\text{C}$	-	1.71	-	
		$I_F = 15 \text{ A}$, $T_J = 175^\circ\text{C}$	-	1.98	-	
I_R	Reverse Current	$V_R = 1200 \text{ V}$, $T_J = 25^\circ\text{C}$	-	1.5	200	μA
		$V_R = 1200 \text{ V}$, $T_J = 125^\circ\text{C}$	-	5	200	
		$V_R = 1200 \text{ V}$, $T_J = 175^\circ\text{C}$	-	12	200	
Q_C	Total Capacitive Charge	$V = 800 \text{ V}$	-	64	-	nC
C	Total Capacitance	$V_R = 1 \text{ V}$, $f = 100 \text{ kHz}$	-	927	-	pF
		$V_R = 400 \text{ V}$, $f = 100 \text{ kHz}$	-	58	-	
		$V_R = 800 \text{ V}$, $f = 100 \text{ kHz}$	-	44	-	

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

ORDERING INFORMATION

Part Number	Top Marking	Package	Shipping
NDSH30120CDN	DSH30120CDN	TO-247-3LD (Pb-Free / Halogen Free)	30 Units / Tube

TYPICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)

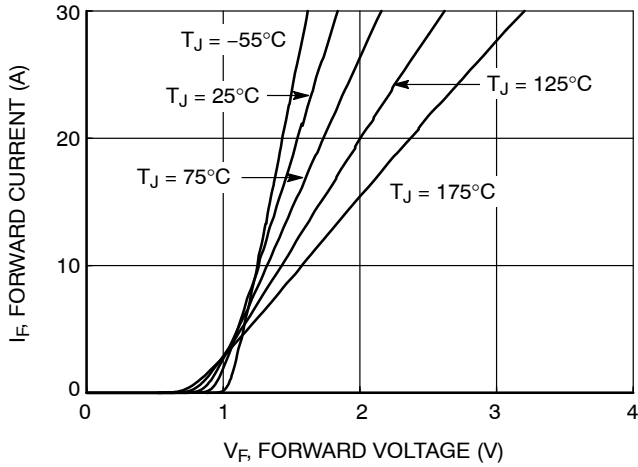


Figure 1. Forward Characteristics

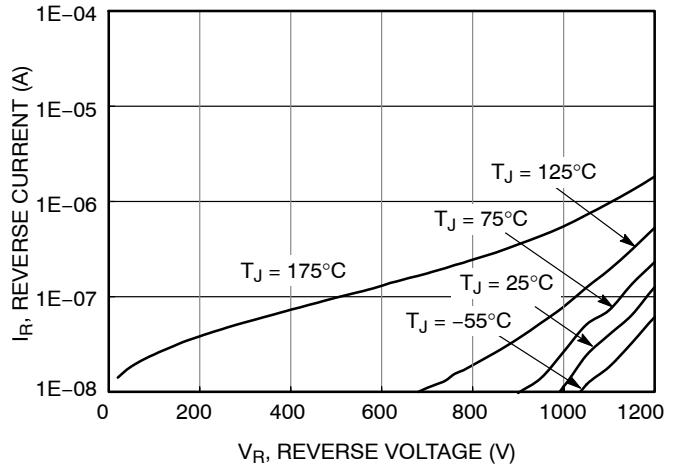


Figure 2. Reverse Characteristics

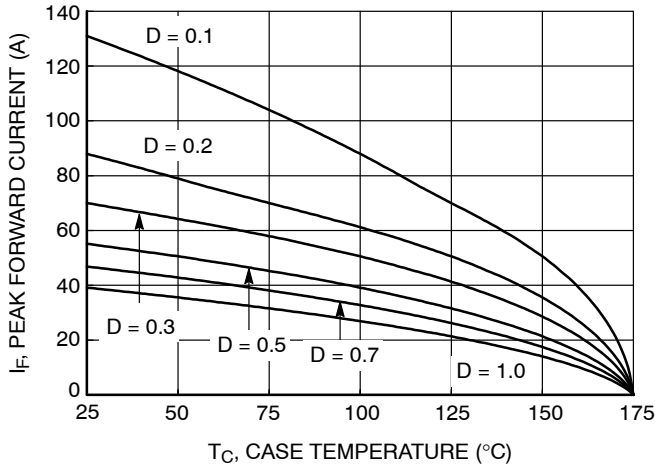


Figure 3. Current Derating

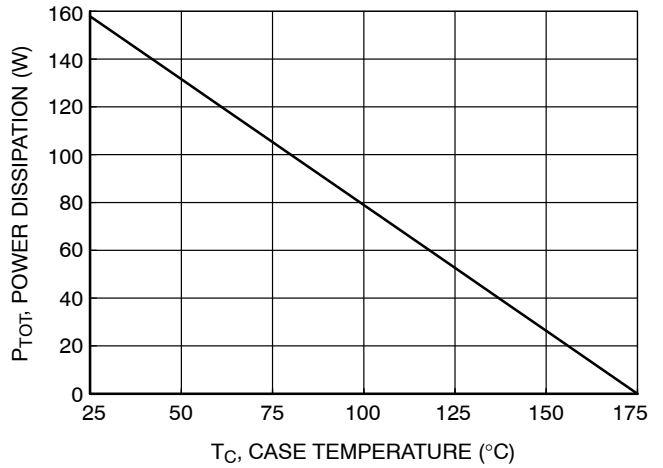


Figure 4. Power Derating

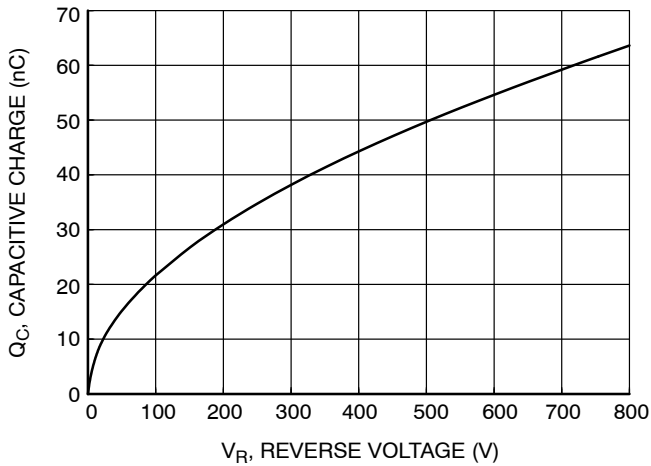


Figure 5. Capacitive Charge vs. Reverse Voltage

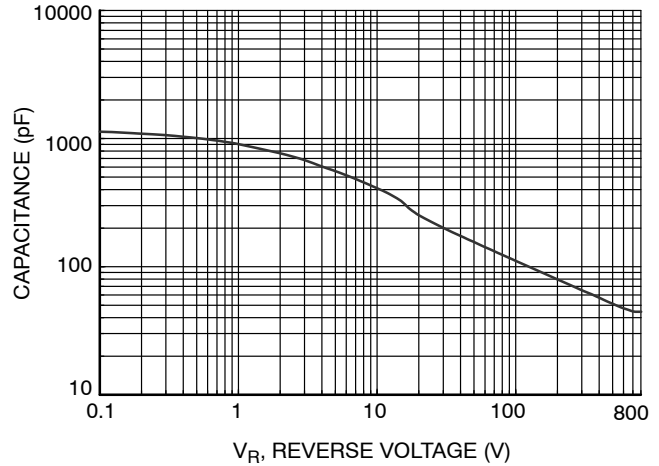


Figure 6. Capacitive vs. Reverse Voltage

NDSH30120CDN

TYPICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)

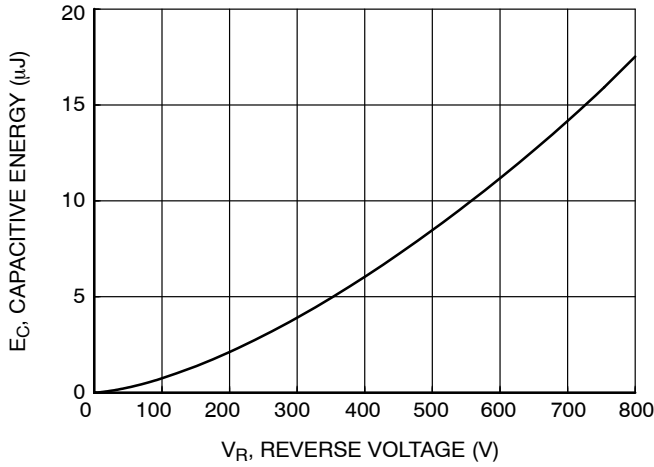


Figure 7. Capacitance Stored Energy

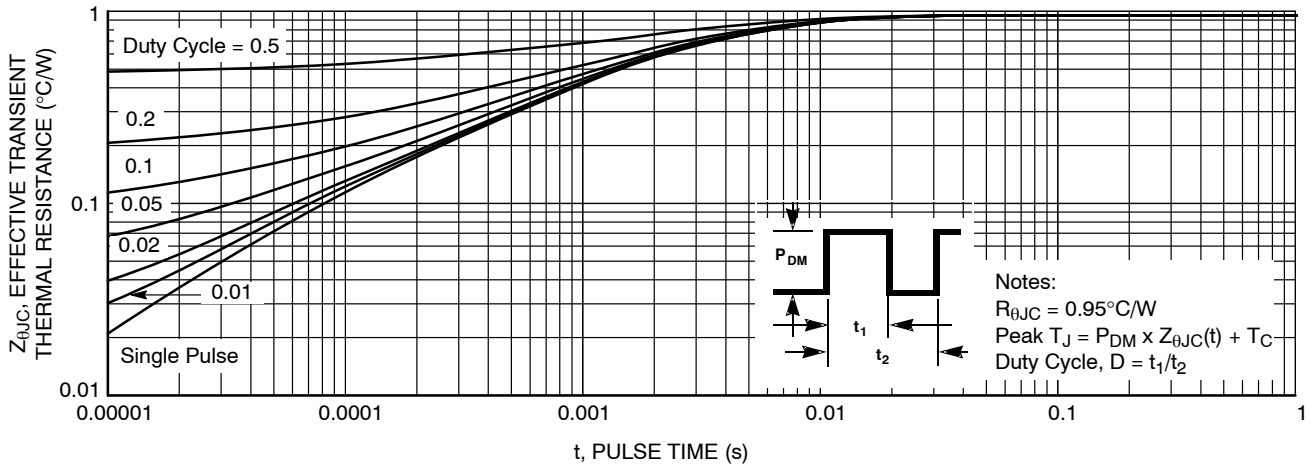


Figure 8. Junction-to-Case Transient Thermal Response Curve

MECHANICAL CASE OUTLINE

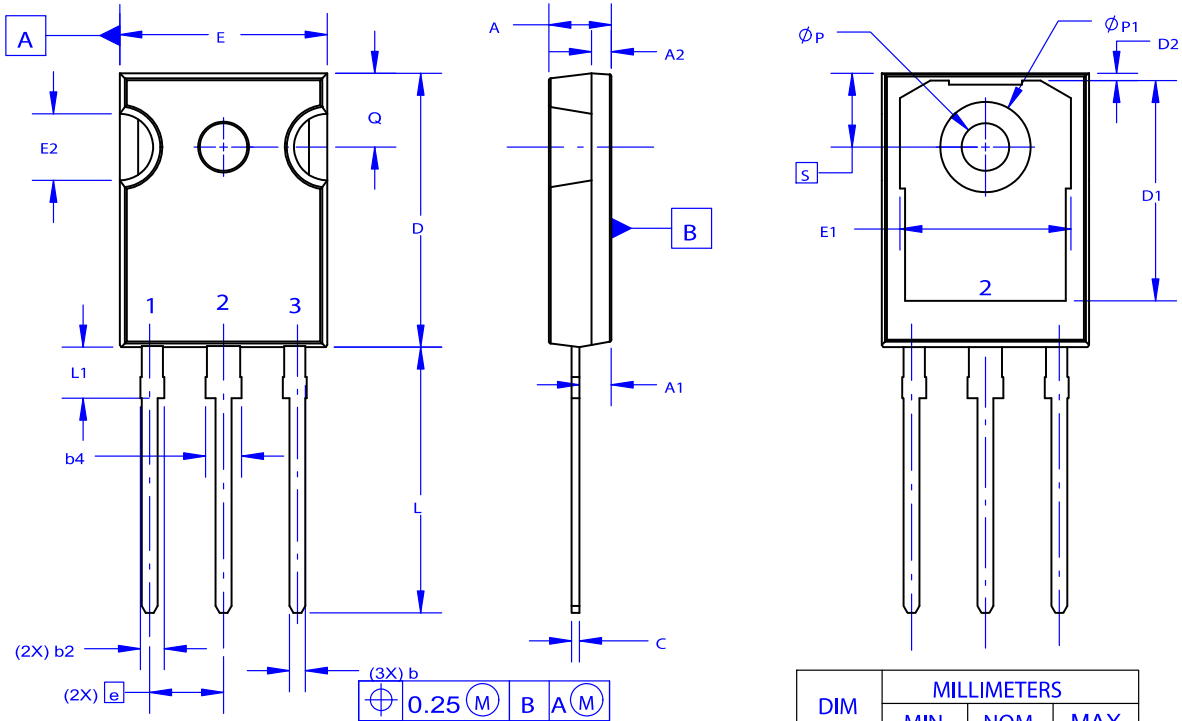
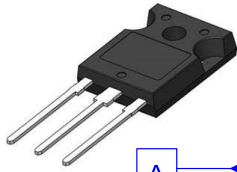
PACKAGE DIMENSIONS

ON Semiconductor®



TO-247-3LD
CASE 340CX
ISSUE A

DATE 06 JUL 2020



NOTES: UNLESS OTHERWISE SPECIFIED.

- A. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.
- B. ALL DIMENSIONS ARE IN MILLIMETERS.
- C. DRAWING CONFORMS TO ASME Y14.5 - 2009.
- D. DIMENSION A1 TO BE MEASURED IN THE REGION DEFINED BY L1.
- E. LEAD FINISH IS UNCONTROLLED IN THE REGION DEFINED BY L1.

DIM	MILLIMETERS		
	MIN	NOM	MAX
A	4.58	4.70	4.82
A1	2.20	2.40	2.60
A2	1.40	1.50	1.60
D	20.32	20.57	20.82
E	15.37	15.62	15.87
E2	4.96	5.08	5.20
e	~	5.56	~
L	19.75	20.00	20.25
L1	3.69	3.81	3.93
ØP	3.51	3.58	3.65
Q	5.34	5.46	5.58
S	5.34	5.46	5.58
b	1.17	1.26	1.35
b2	1.53	1.65	1.77
b4	2.42	2.54	2.66
c	0.51	0.61	0.71
D1	13.08	~	~
D2	0.51	0.93	1.35
E1	12.81	~	~
ØP1	6.60	6.80	7.00

GENERIC MARKING DIAGRAM*



- XXXXX = Specific Device Code
- A = Assembly Location
- Y = Year
- WW = Work Week
- G = Pb-Free Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present. Some products may not follow the Generic Marking.

DOCUMENT NUMBER:	98AON93302G	Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.
DESCRIPTION:	TO-247-3LD	PAGE 1 OF 1

ON Semiconductor and are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. ON Semiconductor does not convey any license under its patent rights nor the rights of others.

onsemi, **Onsemi**, and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "**onsemi**" or its affiliates and/or subsidiaries in the United States and/or other countries. **onsemi** owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of **onsemi**'s product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. **onsemi** reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and **onsemi** makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does **onsemi** assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using **onsemi** products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by **onsemi**. "Typical" parameters which may be provided in **onsemi** data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. **onsemi** does not convey any license under any of its intellectual property rights nor the rights of others. **onsemi** products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use **onsemi** products for any such unintended or unauthorized application, Buyer shall indemnify and hold **onsemi** and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that **onsemi** was negligent regarding the design or manufacture of the part. **onsemi** is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

ADDITIONAL INFORMATION

TECHNICAL PUBLICATIONS:

Technical Library: www.onsemi.com/design/resources/technical-documentation
onsemi Website: www.onsemi.com

ONLINE SUPPORT: www.onsemi.com/support

For additional information, please contact your local Sales Representative at www.onsemi.com/support/sales