

650 V, 75 A trench field-stop IGBT with full rated silicon diode Rev. 1 — 17 January 2025 Product data sheet

1. General description

The NGW75T65M3DFP is a robust Insulated-Gate Bipolar Transistor (IGBT) featuring third-generation technology. It combines carrier stored trench-gate and field-stop (FS) structures. The NGW75T65M3DFP is rated to 175 °C with optimized IGBT turn-off losses, and has a short-circuit withstand time of 5 μ s. This hard-switching 650 V, 75 A IGBT is optimized for high-voltage, high-frequency industrial power inverter applications and servo motor drive applications.

2. Features

- Device current is rated at 75 A
- Low conduction and switching losses
- Stable and tight parameters for easy parallel operation
- Maximum junction temperature 175 °C
- Fully rated and soft fast reverse recovery diode
- 5 µs short circuit withstand time
- HV-H3TRB qualified

3. Applications

- Motor drives for industrial and consumer appliances
 - Servo motors operating between 5-20 kW (up to 20 kHz) for robotics, elevators, operating grippers, in-line manufacturing, etc.
- Power inverters
 - Uninterruptible Power Supply (UPS) inverter
 - EV charging converter
- Induction heating
- Welding

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Max	Unit
V _{CES}	collector-emitter voltage	T _{vj} = 25 °C	-	650	V
T _{vj}	operating junction temperature		-40	175	°C
t _{sc}	short circuit withstand time	V_{GE} = 15 V; V_{CC} = 400 V; T_{vj} ≤ 150 °C	-	5	μs

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5. Pinning information

Table 2.	Pinning info	rmation		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate	mb	
2	С	collector		С
3	E	emitter		
mb	С	mounting base; connected to collector		G

6. Ordering information

Table 3. Ordering information

Type number	Package					
	Name	Description	Version			
NGW75T65M3DFP	TO-247-3L	Plastic single-ended through-hole package; heatsink mounted; 1 mounting hole; 3-lead TO-247-3L	<u>SOT429-2</u>			

7. Limiting values

Table 4. Limiting values

Symbol	Parameter	Conditions	Min	Max	Unit
IGBT					
V _{CES}	collector-emitter voltage	T _{vj} = 25 °C	-	650	V
I _C	collector current [1] $T_c = 25 ^{\circ}C$	-	80	A
		T _c = 100 °C	-	80	А
I _{CRM}	repetitive peak collector current	2]	-	225	А
t _{sc}	short circuit withstand time	B] $V_{GE} = 15 \text{ V}; \text{V}_{CC} = 400 \text{V}; \text{T}_{vj} \le 150 ^{\circ}\text{C}$	-	5.0	μs
V _{GE}	gate-emitter voltage		-20	20	V
P _{tot}	total power dissipation	T _c = 25 °C	-	529	W
		T _c = 100 °C	-	265	W
T _{vj}	operating junction temperature		-40	175	°C
T _{stg}	storage temperature		-55	150	°C
T _{solder}	soldering temperature		-	260	°C
Diode		•			
l _F	diode forward current [1] T _c = 25 °C	-	80	А
		T _c = 100 °C	-	80	А
I _{FRM}	repetitive peak forward current	2]	-	225	А

Value is limited by bondwire and $T_{vj(max)}$. [1]

[2] [3]

Time duration is limited by $T_{vj(max)}$. Short circuit cycles \leq 1000, time between tests \geq 1 s.

8. Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
М	mounting torque, M3 screw		-	0.6	-	Nm
R _{th(j-c)}	thermal resistance from junction to case	IGBT	-	0.24	0.28	K/W
		diode	-	0.38	0.44	K/W
R _{th(j-a)}	thermal resistance from junction to ambier	it in free air	-	-	40	K/W

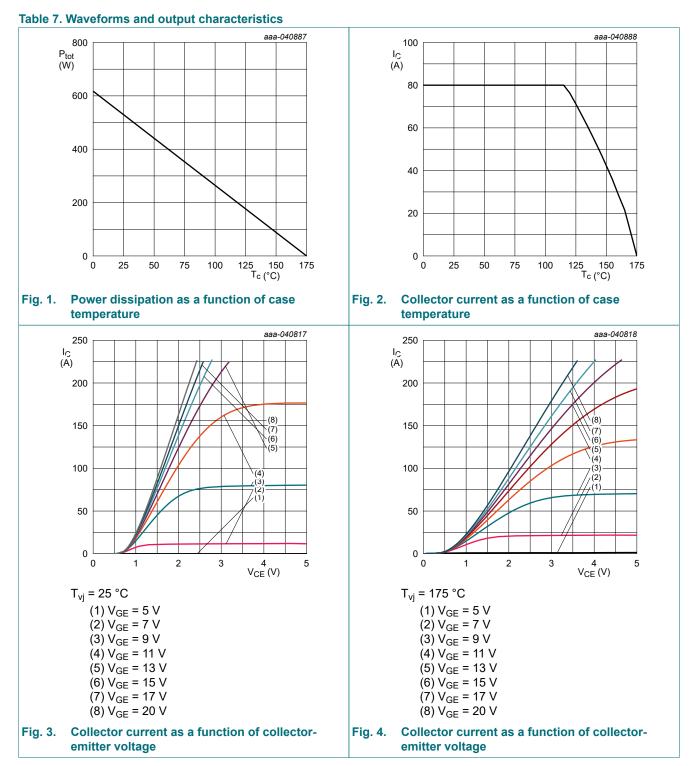
9. Electrical characteristics

Table 6. Characteristics

All values at T_{vj} = 25 °C, unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static ch	aracteristics	1				
V _{(BR)CES}	collector-emitter breakdown voltage	V _{GE} = 0 V; I _C = 0.2 mA	650	-	-	V
V _{CEsat}	collector-emitter saturation	V _{GE} = 15 V; I _C = 75 A; T _{vj} = 25 °C	-	1.49	1.8	V
	voltage	V _{GE} = 15 V; I _C = 75 A; T _{vj} = 175 °C	-	1.90	-	V
V _F	diode forward voltage	V _{GE} = 0 V; I _F = 75 A; T _{vj} = 25 °C	-	1.59	1.9	V
		V _{GE} = 0 V; I _F = 75 A; T _{vj} = 175 °C	-	1.38	-	V
V _{GE(th)}	gate-emitter threshold voltage	I _C = 0.75 mA; V _{CE} = V _{GE} ; T _{vj} = 25 °C	4.3	5.0	5.7	V
I _{CES}	zero gate voltage collector	V _{CE} = 650 V; V _{GE} = 0 V; T _{vj} = 25 °C	-	13	-	nA
	current	V _{CE} = 650 V; V _{GE} = 0 V; T _{vj} = 175 °C	-	0.8	-	mA
I _{GES}	gate-emitter leakage current	V _{CE} = 0 V; V _{GE} = 20 V	-	-	100	nA
g _{fs}	transconductance	V _{CE} = 20 V; I _C = 75 A; T _{vj} = 25 °C	-	39.0	-	S
r _g	internal gate resistor		-	0.5	-	Ω
Dynamic	characteristics					
C _{ies}	input capacitance	V _{CE} = 25 V; V _{GE} = 0 V; f = 1 MHz	-	6456	-	pF
C _{oes}	output capacitance		-	250	-	pF
C _{res}	reverse transfer capacitance		-	46	-	pF
Q _G	gate charge	V _{CC} = 520 V; V _{GE} = 15 V; I _C = 75 A	-	262	-	nC
L _{sCE}	internal stray inductance	measured 5 mm from case	-	7.9	-	nH
I _{C(sc)}	short circuit collector current	V _{GE} = 15 V; V _{CC} = 400 V; t _{sc} ≤ 5 μs; T _{vj} ≤ 150 °C	-	384	-	A

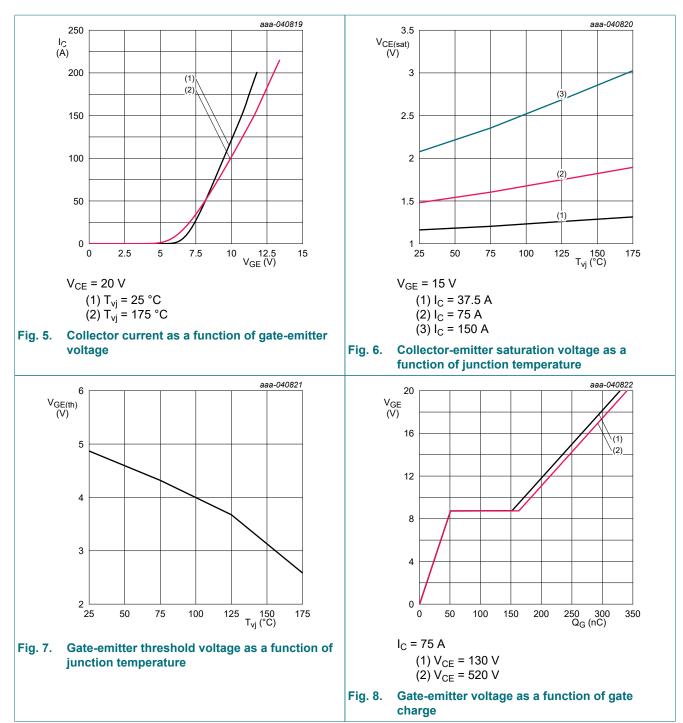
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
IGBT sw	vitching characteristics, indu	ctive load			1	I	
t _{d(on)}	turn-on delay time	V _{GE} = 15/0 V; V _{CC} = 400 V;	T _{vj} = 25 °C	-	44	-	ns
		I_{C} = 75 A; R _{G(on)} = 10 Ω; R _{G(off)} = 10 Ω;	T _{vj} = 175 °C	-	41	-	ns
t _r	rise time	see Fig. 27 and Fig. 28	T _{vj} = 25 °C	-	38	-	ns
		_	T _{vj} = 175 °C	-	35	-	ns
t _{d(off)}	turn-off delay time		T _{vj} = 25 °C	-	305	-	ns
			T _{vj} = 175 °C	-	362	-	ns
t _f	fall time		T _{vj} = 25 °C	-	16	-	ns
			T _{vj} = 175 °C	-	62	-	ns
Eon			T _{vj} = 25 °C	-	2.79	-	mJ
	loss		T _{vj} = 175 °C	-	5.34	-	mJ
E _{off}	E _{off} turn-off switching energy loss		T _{vj} = 25 °C	-	1.39	-	mJ
			T _{vj} = 175 °C	-	2.16	-	mJ
E _{ts}	total switching energy loss		T _{vj} = 25 °C	-	4.18	-	mJ
			T _{vj} = 175 °C	-	7.51	-	mJ
Diode sv	witching characteristics, indu	ictive load					
t _{rr}	reverse recovery time	V _R = 400 V; I _F = 75 A;	T _{vj} = 25 °C	-	124	-	ns
		di _F /dt = 500 A/µs; see <u>Fig. 26</u>	T _{vj} = 175 °C	-	238	-	ns
Q _{rr}	reverse recovery charge	300 <u>r ig. 20</u>	T _{vj} = 25 °C	-	1166	-	nC
			T _{vj} = 175 °C	-	5689	-	nC
l _{rrm}	peak reverse recovery		T _{vj} = 25 °C	-	22	-	Α
	current		T _{vj} = 175 °C	-	44	-	Α
E _{rec}	reverse recovery energy loss		T _{vj} = 25 °C	-	0.10	-	mJ
			T _{vj} = 175 °C	-	0.66	-	mJ
di _{rrf} /dt	fall rate of reverse recovery		T _{vj} = 25 °C	-	248	-	A/µs
	current		T _{vi} = 175 °C	-	320	-	A/µs

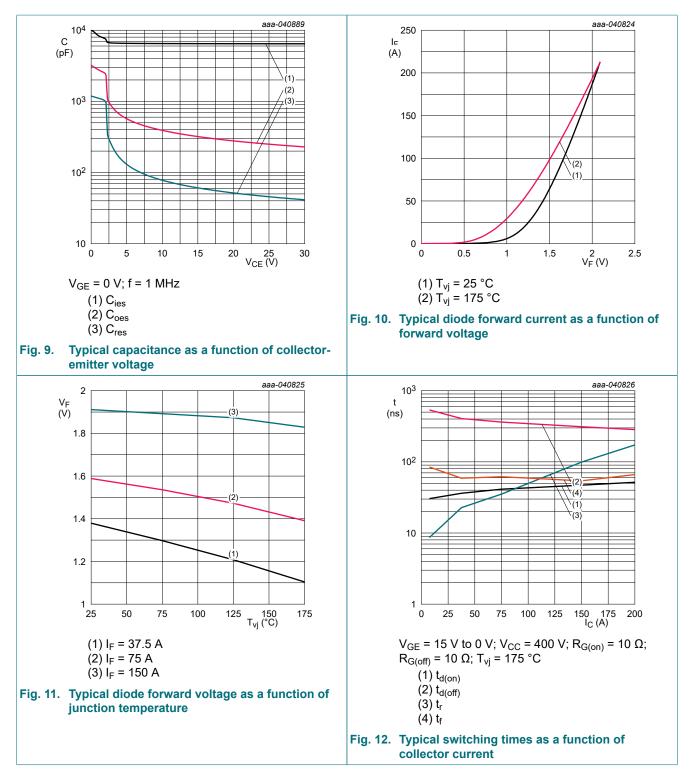


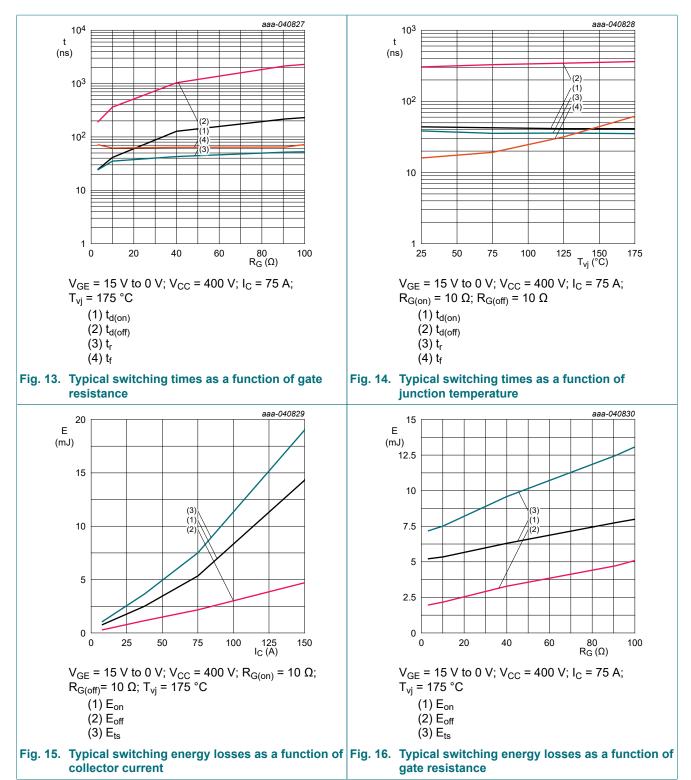
9.1. Characteristic diagrams

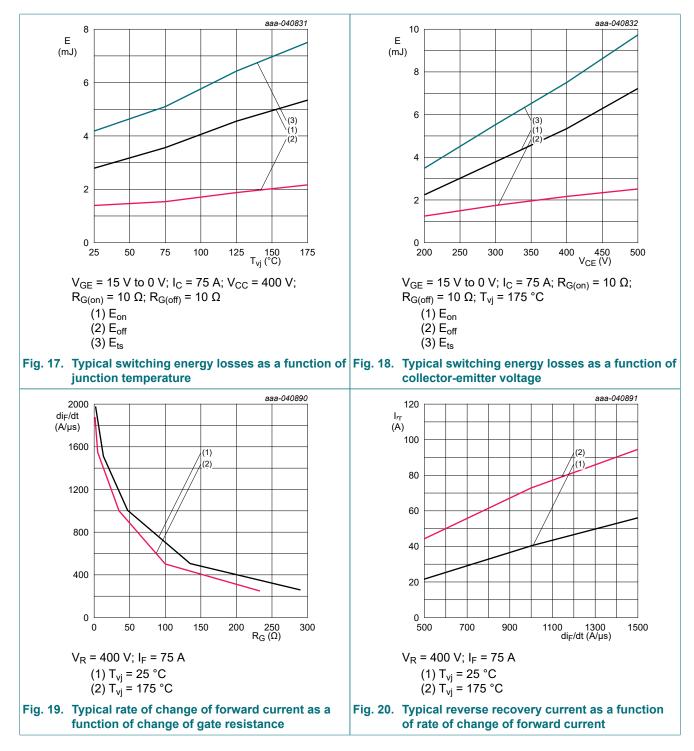
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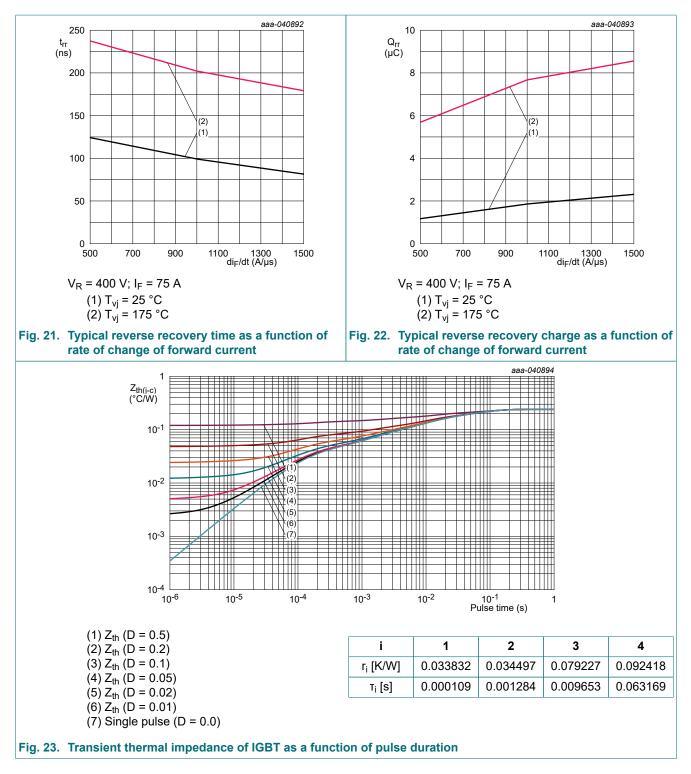
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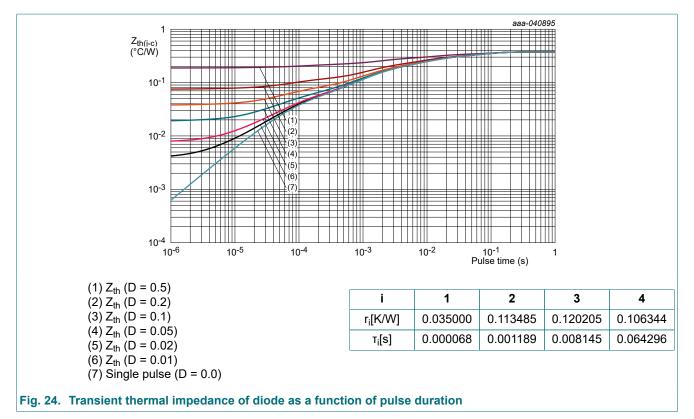




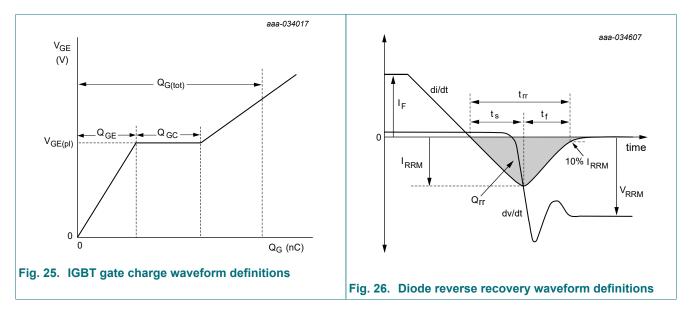




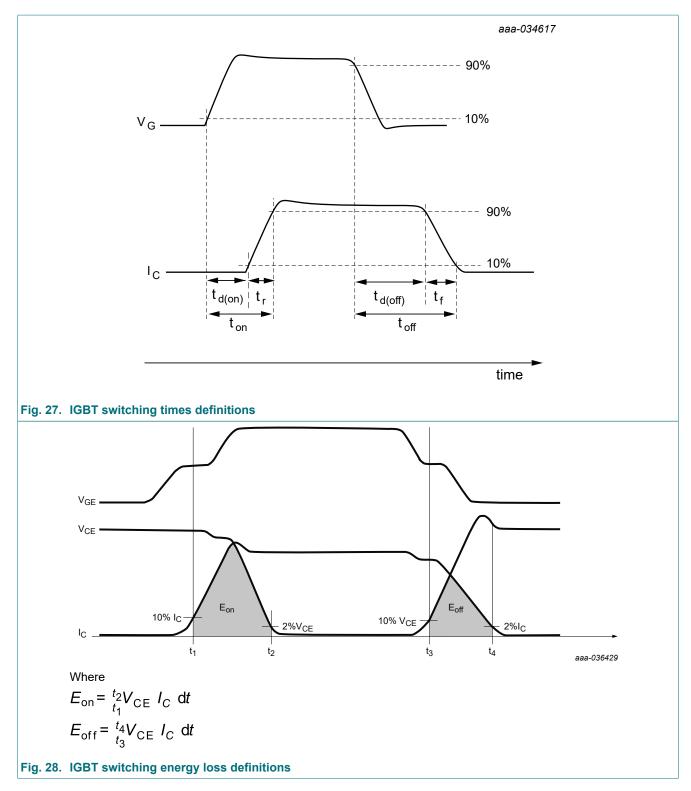
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9.2. Waveform definitions

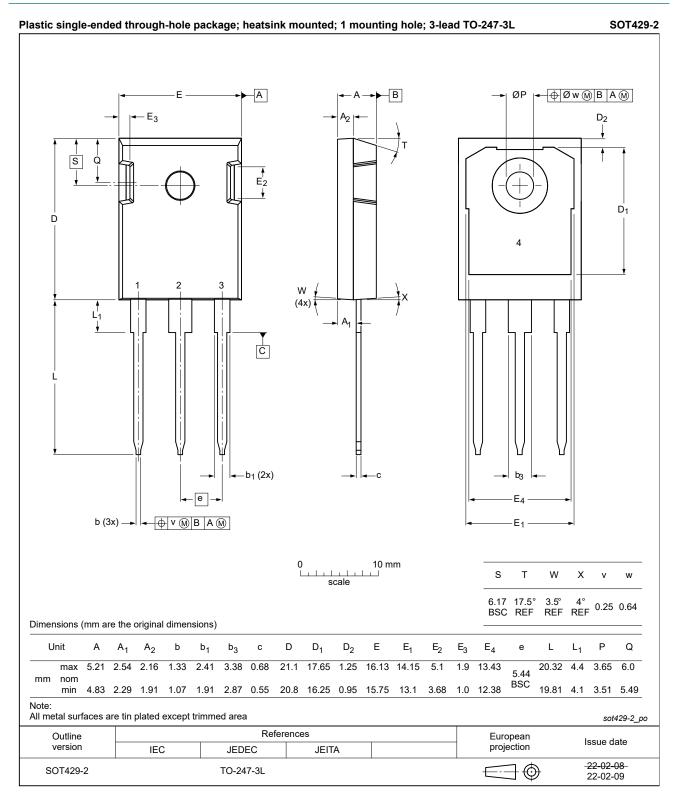


650 V, 75 A trench field-stop IGBT with full rated silicon diode



NGW75T65M3DFP

10. Package outline





11. Revision history

Table 8. Revision history				
Document ID	Release date	Data sheet status	Change notice	Supersedes
NGW75T65M3DFP v. 1	20250117	Product data sheet	-	-

NGW75T65M3DFP

12. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
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