

7.5A High Power DC/DC Converter Controller IC

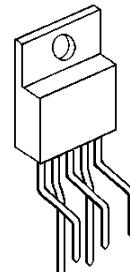
■GENERAL DESCRIPTION

The **NJM2811** is a high power step down DC/DC converter IC.

It incorporates 7.5A power transistor, $\pm 2\%$ accuracy precision voltage reference, fixed frequency PWM controller with cycle-by-cycle current limit and low power consumption stand by function.

The **NJM2811** realizes a high power step down application with minimal external components.

■PACKAGE OUTLINE



PIN FUNCTION

- 1: V_{FB}
- 2: SW_{OUT}
- 3:GND
- 4: V^+
- 5:STBY (V_{COMP})

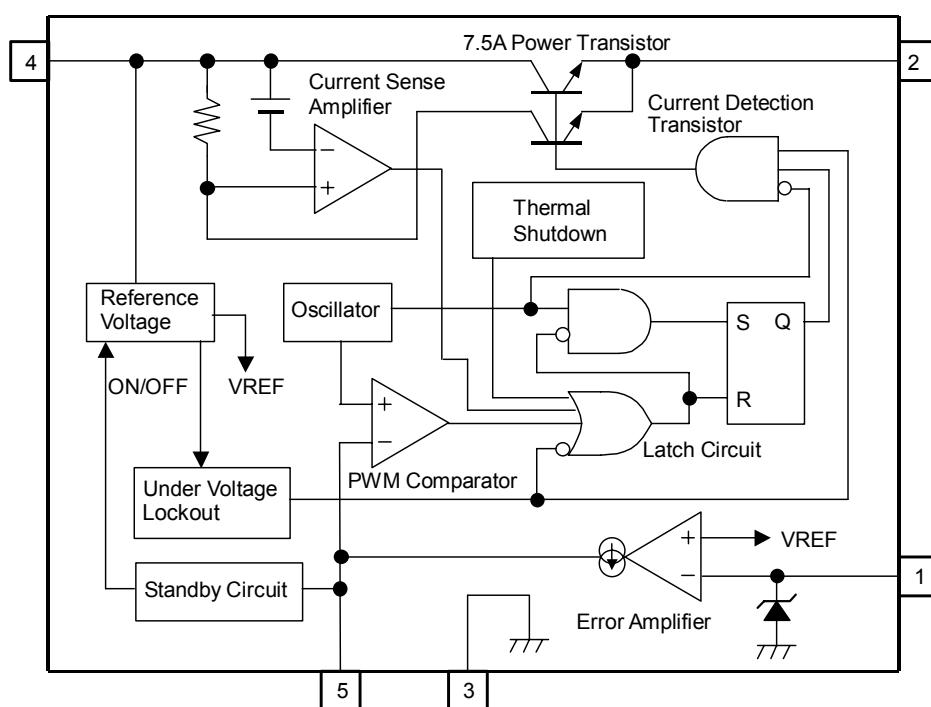
NJM2811TLA2050

Pin 1 locates on the left side of the package drawing (top front view).

■FEATURES

- Adjustable output voltage more than 5V
- Operating Voltage 7.5V to 40V
- PWM form Switching Power Supply Control
- Internal High Power Transistor 7.5A (min.)
- Fixed Frequency Oscillator 72kHz (typ.)
- Current Sense Amplifier
- Under Voltage Lockout
- Thermal Shutdown Circuit
- Bipolar Technology
- Package Outline TO-220(5PIN)

■BLOCK DIAGRAM



NJM2811

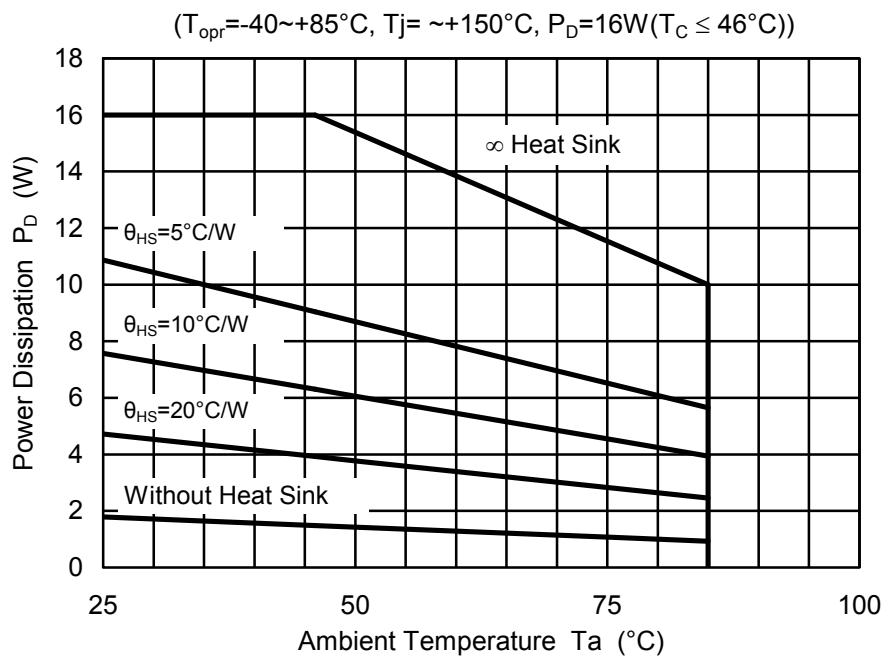
■ABSOLUTE MAXIMUM RATINGS ($T_a=25^\circ\text{C}$)

PARAMETER	SYMBOL	MAXIMUM RATINGS	UNIT
Maximum Supply Voltage	V^+	40	V
Switch Output Voltage	V_o (SWITCH)	-0.5 ~ + V_{in}	V
Voltage Feedback and Compensation Input Voltage Range	V_{FB}, V_{COMP}	-0.3 ~ +7.0	V
Power Dissipation	P_D	TO-220 (5PIN) 16($T_C \leq 46^\circ\text{C}$)	W
Operating Junction Temperature	T_j	-40 ~ +150	$^\circ\text{C}$
Operating Temperature Range	T_{opr}	-40 ~ +85	$^\circ\text{C}$
Storage Temperature Range	T_{stg}	-50 ~ +150	$^\circ\text{C}$

■THERMAL CHARACTERISTICS

Thermal Resistance	Junction-to-Ambient Temperature	θ_{ja}	70	$^\circ\text{C}/\text{W}$
	Junction-to-Case	θ_{jc}	6.5	

■POWER DISSIPATION vs. AMBIENT TEMPERATURE



■ELECTRICAL CHARACTERISTICS ($V^+ = 12V$, $T_a = 25^\circ C$)

Oscillator Block

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Frequency	fosc	$V^+ = 7.5V$	65	72	79	kHz

Error Amplifier Block

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Voltage Feedback Input Threshold	$V_{FB}(th)$		4.9	5.0	5.1	V
Line Regulation	REG-Line	$V^+ = 7.5 \sim 40V$	—	0.03	0.08	%/V
Input Bias Current	I_B	$V_{FB} = V_{FB}(th) + 0.15V$	—	0.15	1.0	μA
Ripple Rejection	PSRR	$V^+ = 10 \sim 20V$	—	80	—	dB
Output Voltage Swing	V_{OH} V_{OL}	$I_{source} = 75\mu A, V_{FB} = 4.7V$ $I_{sink} = 0.4mA, V_{FB} = 5.3V$	4.2 —	4.9 1.6	— 1.9	V V

PWM comparator Block

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Duty Cycle Maximum Minimum	$DC_{(MAX)}$ $DC_{(MIN)}$	$V_{FB} = 0V$ $V_{FB} = 5.3V$	— 0	95 0	— 0	% %

Switch Output Block

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Output Voltage Saturation	V_{SAT}	$V^+ = 15V, I_{source} = 7.5A$	—	$V^+ - 2.2$	$V^+ - 2.5$	V
OFF-State Leakage	$I_{SW(off)}$	$V^+ = 40V, SW_{OUT} = 0V$	—	0	100	μA
Current Limit Threshold	$I_{pk(SWITCH)}$	$V^+ = 15V$	7.5	8.5	10	A
Switching Times						
Output Voltage Rise Time	tr	$V^+ = 40V, R_{OUT} = 7.7\Omega, V_{FB} = 0V$	—	100	—	ns
Output Voltage Fall Time	tf	$V^+ = 40V, R_{OUT} = 7.7\Omega, V_{FB} = 0V$	—	50	—	ns

Under Voltage Lockout Block

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Startup Threshold	$V_{TH(UVLO)}$	V^+ Increasing	6.4	6.8	7.2	V
Hysteresis	$V_{H(UVLO)}$	V^+ Decreasing	0.3	0.5	0.7	V

Total Device

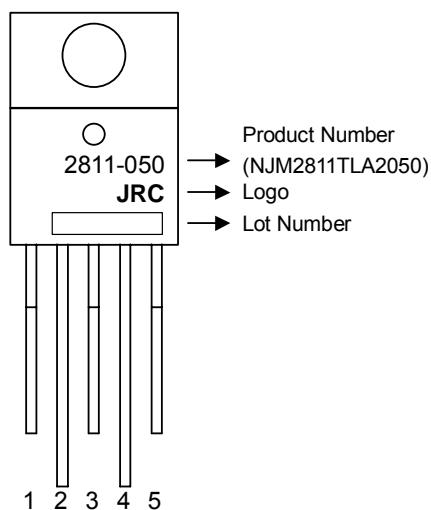
PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Standby-State Power Supply Current	$I_{CC(stby)}$	$STBY \leq 0.1V$	—	36	100	μA
Operating-State Power Supply Current	Icc	$V^+ = 40V, V_{FB} = 0V$ duty-cycle=MAX	—	40	53	mA

Keep the limit of maximum power dissipation not to operate thermal shutdown.

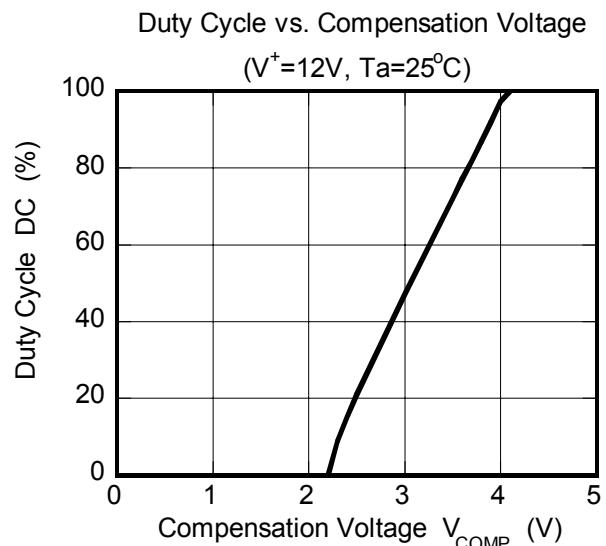
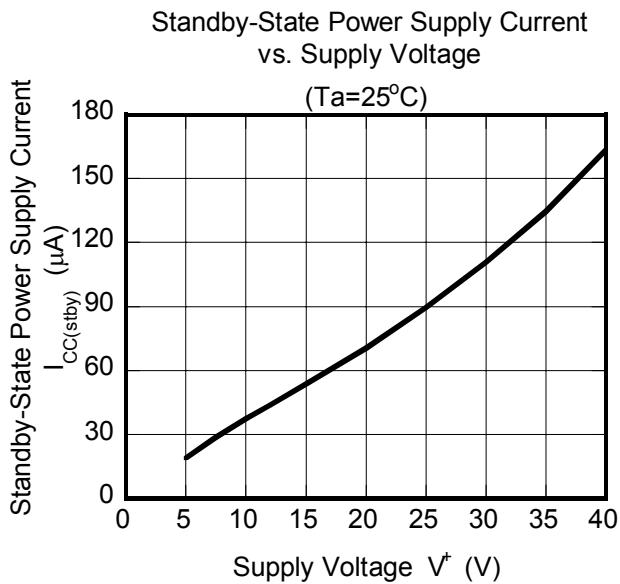
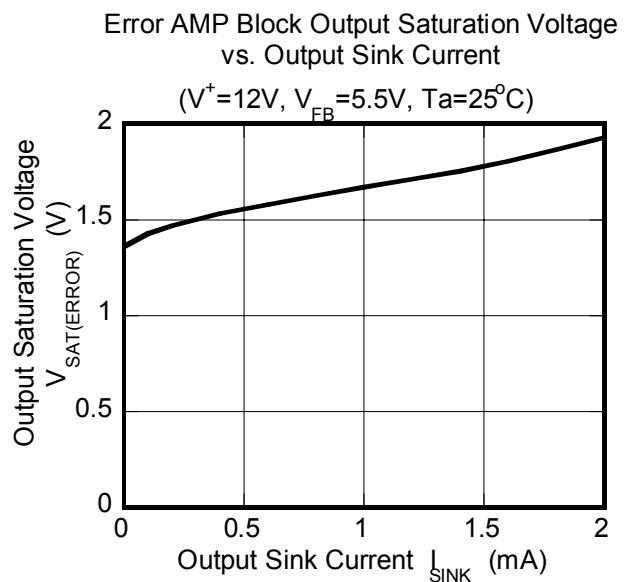
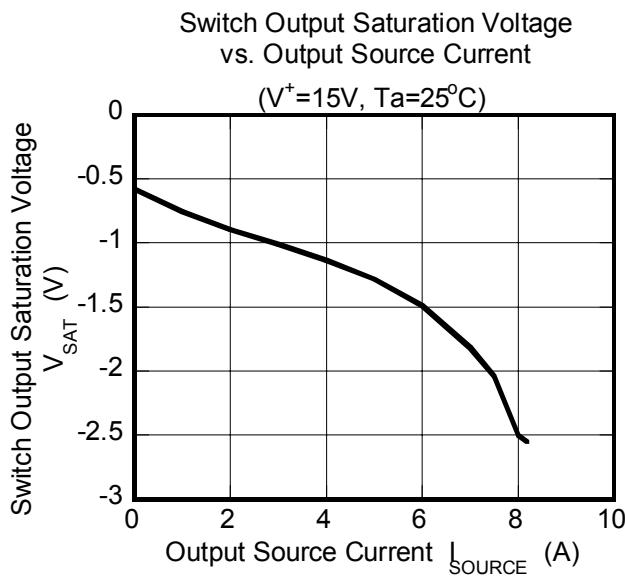
Low duty cycle pulse test is used to close its junction temperature to ambient temperature.

NJM2811

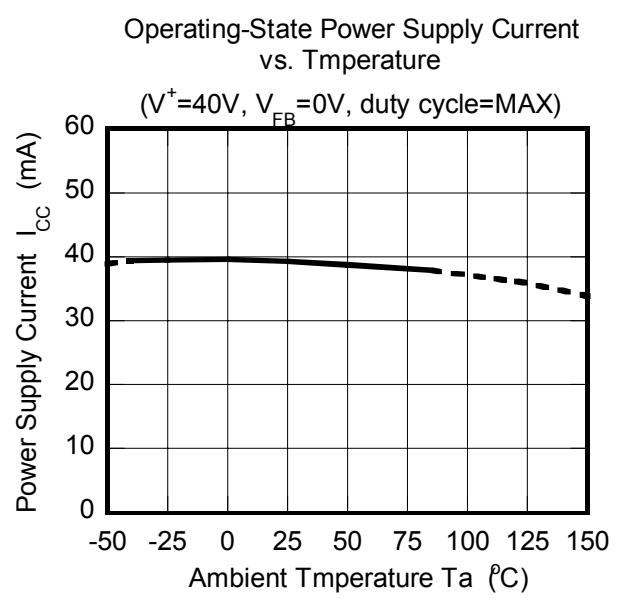
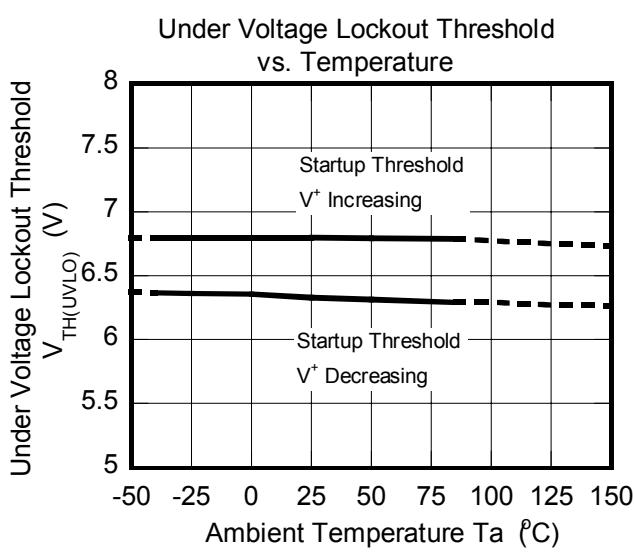
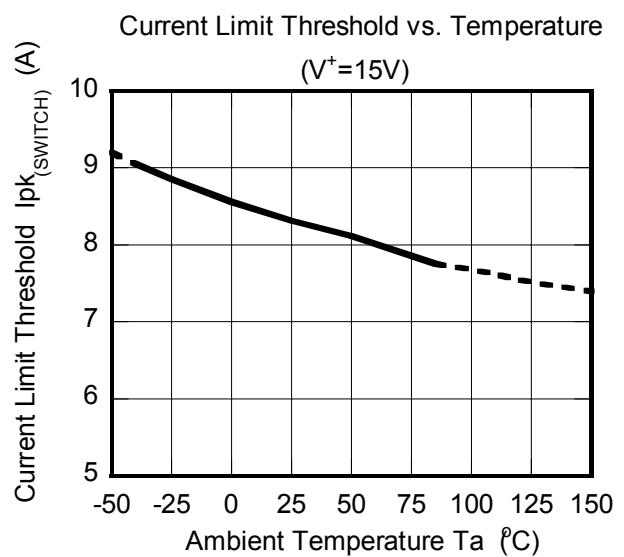
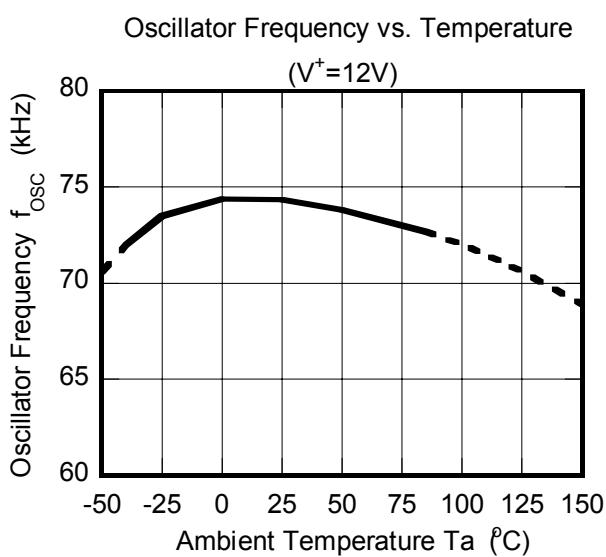
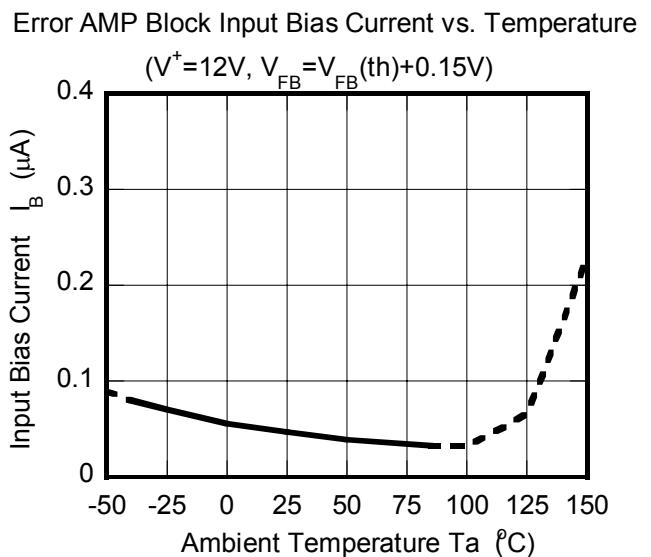
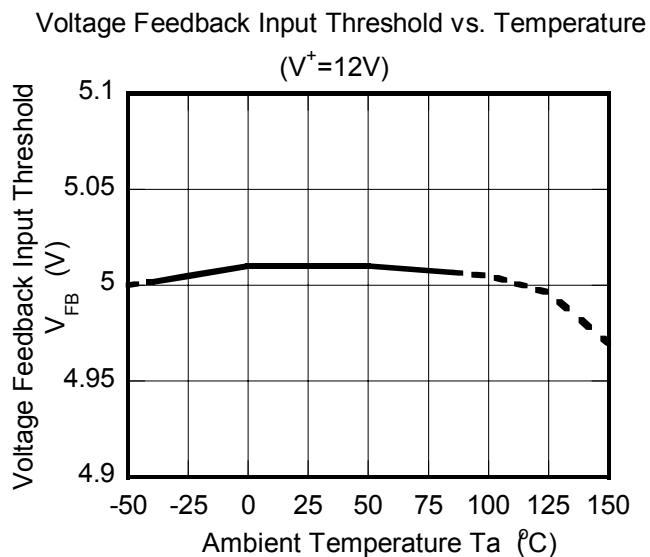
■ MARKING



■ TYPICAL CHARACTERISTICS



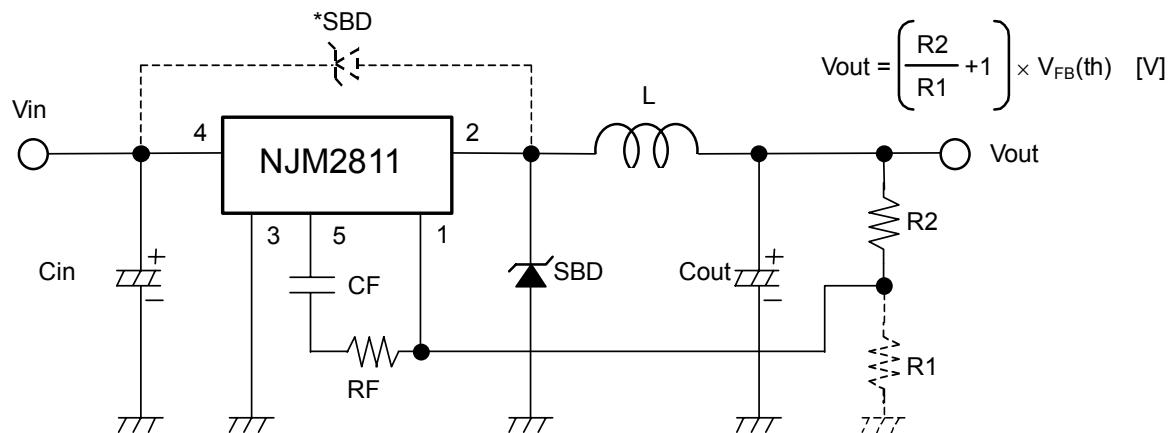
TYPICAL CHARACTERISTICS



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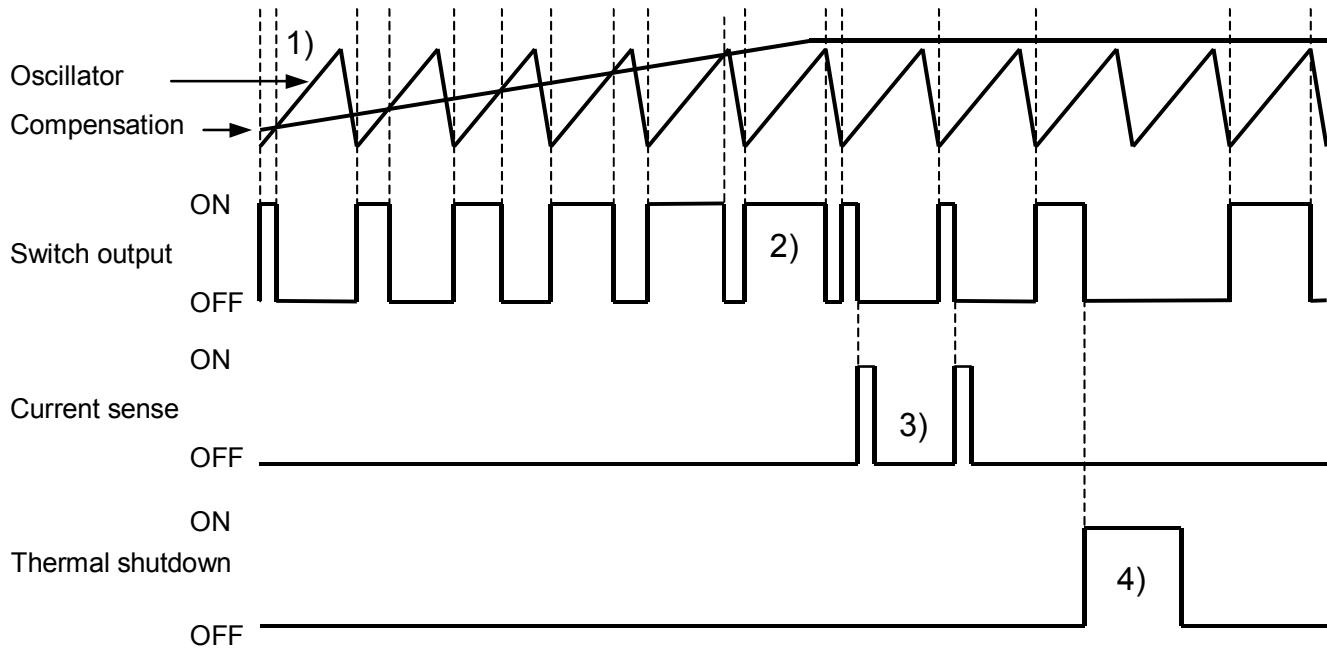
■ TYPICAL APPLICATIONS

Step-Down Converter



- 1) 5V and higher converter, the application must be connected R1 resistor according to above figure.
- 2) High current converter, the application must be placed C_{in} capacitor next to NJM2811, which avoid the power-line fluctuation.
- 3) The sharp fluctuation of output load cause reverse voltage for inductance and over the supply-voltage for SW_{out} terminal. To avoid this problem, the application must be placed SBD between terminal 2 and 4.

■ TIMING CHART



- 1) The NJM2811 generate square waves. The PWM comparator generate PWM signals to compare square waves and compensation voltage.
- 2) The switching duty is maximum 95%(typ.).
- 3) Over the 8.5A(typ.) current, the output switch will be OFF to operate current limit protection. The NJM2811 sense the switching current of power transistor.
- 4) Over the $T_j=180^\circ\text{C}$ (typ.), the switching will be OFF to operate thermal shutdown circuit.

MEMO

[CAUTION]
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