

## FRED Ultrafast Soft Recovery Diode Module, 200 A

### FEATURES

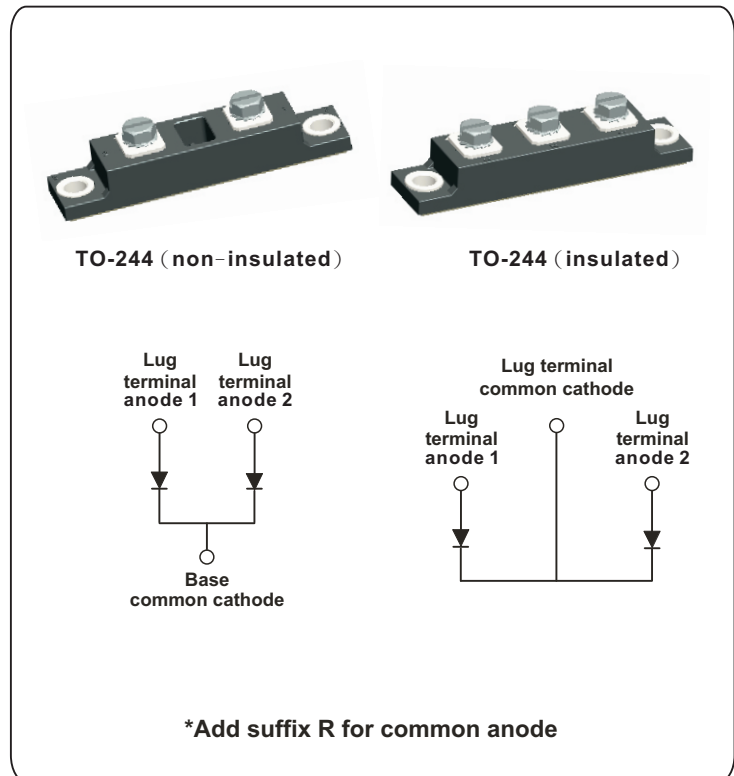
- Very low  $Q_{rr}$  and  $t_{rr}$
- Lead (Pb)-free
- Designed and qualified for industrial level
- Reduced RFI and EMI
- Reduced snubbing

### DESCRIPTION

FRED diodes are optimized to reduce losses and EMI/RFI in high frequency power conditioning systems. An extensive characterization of the recovery behavior for different values of current, temperature and  $di/dt$  simplifies the calculations of losses in the operating conditions. The softness of the recovery eliminates the need for a snubber in most applications.

### TYPICAL APPLICATIONS

- Power converters
- Motor drives
- Welders
- Switching power supplies



PRODUCT SUMMARY	
$I_{F(AV)}$	200A
$V_R$	400V
$I_{F(DC)}$ at $T_C$	160A at 100 °C

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNIT	
Cathode to anode voltage	$V_R$		400	V	
Average forward current	$I_{F(AV)}$	$T_C = 25^\circ\text{C}$ , per leg	315	A	
		$T_C = 120^\circ\text{C}$	per device		200
			per leg		100
DC forward current	$I_{F(DC)}$	$T_C = 100^\circ\text{C}$	160		
Single pulse forward current	$I_{FSM}$	Limited by junction temperature, per leg	1400		
Non-repetitive avalanche energy	$E_{AS}$	$L = 100 \mu\text{H}$ , duty cycle limited by maximum $T_J$	1.4	mJ	
Maximum power dissipation	$P_D$	$T_C = 25^\circ\text{C}$	394	W	
		$T_C = 100^\circ\text{C}$	192		
Operating junction and storage temperature range	$T_J, T_{Stg}$		- 55 to 150	$^\circ\text{C}$	

ELECTRICAL SPECIFICATIONS (T <sub>J</sub> = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Cathode to anode breakdown voltage	V <sub>BR</sub>	I <sub>R</sub> = 100 μA	400	-	-	V
Maximum forward voltage	V <sub>FM</sub>	I <sub>F</sub> = 100 A	-	1.05	1.25	
		I <sub>F</sub> = 200 A	-	1.25	1.45	
		I <sub>F</sub> = 100 A, T <sub>J</sub> = 125 °C	-	0.90	1.10	
Maximum reverse leakage current per leg	I <sub>RM</sub>	T <sub>J</sub> = 125 °C, V <sub>R</sub> = 400V	-	0.50	4.0	mA
		T <sub>J</sub> = 25 °C, V <sub>R</sub> = 400V	-	2.0	10	μA
Junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 200V	-	280	380	pF
Series inductance	L <sub>S</sub>	From top of terminal hole to mounting plane	-	6.0	-	nH
Maximum RMS insulation voltage (for insulated type)	V <sub>INS</sub>	50Hz	-	-	3000(1min)	V
					3600(1s)	

DYNAMIC RECOVERY CHARACTERISTICS PER LEG (T <sub>J</sub> = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Reverse recovery time	t <sub>rr</sub>	I <sub>F</sub> = 0.5A, I <sub>R</sub> = 1.0A, I <sub>RR</sub> = 0.25A	-	60	70	ns
		I <sub>F</sub> = 1.0A, dI <sub>F</sub> /dt=200A/μs, V <sub>R</sub> = 30V	-	50	-	
		T <sub>J</sub> = 25 °C	-	77	120	
		T <sub>J</sub> = 125 °C	-	290	440	
Peak recovery current	I <sub>RRM</sub>	T <sub>J</sub> = 25 °C	-	7.5	14	A
		T <sub>J</sub> = 125 °C	-	16	30	
Reverse recovery charge	Q <sub>rr</sub>	T <sub>J</sub> = 25 °C	-	290	780	nC
		T <sub>J</sub> = 125 °C	-	2300	3600	
Peak rate of recovery current	dI <sub>(rec)</sub> /dt	T <sub>J</sub> = 25 °C	-	320	-	A/μs
		T <sub>J</sub> = 125 °C	-	270	-	

THERMAL AND MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	MIN.	TYP.	MAX.	UNIT
Maximum junction and storage temperature range		T <sub>J</sub> , T <sub>stg</sub>	-55	-	150	°C
Thermal resistance, junction to case per leg	TO-244 (non-insulated)	R <sub>thJC</sub>	-	-	0.19	°C/W
	TO-244 (insulated)		-	-	0.28	
Thermal resistance, junction to case per module	TO-244 (non-insulated)	R <sub>thJC</sub>	-	-	0.095	
	TO-244 (insulated)		-	-	0.14	
Typical thermal resistance, case to heatsink		R <sub>thCS</sub>	-	0.10	-	
Weight	TO-244 (non-insulated)		-	80 (2.82)	-	g (oz.)
	TO-244 (insulated)		-	95 (3.36)	-	
Mounting torque <sup>(1)</sup>			30 (3.4)	-	40 (4.6)	lbf · in (N · m)
Mounting torque center hole			12 (1.4)	-	18 (2.1)	
Terminal torque			30 (3.4)	-	40 (4.6)	
Vertical pull			-	-	80	lbf · in
2" lever pull			-	-	35	

**Note**

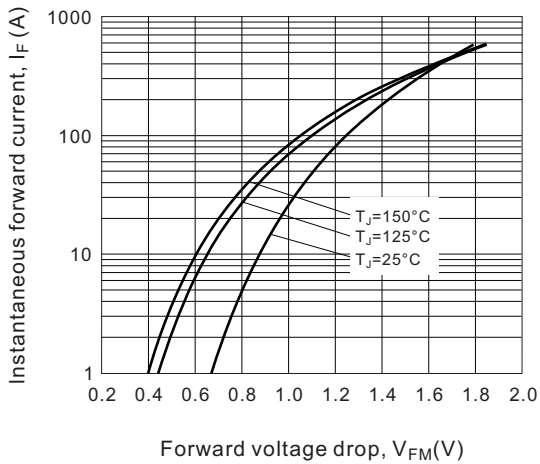
(1) Mounting surface must be smooth, flat, free of burrs or other protrusions. Apply a thin even film of thermal grease to mounting surface. Gradually tighten each mounting bolt in 5 to 10 lbf. in steps until desired or maximum torque limits are reached

## Ordering Information Tabel

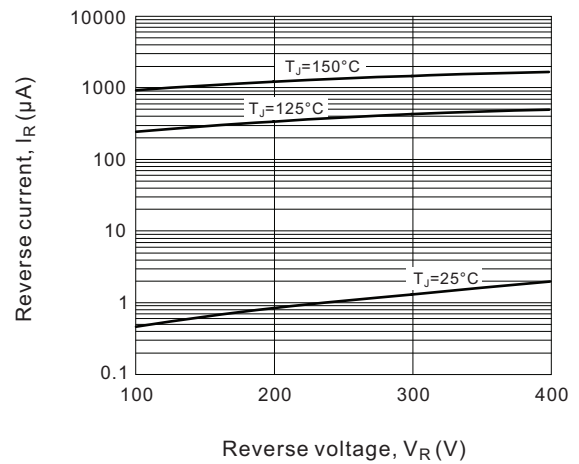
Device code	<b>NK</b>	<b>F</b>	<b>D</b>	<b>200</b>	<b>—</b>	<b>40</b>	<b>R</b>	<b>I</b>
	①	②	③	④		⑤	⑥	⑦

- ① - Nell's power module
- ② - F for Ultrafast soft recovery diode
- ③ - D for Dual Diodes, TO-244 Package
- ④ - Maximum average forward current, A
- ⑤ - Voltage rating (40 = 400V)
- ⑥ - None for common cathode configuration  
"R" for common anode configuration
- ⑦ - None for non-insulated type  
"I" for insulated type

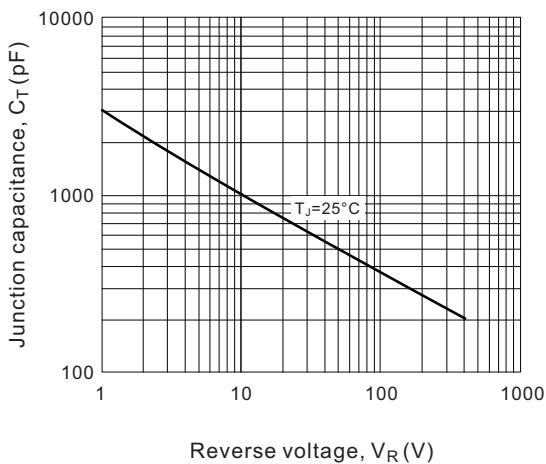
**Fig.1 Maximum forward voltage drop vs. Instantaneous forward current (per leg)**



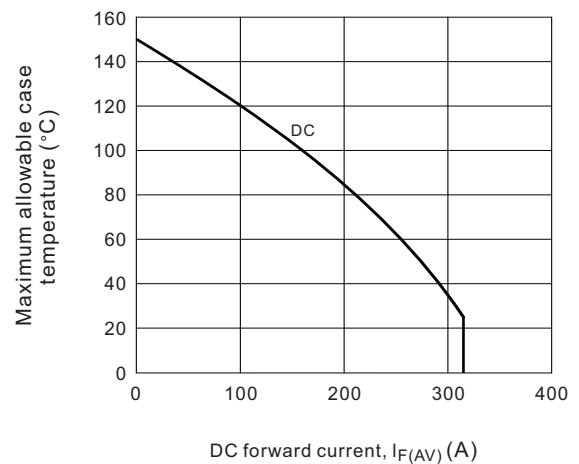
**Fig.2 Typical reverse current vs. reverse voltage (per leg)**



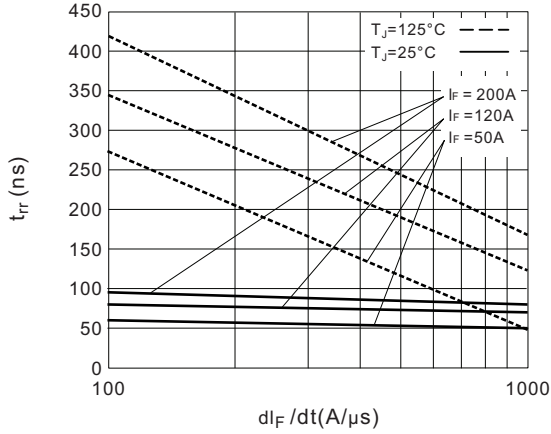
**Fig.3 Typical junction capacitance vs. reverse voltage (per leg)**



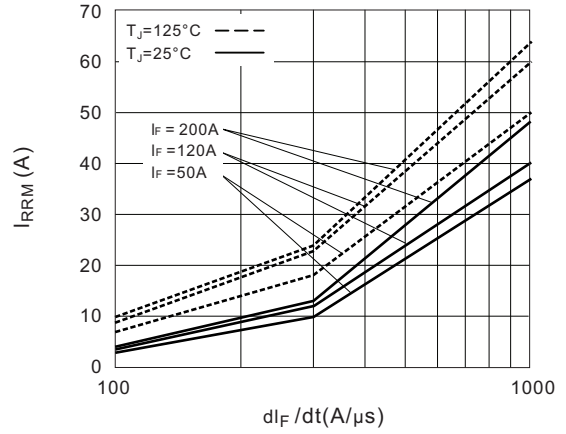
**Fig.4 Maximum allowable case temperature vs. DC forward current (per leg)**



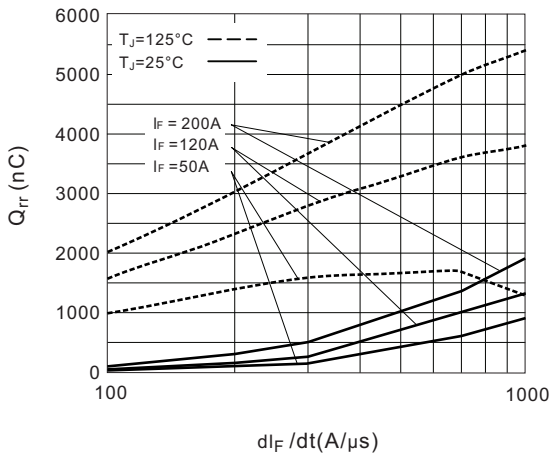
**Fig.5 Typical reverse recovery time vs.  $di_F/dt$  (per leg)**



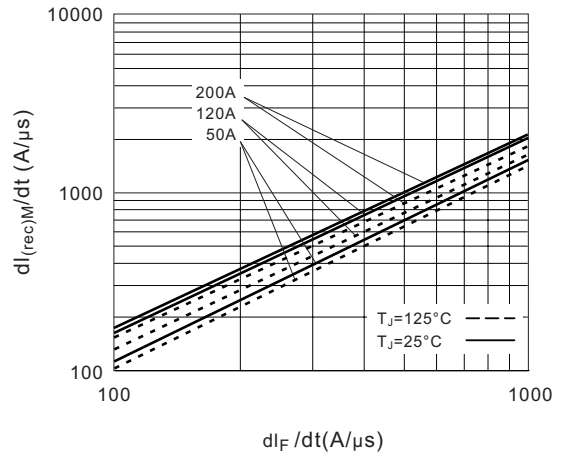
**Fig.6 Typical recovery current vs.  $di_F/dt$  (per leg)**



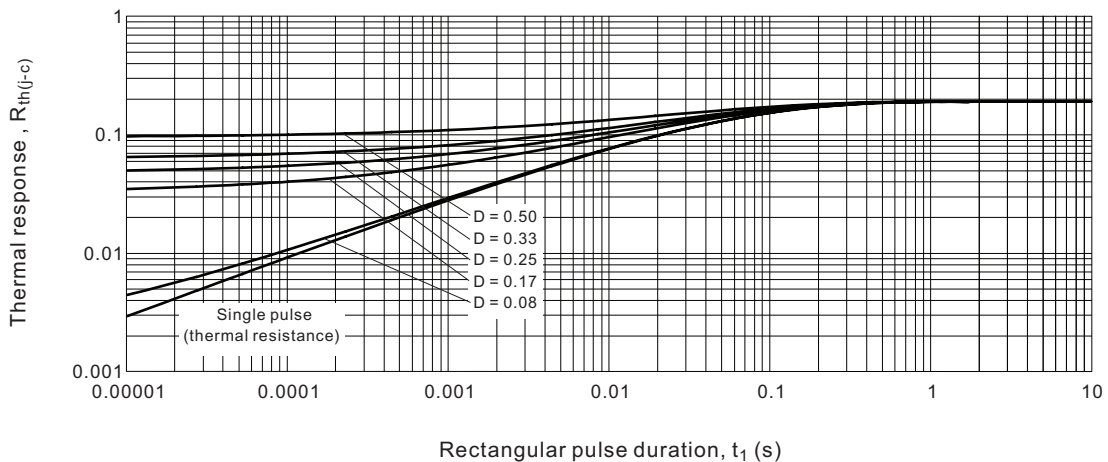
**Fig.7 Typical stored charge vs.  $di_F/dt$  (per leg)**



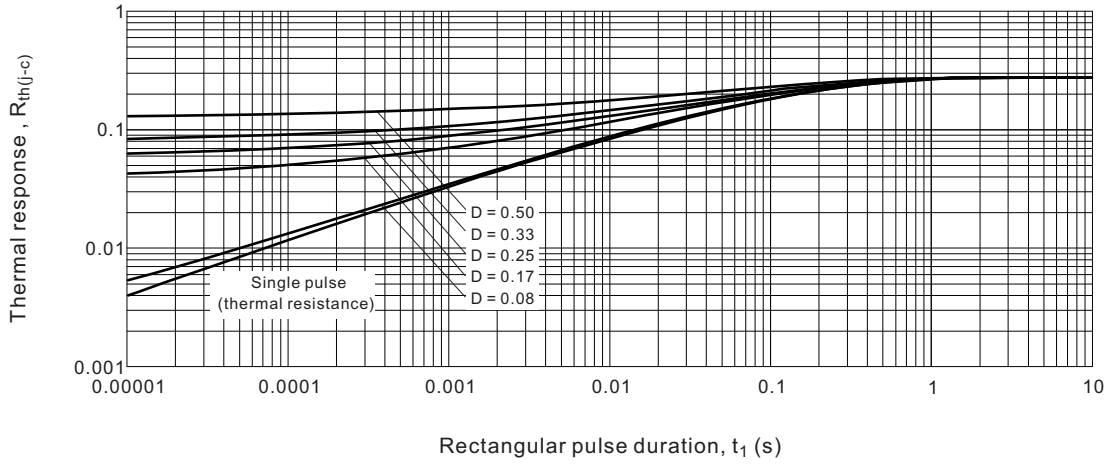
**Fig.8 Typical  $di_{(rec)M}/dt$  vs.  $di_F/dt$  (per leg)**



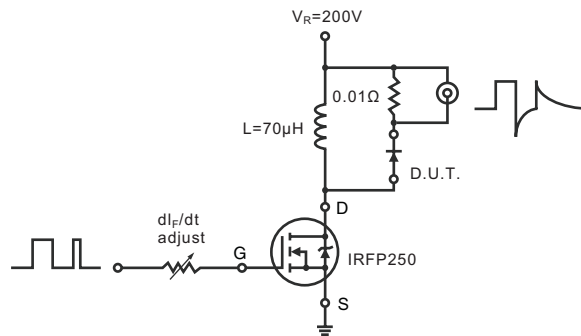
**Fig.9-1 Maximum thermal impedance  $R_{th(j-c)}$  characteristics (per leg, for TO-244 non-insulated)**



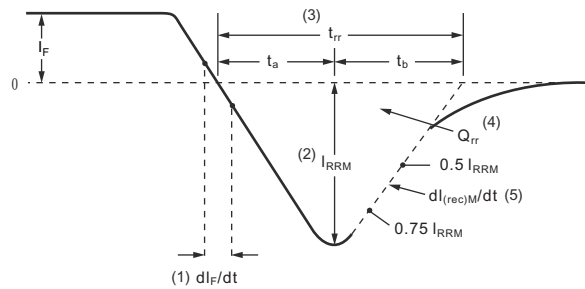
**Fig.9-2 Maximum thermal impedance  $R_{th(j-c)}$  characteristics (per leg, for TO-244 insulated)**



**Fig.10 Reverse recovery parameter test circuit**



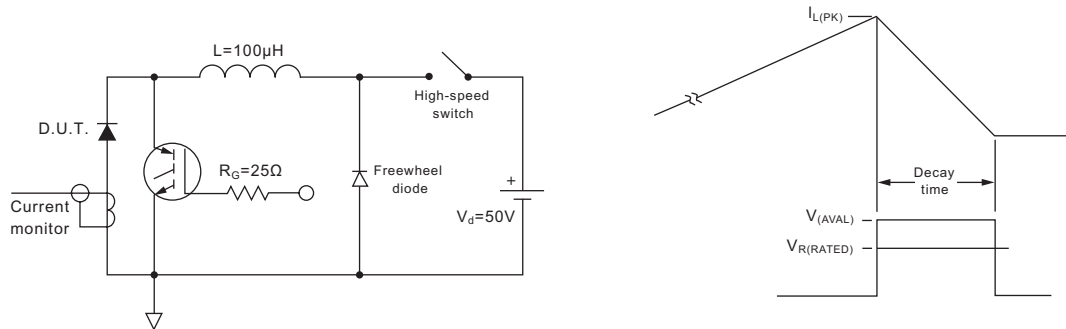
**Fig.11 Reverse recovery waveform and definitions**



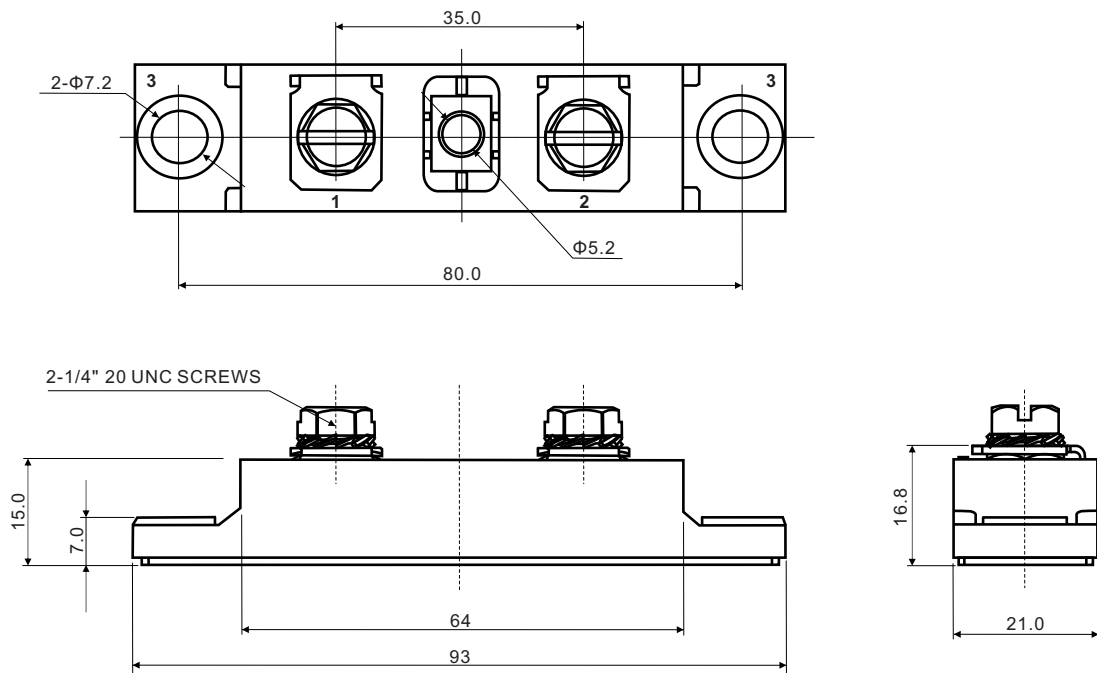
- (1)  $dI_F/dt$  - rate of change of current through zero crossing
- (2)  $I_{RRM}$  - peak reverse recovery current
- (3)  $t_{rr}$  - reverse recovery time measured from zero crossing point of negative going  $I_F$  to point where a line passing through  $0.75 I_{RRM}$  and  $0.50 I_{RRM}$  extrapolated to zero current.
- (4)  $Q_{rr}$  - area under curve defined by  $t_{rr}$  and  $I_{RRM}$
- (5)  $dI_{(rec)}/dt$  - peak rate of change of current during  $t_b$  portion of  $t_{rr}$

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

**Fig.12 Avalanche test circuit and waveforms**

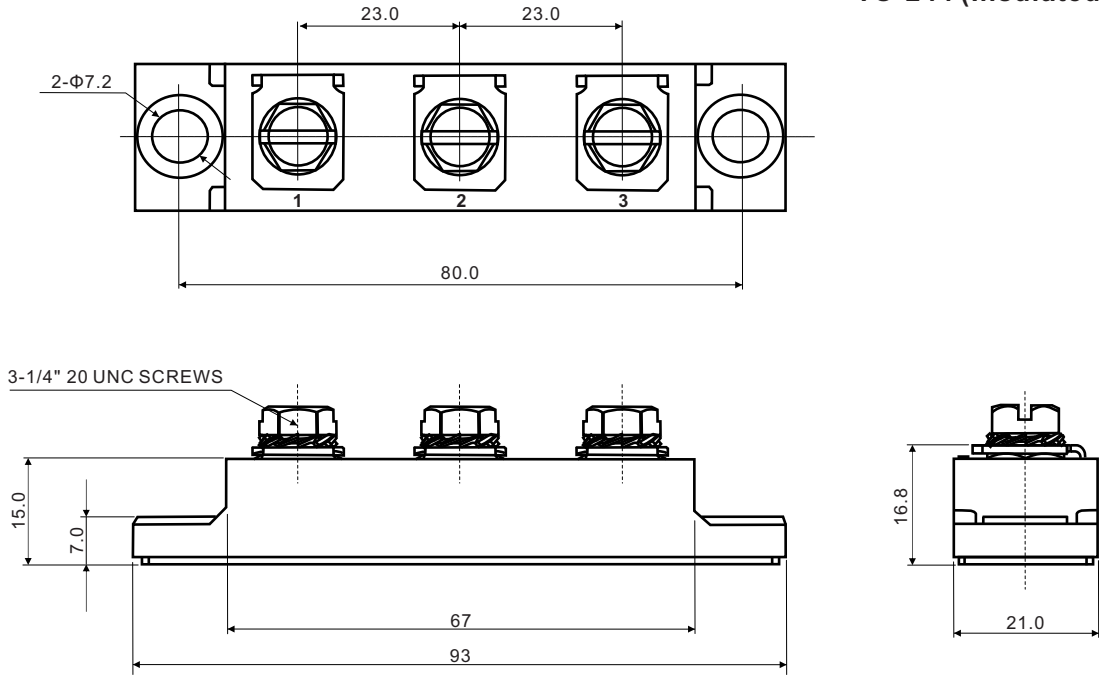


**TO-244 (Non-Insulated)**



All dimensions in millimeters

**TO-244 (Insulated)**



All dimensions in millimeters