

Schottky Rectifier 600A/100V (300A x 2/100V)

FEATURES

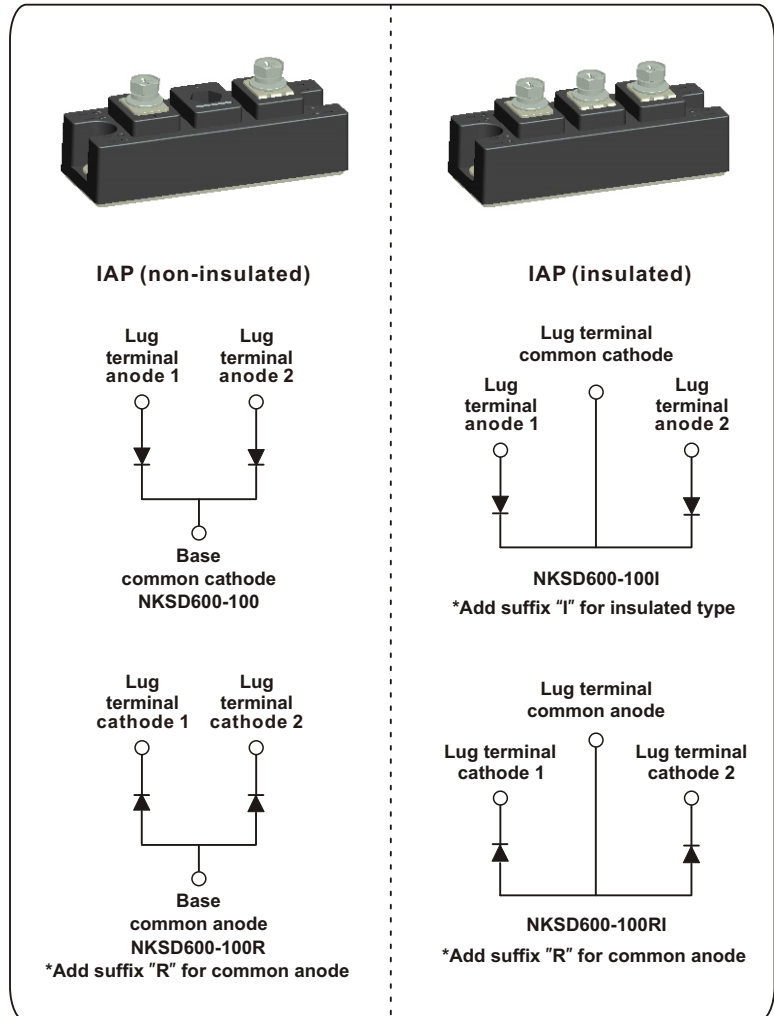
- 175°C T_J operation
- Center tap module
- Low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Lead (Pb)-free
- Designed and qualified for industrial level

DESCRIPTION

The NKSD600... Schottky rectifier common cathode module series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature.

TYPICAL APPLICATIONS

- High current switching power supplies
- Plating power supplies
- UPS system
- Converters
- Freewheeling
- Welder
- Reverse battery protection.



PRODUCT SUMMARY

I _{F(AV)}	600A (300AX2)
V _R	100 V

MAJOR RATINGS AND CHARACTERISTICS

SYMBOL	CHARACTERISTICS	VALUES	UNIT
I _{F(AV)}	Rectangular waveform	600	A
V _{RRM}		100	V
I _{FSM}	t _p = 5 μs sine	38500	A
V _F	300 Apk, T _j = 125°C (per leg)	0.70	V
T _J	Range	-55 to 175	°C

VOLTAGE RATINGS

PARAMETER	SYMBOL	NKSD600-100	UNIT
Maximum DC reverse voltage	V _R	100	V
Maximum working peak reverse voltage	V _{RWM}		

Nell High Power Products

ABSOLUTE MAXIMUM RATINGS						
PARAMETER		SYMBOL	TEST CONDITIONS		VALUES	UNIT
Maximum average forward current See fig.5	per leg per device	I _{F(AV)}	50% duty cycle at T _C = 130°C, rectangular waveform		300	A
					600	
Maximum peak one cycle non-repetitive surge current per leg See fig.7		I _{FSM}	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated V _{RRM} applied	38500	
			10 ms sine or 6 ms rect. pulse		5000	
Non- repetitive avalanche energy per leg		E _{AS}	T _J =25°C, I _{AS} =20A, L=0.2mH		40	mJ
Repetitive avalanche current per leg		I _{AR}	Current decaying linearly to zero in 1 μs Frequency limited by T _J maximum V _A =1.5xV _R typical		2	A

ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNIT
Maximum forward voltage drop per leg See fig.1	V _{FM} (¹)	300A	T _J = 25°C	0.80	V
		600A		1.05	
		300A	T _J = 125°C	0.70	
		600A		0.90	
Maximum reverse leakage current per leg See fig.2	I _{RM} (¹)	T _J = 25°C	V _R = Rated V _R	100	μA
		T _J = 125°C		50	mA
Maximum junction capacitance per leg	C _T	V _R = 5 V _{DC} (test signal range 100 kHz to 1 MHz) 25°C		9000	pF
Typical series inductance per leg	L _S	From top of terminal hole to mounting plane		5	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10000	V/μs
Maximum RMS insulation voltage (for insulated type)	V _{INS}			2500 (1min)	V

Note

(1) Pulse width < 300 μs , duty cycle < 2%

THERMAL-MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	MIN.	TYP.	MAX.	UNIT
Maximum junction and storage temperature range		T _J , T _{Stg}	-55	-	175	°C
Thermal resistance, junction to case per leg	TO-244 (non-insulated)	R _{thJC}	-	-	0.15	°C/W
	TO-244 (insulated)		-	-	0.20	
Thermal resistance, junction to case per module	TO-244 (non-insulated)	R _{thJC}	-	-	0.075	
	TO-244 (insulated)		-	-	0.10	
Thermal resistance, case to heatsink		R _{thCS}	-	0.08	-	
Weight	IAP (non-insulated)		-	145 (5.11)	-	
	IAP (insulated)		-	155 (5.47)	-	
Mounting torque, M6			-	44.2 (5)	53.1 (6)	lbf • in (N•m)
Terminal torque, M6			-	44.2 (5)	53.1 (6)	
vertical pull			-	-	80	lbf • in
2" lever pull			-	-	35	
Case style			JEDEC		INT-A-PAK	

Ordering Information Table

Device code	NK	S	D	600	—	100	R	I
	1	2	3	4		5	6	7
	1 - Nell's power module	2 - S for Schottky Barrier Diode	3 - D for Dual Diodes, IAP Package	4 - Maximum average forward current, A		5 - Voltage rating (100 = 100V)	6 - None for common cathode configuration "R" for common anode configuration	7 - None for non-insulated type "I" for insulated type

Fig.1 Typical forward voltage drop characteristics (Per Leg)

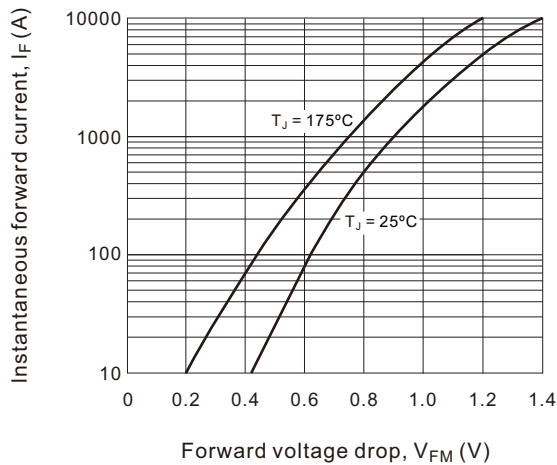


Fig.2 Typical values of reverse current vs. Reverse voltage (Per Leg)

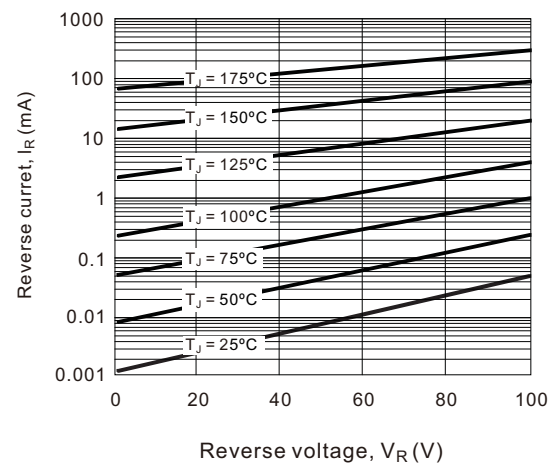
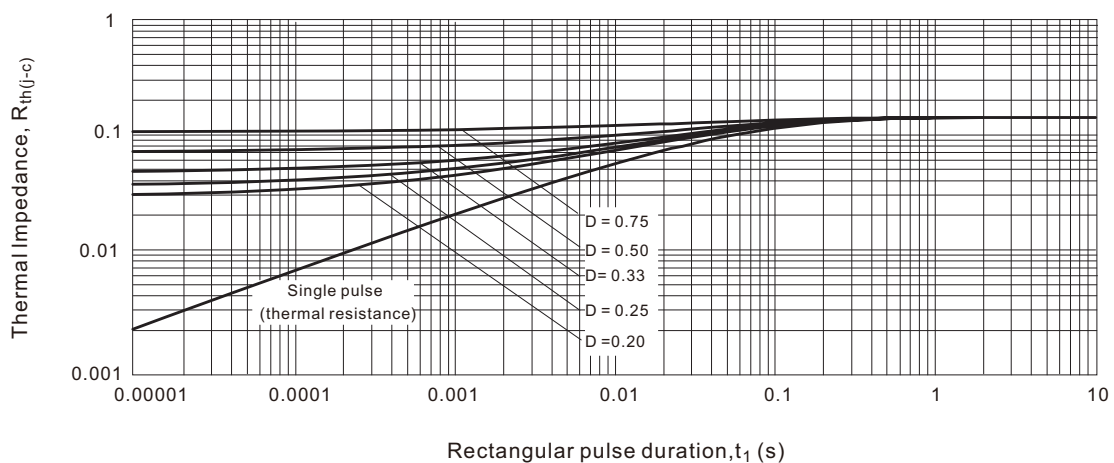
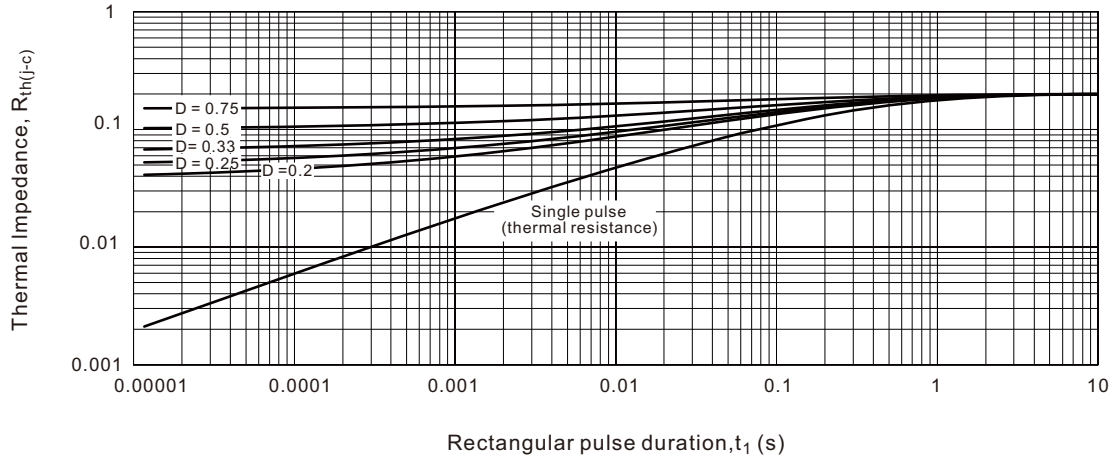


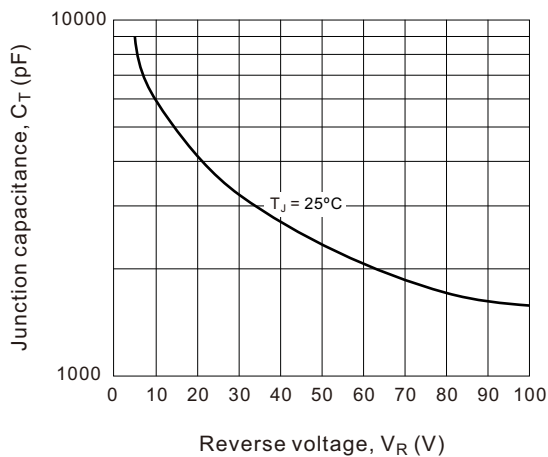
Fig.3-1 Maximum thermal impedance $R_{th(j-c)}$ characteristics (Per Leg, for non-insulated)



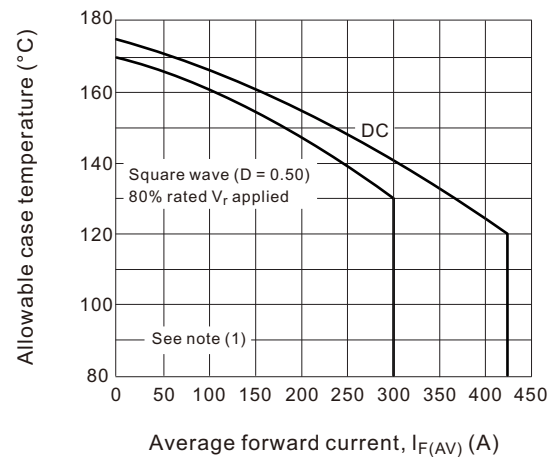
**Fig.3-2 Maximum thermal impedance $R_{th(j-c)}$ characteristics
(Per Leg, for insulated)**



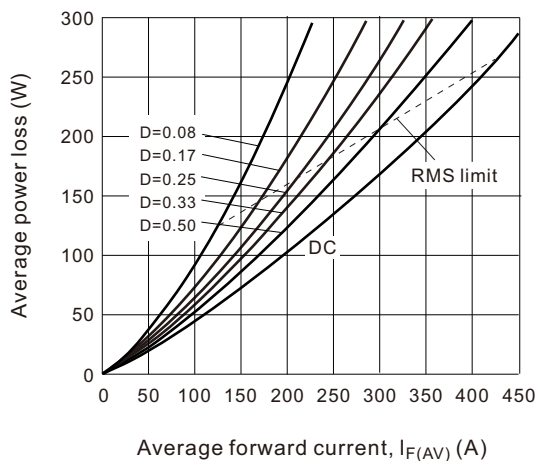
**Fig.4 Typical junction capacitance vs.
Reverse voltage (Per Leg)**



**Fig.5 Maximum allowable case temperature
vs. Average forward current (Per Leg)**



**Fig.6 Forward power loss characteristics
(Per Leg)**



**Fig.7 Maximum non-repetitive surge current
(Per Leg)**

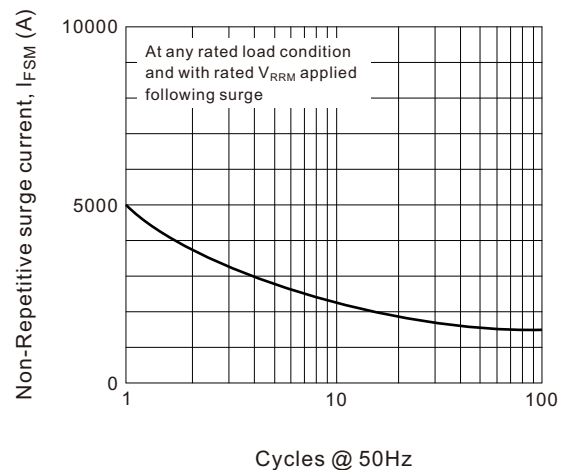
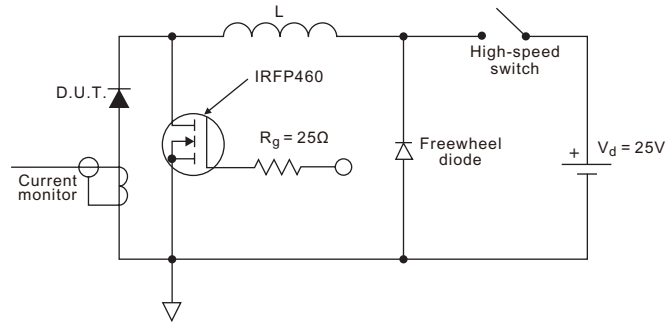


Fig.8 Unclamped Inductive test circuit

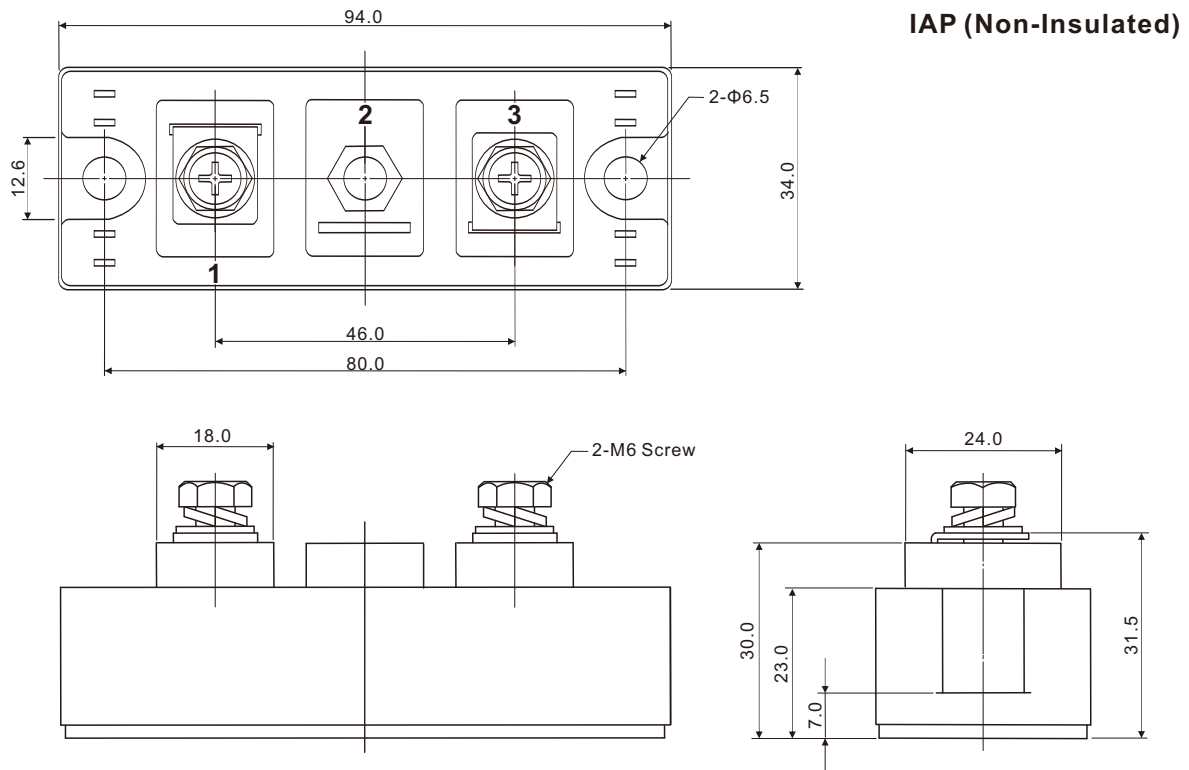


Note

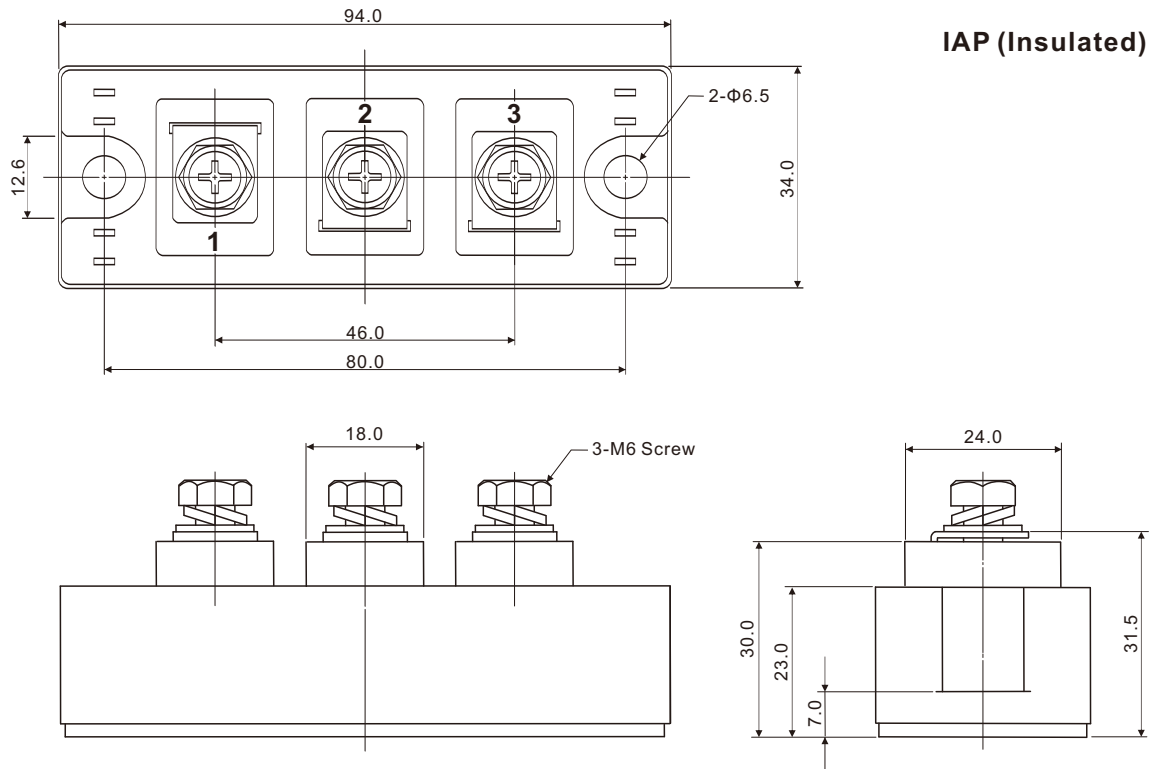
(1) Formula used: $T_C = T_J - (P_d + P_{d_{REV}}) \times R_{thJC}$;

P_d = Forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig.6)

$P_{d_{REV}}$ = Inverse power loss = $V_{R1} \times I_R (1-D)$; I_R at $V_{R1} = 80\%$ rated V_R



All dimensions in millimeters



All dimensions in millimeters