

# TFT COLOR LCD MODULE NL10276AC30-03L

38 cm (15.0 inches), 1024 × 768 pixels, fullcolor, High luminance, Wide viewing angle, multiscan function, built-in CRT interface board

#### **DESCRIPTION**

The NL10276AC30-03L is a TFT (thin film transistor) active-matrix color liquid crystal display (LCD) comprising an amorphous silicon TFT attached to each signal electrode, a driving circuit, CRT interface board, and a backlight. The NL10276AC30-03L has a built-in backlight with an inverter.

The 38 cm (15.0 inch) diagonal display area contains  $1024 \times 768$  pixels and can display fullcolor (more than 16 million colors simultaneously). Also, it has a wide viewing angle and multiscan function.

#### **FEATURES**

- High luminance (350 cd/m² typ.)
- · Wide viewing angle
- · Low reflection
- CRT interface board

Auto recognition of input signal:

Analog RGB signals, synchronous signals (Hsync, Vsyn, composite)

Digital control: e.g. brightness, display position

Free supply voltage sequence Corresponds to DDCI and DDC2B

Corresponds to VESA DPMS

- Multi-scan function: e.g., XGA, SVGA, VGA, VGA-TEXT, MAC
- Incorporated edge-type backlight (Individually two lamps into two lamp holders, inverter)
- Lamp holders replaceable (Part No.: 150LHS13)
- Approved by UL1950 Third Edition and CSA-C22.2 No.950-95

# **On-Screen Display (OSD)**

Application with the OSD function might conflict with patents in Europe and/or the U.S.A

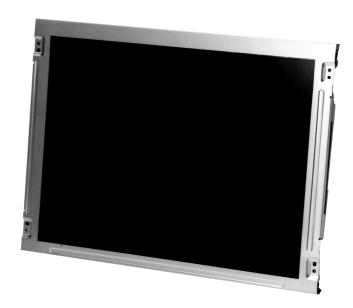
If you apply the OSD function, please do so in accordance with the patent regulations of your location.

VESA: Video Electronics Standards Association DPMS: Display Power Management Signaling

DDC1: Display Data Channel 1
DDC2B: Display Data Channel 2B

#### **APPLICATIONS**

- Desktop PCs
- · Engineering workstations
- · Monitors for process controllers



The information in this document is subject to change without notice. Please confirm the delivery specification before starting to design your system.



#### STRUCTURE AND FUNCTION

A color TFT (thin film transistor) LCD module is comprised of a TFT liquid crystal panel structure, LSIs for driving the TFT array, and a backlight assembly. The TFT panel structure is created by sandwiching liquid crystal material in the narrow gap between a TFT array glass substrate and a color filter glass substrate. After the driver LSIs are connected to the panel, the backlight assembly is attached to the back side of the panel.

RGB (red, green, blue) data signals from a source system are modulated into a form suitable for active-matrix addressing by the onboard signal processor and sent to the driver LSIs, which in turn address the individual TFT cells.

Acting as an electro-optical switch, each TFT cell regulates light transmission from the backlight assembly when activated by the data source. By regulating the amount of light passing through the array of red, green, and blue dots, color images are created with clarity.

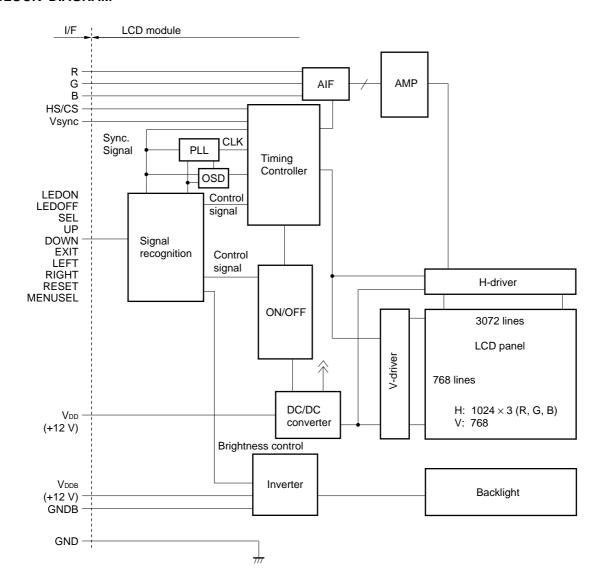
# CHARACTERISTICS (at room temperature)

Items	Description		
Display area	304.128 (H) × 228.096 (V) mm		
Drive system	a-Si TFT active matrix		
Display colors	Fullcolor		
Number of pixels	1024 (H) × 768 (V)		
Pixel arrangement	RGB vertical stripe		
Pixel pitch	0.297 (H) × 0.297 (V) mm		
Module size	350.0 (H) × 265.0 (V) × 22.5 (D) mm (typ.)		
Weight	1680 g (typ.)		
Contrast ratio	250:1 (typ.)		
Viewing angle (more than the contrast ratio of 10:1)	Horizontal: 55° (typ., left side, right side)     Vertical: 45° (typ., up side, down side)		
Designed viewing direction	<ul> <li>Optimum gray-scale (γ = 2.2): perpendicular</li> <li>Best contrast angle: down side 5°</li> </ul>		
Color gamut	40% (typ., at center, to NTSC)		
Response time	15 ms (typ.), "white 100%" to "black 10%"		
Luminance	350 cd/m² (typ.)		
Signal system	Analog RGB signals, synchronous signals (Hsync and Vsync or composite)		
Backlight	Edge light type: four cold cathode fluorescent lamps with an inverter Lamp holder: 150LHS13 Inverter: 150PW131		
Supply voltage	12 V, 12 V (logic/LCD driving, backlight)		
Power consumption	28.2 W (typ.)		

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# **BLOCK DIAGRAM**



HS: Hsync

CS: Composite synchronous signal

Note: Neither GND nor GNDB is connected to Frame. These should be connected in customer equipment.



# **GENERAL SPECIFICATIONS**

ltem	em Description	
Module size	$350 \pm 0.6 \text{ (H)} \times 265.0 \pm 0.6 \text{ (V)} \times 23.5 \text{ (max.) (D)}$	mm
Display area	304.128 (H) × 228.096 (V) [Diagonal 38 cm (15.0)]	mm
Number of dots	1024 × 3 (H) × 768 (V)	dots
Pixel pitch	0.297 (H) × 0.297 (V)	mm
Dot pitch	0.099 (H) × 0.297 (V)	mm
Pixel arrangement	RGB (Red, Green, Blue) vertical stripe	_
Display colors	Full color	color
Weight	1680 (typ.) 1730 (max.)	g

# **ABSOLUTE MAXIMUM RATINGS**

Parameter	Symbol	Rating	Unit	Remarks	
Supply voltage	V <sub>DD</sub>	-0.3 to +14	V	T <sub>a</sub> = 25°C	
	V <sub>DDB</sub>	-0.3 to +14	V		
Logic input voltage	V <sub>In1</sub>	-0.3 to +5.5	V	T <sub>a</sub> = 25°C	
R,G,B input voltage	VIn2	-6.0 to +6.0	V	V <sub>DD</sub> = 12 V	
Storage temp.	Tst	-20 to +60	°C	-	
Operating temp.	Тор	0 to +50	°C	Module surface Note 1	
Relative humidity (RH)		≤ 95	%	Ta ≤ 40°C	
Note 2		≤ 85		40 < Ta ≤ 50°C	
Absolute humidity	Absolu	Absolute humidity shall not exceed		Ta > 50°C	
Note 2		T <sub>a</sub> = 50°C, RH = 85 %			

Notes: 1. Measured at the display area (including self-heat.)

2. No condensation



# **ELECTRICAL CHARACTERISTICS**

# (1) Logic, LCD Driving, Backlight

 $(T_a = 25^{\circ}C)$ 

Item	Symbol	Min.	Тур.	Max.	Unit	Remarks
Supply voltage	V <sub>DD</sub>	10.8	12.0	13.2	V	Logic and LCD driving
	V <sub>DDB</sub>	10.8	12.0	13.2	V	Backlight
Logic input "L" voltage	VIL	0	-	0.8	V	-
Logic input "H" voltage	VIH	2.2	_	5.25	V	
Logic output "L" voltage	Vol	_	_	0.4	V	DDCDAT
Logic output "H" voltage	Vон	2.4	_	-	V	
Logic input "L" current 1	lo <sub>L1</sub>	ı	_	3.0	μΑ	DDCDAT
Logic input "H" current 1	<b>І</b> он1	-1.0	_	_	μΑ	
Logic output "L" current	lı∟	-1.0	_	-	μΑ	HS/CS, Vsync
Logic output "H" current	Ітн	ı	_	1.0	μΑ	
Supply current	lod	ı	620 <b>Note</b>	900	mA	V <sub>DD</sub> = 12.0 V
		_	185	200	mA	Power-saving mode VDD = 12.0 V
	IDDB	-	1700	1850	mA	V <sub>DDB</sub> = 12.0 V (Max. luminance)
		ı	20	30	mA	Power-saving mode VDDB = 12.0 V

Note: Checker flag pattern (In EIAJ ED-2522)

# (2) Video Signal (R, G, B) Input

 $(Ta = 25^{\circ}C)$ 

Items	Min.	Тур.	Max.	Unit	Remarks
Maximum amplitude (black - white)	0 (black)	0.7 (white)	*A	Vp-p	Note
DC input level (black)	-0.5	_	+2.5	٧	-
Sync level	-0.2	0.3	*B (0.6)	Vp-p	G terminal (sync on green)
*A + *B	-	-	1.4	Vp-p	-

Note: Needs to adjust contrast ratio if the input level is beyond 0.7 Vp-p.

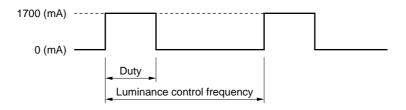
Remark: You may see noise on the display if input video sinal have noise. And the frequency is bad characteristics, the display becomes blurry indication.



# **Power Supply Design**

- (1) Note that the supply voltage must not be applied while the control signals (SEL, UP, DOWN, EXIT, LEFT, RIGHT, and RESET) are connected to GND. Otherwise, the module may malfunction.
- (2) If the power supply voltage is applied while UP and DOWN are connected to GND, the input control signals become ineffective and all key inputs are neglected. To reset this mode, turn off the power once and then turn on the power while UP and DOWN are connected to GND. The mode will be released.
- (3) Do not change the MENUSEL setting while the module is being operated.
  MENUSEL selection (hierarchical type or toggle type) is continuously set during the power supply voltage is applied.
- (4) Inverter current wave

The inverter current wave is as follows.



Maximum luminance control: 100% (Duty) Minimam luminance control: 20% (Duty)

Luminance control frequency = Input Vsync frequency  $\times$  K

Input Vsync frequency  $\leq$  75 Hz: K = 4.6 Input Vsync frequency > 75 Hz: K = 3.6

Please set up like above diagram.

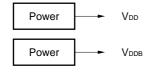
#### (5) Ripple of supply voltage

Supply voltage	V <sub>DD</sub> (for logic and LCD driver)	V <sub>DDB</sub> (for backlight)
Acceptable range	≤ 100 mVp-p	≤ 200 mVp-p

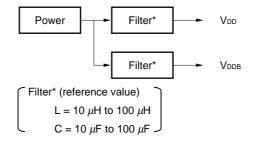
Remark: The acceptable range of ripple voltage includes spike noise.

Example of the power supply connection

a) Separate the power supply



b) Put in the filter





# (6) Fuses

Supply voltage	Part No.	Supplier	Ratings	Remarks
VDD	CCFINTE3.15	KOA	60VDC/3.15A	_
V <sub>DDB</sub>	① ICP-S2.3 T104	ROHM	72VDC/2.3A	① or ② is used.
	② CCP2E40H	KOA	72VDC/2.0A	Note 1

Note: Please design power-supply capacity for the more than 1.5 times of the fuse rating from the safety point of view.

In case where the capacity is under the 1.5 times of the rating, the LCD module should be enough evaluated about safety in the case of the short circuit.

#### INTERFACE PIN CONNECTIONS

# (1) Logic and LCD driving

CN1

Part No.: 1-353119-4 Adaptable socket: 1-353068-4

Supplier: Tyco Electronics AMP K.K.

Pin No.	Symbols	Pin No.	Symbols
1	В	8	Vsync
2	BGND	9	GND
3	G	10	HS/CS
4	GGND	11	GND
5	R	12	DDCCLK
6	RGND	13	N.C.
7	GND	14▼	DDCDAT

Figure from socket view 1 2 . . . . . . 13 14

Note: N.C. (No connection) must be open.



CN3

Part No.: DF14A-25P-1.25H

Adaptable socket: DF14-25S-1.25C (Gold-Plated terminals)

Supplier: Hirose Electric Co., Ltd.

Pin No.	Symbols	Pin No.	Symbols
1	LEDON	14	EXIT
2	LEDOFF	15	GND
3	GND	16	N.C.
4	N.C.	17	GND
5	N.C.	18	GND
6	RIGHT	19	MENUSEL
7	LEFT	20	N.C.
8	RESET	21	N.C.
9	N.C.	22	N.C.
10	GND	23	GND
11	SEL	24	N.C.
12	UP	25	N.C.
13	DOWN		

Figure from socket view

▼

1 2 · · · 24 25

Note: N.C. (No connection) must be open.

CN5

Part No.: IL-Z-8PL-SMTY Adaptable socket: IL-Z-8S-S125C3

Supplier: Japan Aviation Electronics Industry, Limited (JAE)

Pin No.	Symbols	Pin No.	Symbols
1	V <sub>DD</sub>	5	GND
2	V <sub>DD</sub>	6	GND
3	V <sub>DD</sub>	7	GND
4	V <sub>DD</sub>	8▼	GND

Figure from socket view

▼

8 7 · · · · · 2 1



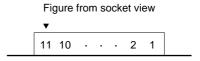
# (2) Backlight

CN201

Part No.: IL-Z-11PL1-SMTY Adaptable socket: IL-Z-11S-S125C3

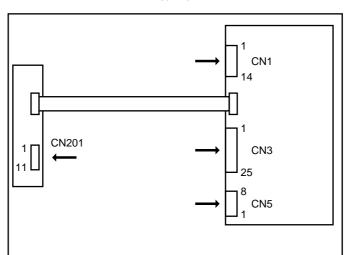
Supplier: Japan Aviation Electronics Industry, Limited (JAE)

Pin No.	Symbols	Pin No.	Symbols
1	V <sub>DDB</sub>	7	N.C.
2	V <sub>DDB</sub>	8	N.C.
3	V <sub>DDB</sub>	9	N.C.
4	GNDB	10	N.C.
5	GNDB	11▼	N.C.
6	GNDB		



Note: N.C. (No connection) must be open.

# Rear view





# (3) PIN FUNCTIONS

Symbol	I/O	Logic	Description	
HS/CS	Input	Negative/ Positive	Horizontal synchronous signal input or composite synchronous signal input (TTL level), positive/negative auto recognition	
Vsync	Input	Negative/ Positive	Vertical synchronous signal input (TTL level), positive/negative auto recognition, clock input for DDC1	
R	Input	_	Red video signal input (0.7 Vp-p, input impedance 75 $\Omega$ )	
G	Input	-	Green video signal input (0.7 Vp-p, input impedance 75 $\Omega$ )	
В	Input	-	Blue video signal input (0.7 Vp-p, input impedance 75 $\Omega$ )	
LEDON	Output	Positive	Indicator for LED power on "H": LED select; "L": Other status	
LEDOFF	Output	Positive	Indicator for power saving mode "H": Power saving mode select; "L": Other status	
DDCCLK	Input	Positive	CLK for DDC2B	
DDCDAT	Input/ Output	Positive	Data for DDC1/2B Read/Write	
SEL	Input	Negative	Control function select signal (TTL level) SEL is pulled up in the module. Details of the functions are mentioned in <b>CONTROL FUNCTIONS</b> , Page 14. "H" or "open": SEL off; "L": SEL on	
UP	Input	Negative	Control signal (TTL level) The signal increases the value of the selected function. UP is pulled up in the module. "H" or "open": UP off; "L": UP on	
DOWN	Input	Negative	Control signal (TTL level) The signal decreases the value of the selected function. DOWN is pulled up in the module. "H" or "open": DOWN off; "L": DOWN on	
EXIT	Input	Negative	Control function exit signal (TTL level)  EXIT is pulled up in the module.  "H" or "open": EXIT off; "L": EXIT on	
RIGHT <sup>Note 1</sup>	Input	Negative	Control signal (TTL level) The signal increases the value of the selected function. RIGHT is pulled up in the module. "H" or "open": RIGHT off; "L": RIGHT on	
LEFT <sup>Note 1</sup>	Input	Negative	Control signal (TTL level) The signal decreases the value of the selected function. LEFT is pulled up in the module. "H" or "open": LEFT off; "L": LEFT on	
RESET <sup>Note 1</sup>	Input	Negative	Control signal (TTL level) The signal initializes the selected function. RESET is pulled up in the module. "H" or "open": RESET off; "L": RESET on	
MENUSEL	Input	_	OSD menu select signal (TTL level) MENUSEL is pulled up in the module. "H or open": Hierarchical type "L": Toggle type For details, see Page 11, OSD Menu Select.	

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Symbol	I/O	Logic	Description
V <sub>DD</sub>	-	-	Power supply for logic and LCD driving +12 V (±10%)
V <sub>DDB</sub>	-	_	Power supply for backlight +12 V (±10%)
GND	_	_	Ground for system
RGND GGND BGND	-	-	Ground for R, G, B analog data
GNDB	-	-	Ground for backlight

Note: This terminal becomes effective only when the value of Button Sel is 7. Detail of the functions is mentioned in page 27, INITIAL VALUE CHANGE FUNCTIONS.

Remarks 1: Neither GND nor GNDB is connected to Frame. These should be connected in customer-

2: 12 V for backlight should be started up within 300 ms, othernise, the protection circuit makes the backlight turn off.

# (4) OSD MENU SELECT

OSD menu type	Hierarchical type	Toggle type		
MENUSEL	"H" or "open"	"["		
How to adjust	For details, see page 27, INITIAL VALUE CHANGE FUCNTIONS.  MENU  Auto Adjust Brightness Clock Position Contrast	For details, see page 27, INITIAL VALUE CHANGE FUCNTIONS.		

Notes: 1. If the OSD menu type is changed, the adjustment values become default. Then, it takes 5 seconds to display the changed menu after the supply voltage is input again. Indicators for both LEDON and LEDOFF are "ON" during the 5 seconds.

2. The initial value of the Contrast ratio is as follows.

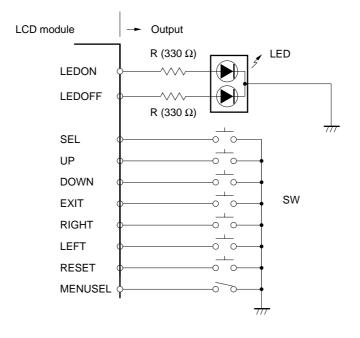
Hierarchical type: 128:1 (typ.) Toggle type: 100:1 (typ.)



# (5) Equivalent Circuit

Symbol	I/O	Equivalent circuit
R, G, B	Input	Input ο 75Ω >
HS/CS, Vsync	Input	Input ο
LEDON LEDOFF	Output	RN2306 (Toshiba) Or equivalent  Output

# **Example of LED circuit**





# **INPUT SIGNALS**

# (1) Synchronous Signal

This module is corresponding to the synchronous signals below.

Automorphism and to		Synchron	ous signal
Auto recognition mode	HS/CS	Vsync	Sync. on Green
Separate synchronous signal mode (HS, Vsync)	Input	Input	Input or no input
Composite synchronous mode	Input (cs)	No input	Input or no input
Sync. on Green mode	No input	No input	Input
Power-saving mode	Input (HS)	No input	Input or no input
	No input	Input	Input or no input
	No Input	No input	No input

**Notes: 1.** Power-saving mode corresponds to VESA DPMS.

2. The module recognizes the Sync on Green signal automatically, when the FUNC in SOG Sel of Sel Data is "Y". Refer to Page 27, Initial Value Chance Functions.

# (2) Preset Timing

The fourteen kinds of timings below are already programmed in this module. The input synchronous signals are automatically recognized.

Preset No.	Display size	Vsync (Hz)	Hsync (kHz)	SYSTEM CLK (MHz)	V Pulse (H)	V B.Porch (H)	H Pulse (CLK)	H B.Porch (CLK)	Sync Logic V, H	Remarks
1	640 × 400	56.432	24.830	21.053	8	25	96	48	-,-	Note
2	640 × 480	59.992	31.469	25.175	2	33	96	48	-,-	VGA
3	720 × 400	70.087	31.469	28.322	2	35	108	45	+,-	VGA TXT
4	800 × 600	60.317	37.879	40.000	4	23	128	88	+,+	VESA
5	640 × 480	66.667	35.000	30.240	3	39	64	96	S on G	Macintosh
6	640 × 480	75.000	37.500	31.500	3	16	64	120	-,-	VESA
7	720 × 400	85.039	37.927	35.500	3	42	36	144	+,-	VESA
8	640 × 480	85.008	43.269	36.000	3	25	48	112	-,-	Note
9	1024 × 768	60.004	48.363	65.000	6	29	136	160	-,-	VESA
10	800 × 600	75.000	46.875	49.500	3	21	80	160	+,+	VESA
11	832 × 624	74.565	49.735	57.283	3	39	64	224	S on G	Macintosh
12	800 × 600	85.061	53.674	56.250	3	27	64	152	+,+	Note
13	1024 × 768	70.069	56.476	75.000	6	29	136	144	+,+	VESA
14	1024 × 768	75.029	60.023	78.750	3	28	96	176	+,+	VESA

Note: Out of specification.



- **Remarks 1:** Even if the one of preset timing is entered, a little adjustment of the functions such as horizontal period, CLK delay and display position is required. The adjusted values are memorized in every preset Number.
  - 2: This module recognizes the synchronous signals with near preset timing of the frequency of HS, Vsync, even if the signals other than the preset timing that were entered. For instance, it is displayed with presetting number 6 in the case of 640 × 480 dot; HS: 37.861kHz; Vsync: 72.809 Hz. Please note that using other than the preset timings may cause incorrect magnification ratios, unfitness of control functions, or other display problems.
  - 3: The display color scale may be different between Sync on Green input and the others.

#### **CONTROL FUNCTIONS**

#### (1) Expansion Modes

Expansion mode is a function by which expand the screen size in different resolutions. For example, the VGA signal has  $640 \times 480$  pixels. But if the display data can be expanded to 1.6 times vertically and horizontally, the VGA screen image can be displayed fully on a screen with XGA resolution.

This module automatically recognizes the timing shown in **Preset Timing** as an expansion mode.

Please adopt this mode after evaluating display quality, because the appearance in expansion mode may degrade in some cases.

The following table shows display magnifications for each mode.

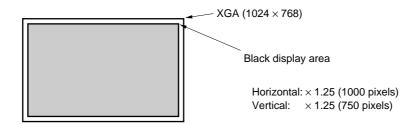
lanest Olemente	Newskan of Pinals	Magnifications		
Input Signals	Number of Pixels	Vertical	Horizontal	
XGA	1024 × 768	1.0	1.0	
SVGA	800 × 600	1.25	1.25	
VGA	640 × 480	1.6	1.6	
VGA text	720 × 400	1.92 / 1.6 hierarchical/toggle	1.42	
MAC	832 × 624	1.2	1.2	

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# **Display Image**

# (a) SVGA mode $(800 \times 600)$

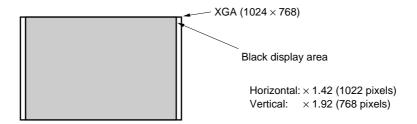


# (b) VGA mode $(640 \times 480)$

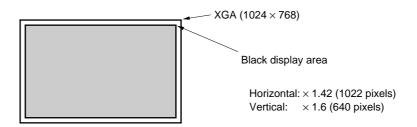


# (c) VGA text mode $(720 \times 400)$

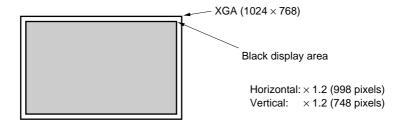
#### (i) Hierarchical type select



# (ii) Toggle type select



# (d) $832 \times 624$ MAC mode $(832 \times 624)$





#### (2) DDC Function

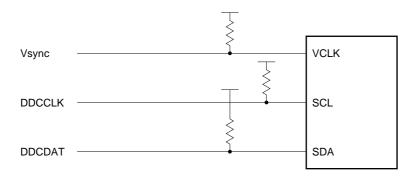
This function corresponds to VESA DDC<sup>™</sup> and EDID<sup>™</sup> (Structure Version 1).

Follow the specifications of the IC when you write data. (VCLK is fixed on "H.")

Write a data into the necessary addresses in advance when using this function. Data "55H" in address "00H" and "FFH" in other address have already been programmed upon shipping. The input equivalent circuit diagram is as follows.

EDID: Extended Display Identification Data

# Internal circuit diagram



Product: Microchip Technology, Inc. 24LC21A or equivalent

# (3) DPMS

This function corresponds to the VESA DPMS<sup>™</sup> standard.

		NL10276AC30-03L					
Ctata	Signal				December times	Dawan aasima	Recovery-
State	Horizontal	Vertical	Video	Power-saving	ng Recovery-time	Power-saving	time
On	Pulses	Pulses	Active	None	Not applicable	None	Not applicable
Standby	No pulses	Pulses	Blanked	Minimum	Short	Maximum	Short
Suspend	Pulses	No pulses	Blanked	Substantial	Longer	Maximum	Short
Off	No pulses	No pulses	Blanked	Maximum	System dependent	Maximum	Short

**Remark:** The power consumption of power saving mode is different between Sync. On Green and others. The power consumption of Sync on Green is higher than others because of synchronous separation circuit.

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# (4) Control Function Items

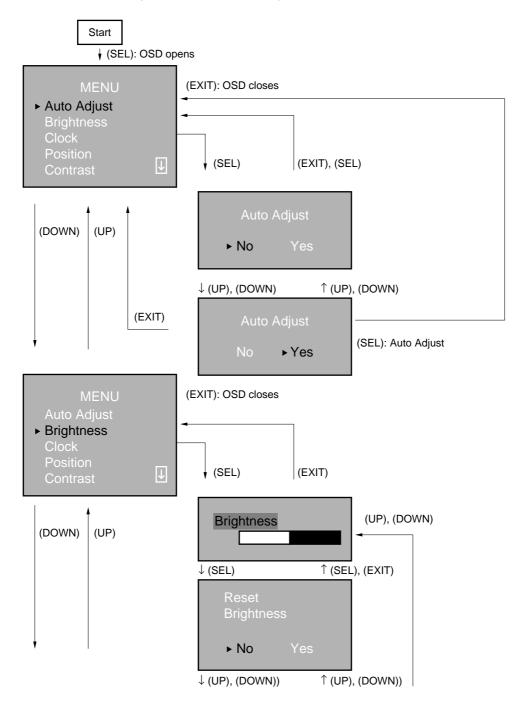
No.	Fo	orm	Function items
1	Auto adjust		Clock and position auto adjust
2	Brightness		Brightness of backlight control
3	Clock	H. Size	Horizontal display period adjust
		Clock Phase	CLK phase adjust
4	Position H. Position		Horizontal position adjust
	V. Position		Vertical position adjust
5	Contrast W, R, G, B		White level of video signals; synchronous color and each color control
	W		White level of video signal synchronous color control
6	Color Level W, R, G, B		Color level of video signals; synchronous color and each color control
		W	Color level of video signal synchronous color control
7	Information		Video signal information Display multi scan function, Hsync and Vsync frequency
8	OSD Position		OSD position adjust The display position of OSD can be moved.
9	Config		Sync on Green signal.  Response or non response to Sync on Green signal is selected.  When non response is selected (see Page 27, Initial Value Change Functions), config is not displayed.
10	All Reset		Reset to initial value.  The values in No. 1 to No. 6, No. 8 and No. 9 are returned to the initial values.

- Remarks 1: 1 In order to achieve the best picture quality, the above functions should be adjusted by setting the SEL, UP, DOWN, LEFT, RIGHT, RESET, and EXIT signals. clock and position adjusted values are memorized every display modes.
  - ② The memorized values are not affected even if the power is switched off. But the selected value is not memorized if a selected mode is not completed.
  - 2: The auto-adjusted values may not become the optimal values for the best picture quality by customer equipment and display screens. And the Auto adjust function does not work correctly when both Sync On Green signal and separate synchronous signals are input in this module. In that case, please adjust each function (clock and position).
  - 3: Screen may be disturbed only a moment when each adjusted value is changed.

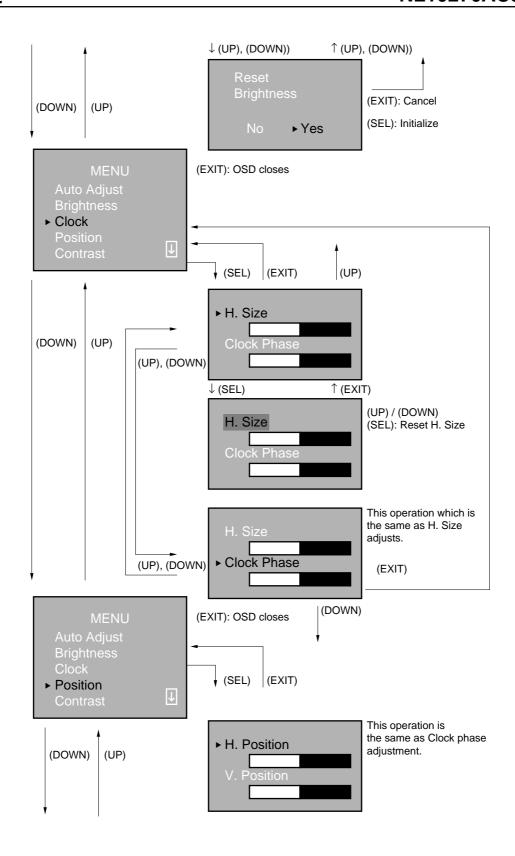
# (5) Control Function Flow Chart

# (a) Hierarchical type (Number of adjust switch is 4:SEL; UP; DOWN; EXIT)

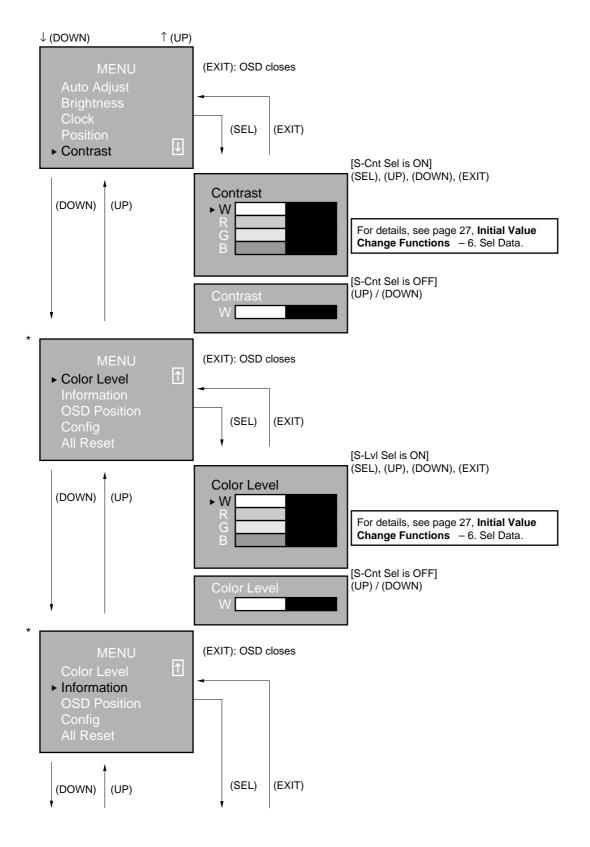
For details, see page 27, Initial Value Change Functions – 6. Sel Data, Button Sel.



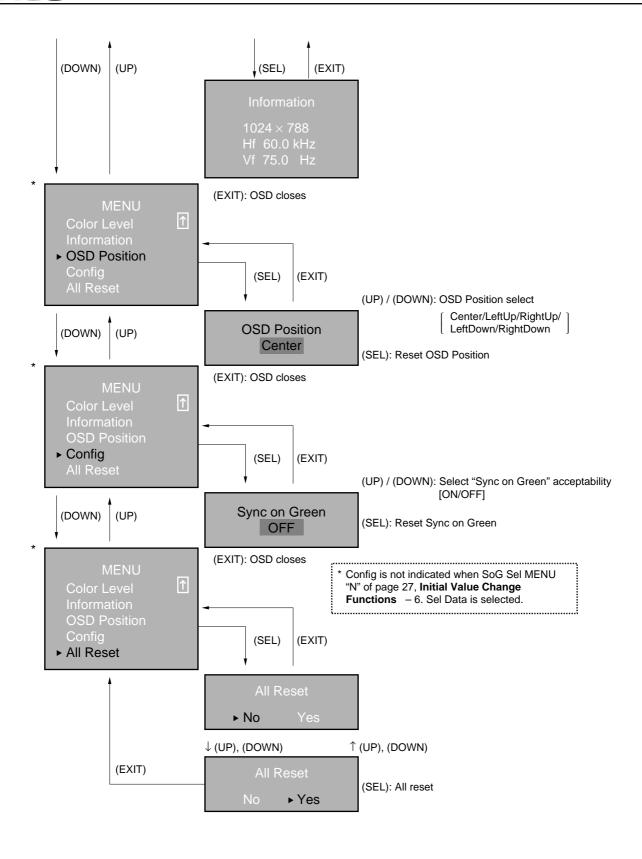
Continued on next page



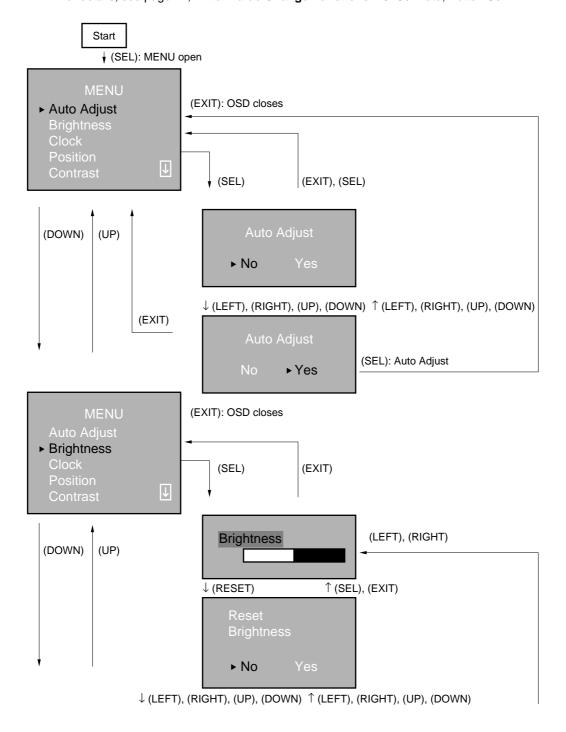
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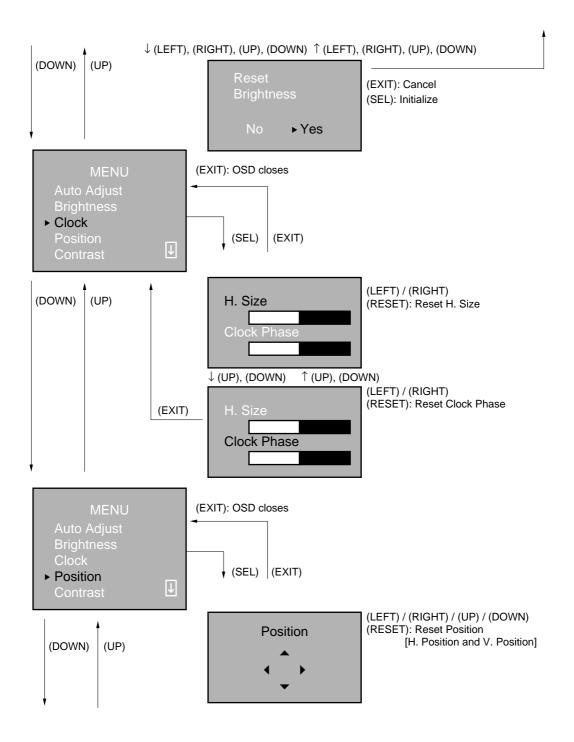
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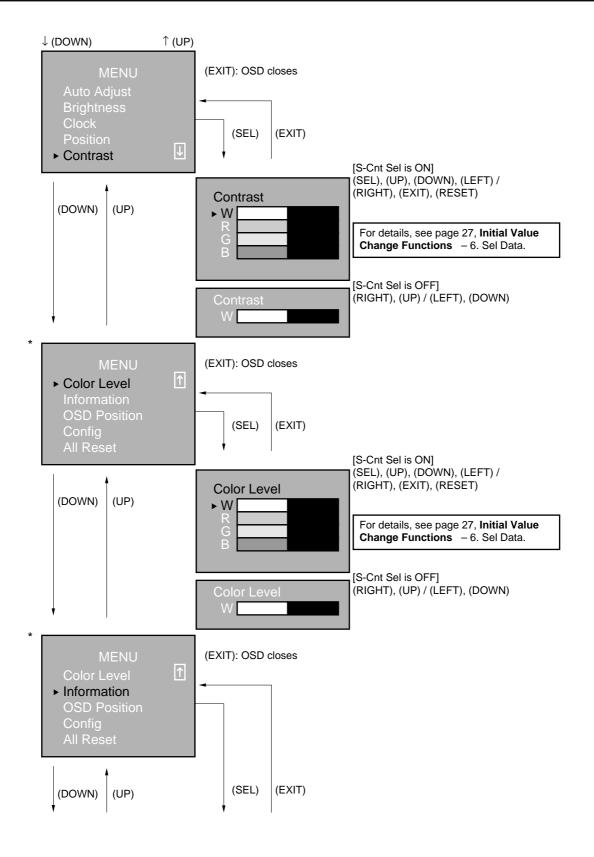
(b) Hierarchical type (Number of adjust switch is 7:SEL; UP, DOWN; EXIT; LEFT; RIGHT; RESET) For details, see page 27, Initial Value Change Functions – 6. Sel Data, Button Sel.



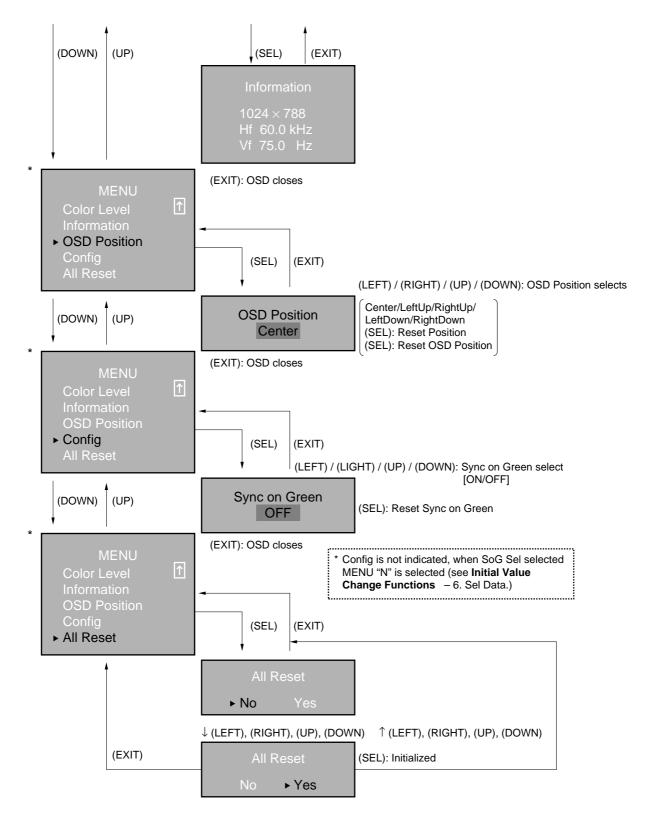
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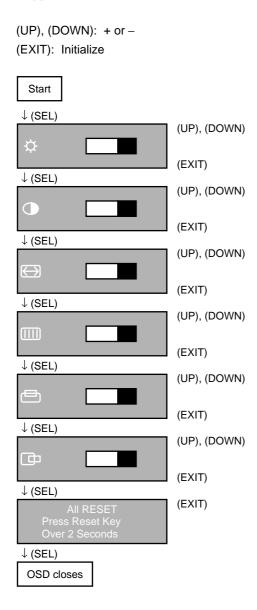


**Notes:** 1. The value of the selected signals of the LEFT, RIGHT, UP, and DOWN keys is continuously increased or decreased if the input signal is held for more than approximately one second. If it's held less than one second, the value is increased or decreased by one value.

- 2. The RESET signal initializes the value selected by the SEL key. "All Reset" function initializes all the values.
- 3. No key input for more than ten seconds will be regarded as "time out".
- 4. Contrast W is synchronous with Contrast R, Contrast G, and Contrast B.
- 5. Color Level W is synchronous with Color Level R, Color Level G, and Color Level B.



# (c) Toggle type (SEL, UP, DOWN, and EXIT)



**Notes:** 1. The value of the selected signals by the UP and DOWN key is continuously increased or decreased when the UP or DOWN key is pushed for more than about one second.

- **2.** The RESET signal initializes the value selected by the SEL key. The All Reset function initializes all the values.
- 3. No key input for more than five seconds will be regarded as "time out."
- $\textbf{4.} \ \ \mathsf{EXIT} \ \mathsf{is} \ \mathsf{connected} \ \mathsf{to} \ \mathsf{GND} \ \mathsf{for} \ \mathsf{more} \ \mathsf{than} \ \mathsf{approximately} \ \mathsf{two} \ \mathsf{seconds}.$



# (6) Initial Value Change Functions

This module can change initial values, OSD items, and so on in customers.

The changed initial value becomes the initial value in each preset timing, and is applicable when the value is reset too (including All Reset).

• How to transpose to initial value change mode.

Hierarchical type: Connect SET and UP to GND at the same time during the OSD function is "information". Connect SEL to GND while UP is connected to GND during the OSD function is "All Toggle type:

Reset".

Operation

SEL, UP, DOWN, and EXIT

· How to return to normal mode

Connect EXIT to GND or turn off the Power supply when OSD closes.

Both LEDON and LEDOFF become "H" when this mode is operating.

No.	Iter	ns	Function items
1	Compatible		It becomes valid when the Toggle menu type only is selected.  YES Sync on Green  The initial value of contrast (RGB synchronous) becomes 128.
			NO: No Sync on Green The initial value of contrast (RGB synchronous) becomes 100. Initial value: NO
2	Brightness		The maximum value of brightness of the backlight can be changed. Initial value: 100
3	Color Level		The initial value of the color level can be changed. Initial value: 128
4	Contrast		The initial value of contrast can be changed. Initial value: 128 (When NO is selected by Compatible in the toggle menu type, the initial value of RGB synchronous only is 100. As for RGB synchronous, this clause setup value becomes valid as the initial value regardless of the selection state of Compatible.)
5	LCD Data <sup>Note 1</sup>	Mode	Number of page 13 PRESET TIMINGS is displayed.
		HSE	The initial value of the Double Value can be changed.
		Delay	The value of the CLK delay can be changed. (Initial value can not be changed.) Initial value: 0
		VD	The initial value of the up/down position of the display position can be changed.
		HD	The initial value of the left/right position of the display position can be changed.
	Auto Adjust		Automatic adjustment
	Par. Delay <sup>Note 2</sup>		The compensation value of Delay of the auto adjust can be changed.  Valid range: 0 to 255
		Par. HD <sup>Note 2</sup>	The compensation value of the horizontal position of the auto adjust can be changed.  Valid range: 0 to 127

Continue to next page



No.	Fo	orm	Function Items
5	LCD Data	LCD Reset	The values set in No. 5 LCD Data, are returned to the NEC factory values.
6	Sel Data	S – Lvl Sel	RGB individual adjustment can be made impossible in the adjustment of color level. In that case, the adjustment items are not displayed on the menu.  ON: Individual adjustment is available.  OFF: Individual adjustment is impossible Initial value: ON.
		S – Cnt Sel	RGB individual adjustment can be made impossible in the adjustment of contrast. In that case, the adjustment items are not displayed on the menu.  ON: Individual adjustment is available.  OFF: Individual adjustment is impossible Initial value: ON.
		SOG Sel	Select or nonselect of Sync on Green and display or nondisplay of the select menu can be selected. When nondisplay selected, Config is not displayed.  • FUNC (Y: For Sync on Green select)  (N: For Sync on Green nonselect)  • MENU (Y: Select MENU display)  (N: Select MENU nondisplay)  Initial value (Y/N)  * Effective only when the hierarchical menu type is selected.  It becomes invalid when the toggle menu type is selected.
		MENU Sel	Two kinds of B (Blue) and S (transparency) can be selected as the background color of the OSD menu.  Initial value: B
		Button Sel	4 (SEL, UP, DOWN, and EXIT) or 7 (SEL, UP, DOWN, RIGHT, LEFT, EXIT, and RESET) as the numbers of the adjustment switches are selected.  Initial value: 4
		PSOSD Sel	Display or no display for Power Save OSD can be selected.  (For details, see page 29, Information Display by OSD)  ON: Display  OFF: No display  Initial value: OFF
7	Information		Input signal information is displayed.
8	OSD Position		The initial value of the OSD display position can be changed.  C: Center UL: Upper left UR: Upper right DL: Down left DR: Down right Initial value: C
9	Used Time		P. ON: Normal mode P. OFF: Power saving mode Used time is recorded every five minutes. But it is not recorded when the power supply is cut off within five minutes.
10	Full Reset		All values are returned to NEC factory values (default). It takes about seven seconds.

Remark: Selected adjustment item is displayed with OSD.

Remark: All adjustment values are returned to NEC factory values (default) when menu type is changed.

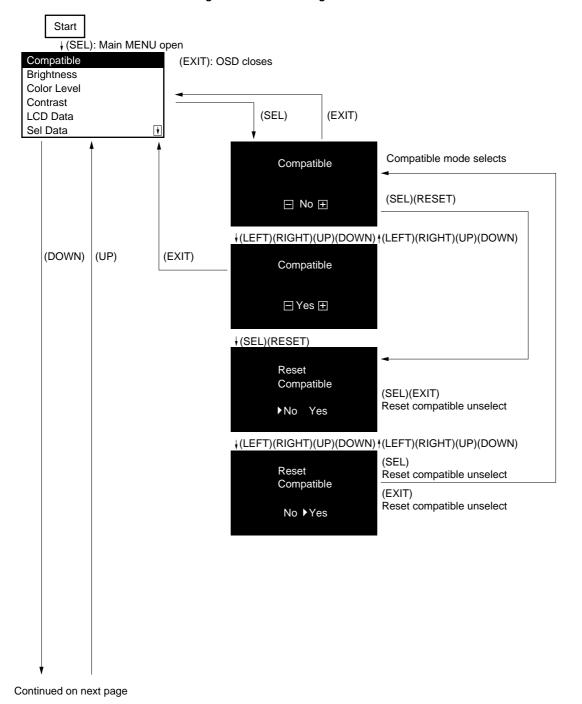
Notes 1: • The values of No. 5 LCD Data are memorized in every preset timing.

• Data except Auto Adjust of No. 5 LCD Data is saved at the moment when it is returned in the top menu. But it is not saved if the power supply is cut off before completion. It is saved after auto adjust is finished in case of the Auto Adjust.

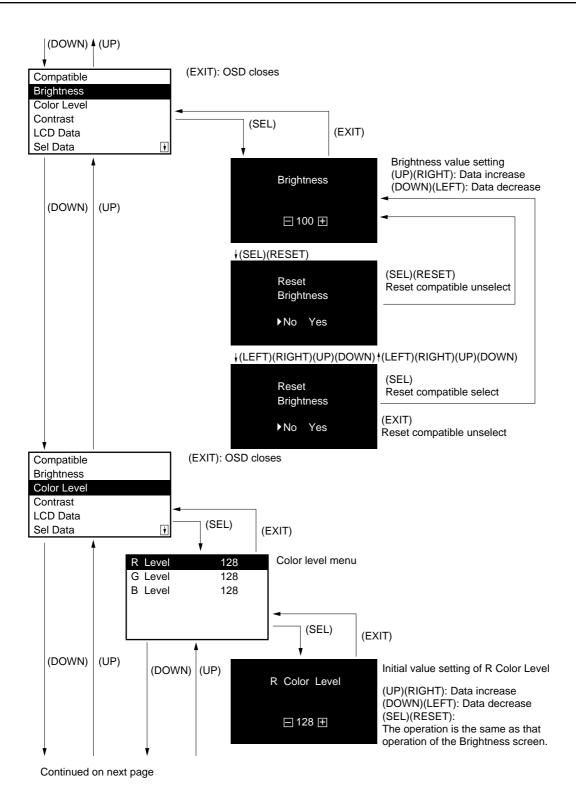
2: Evaluate compensation value of Par. Delay and Par. HD of No. 5 LCD Data.

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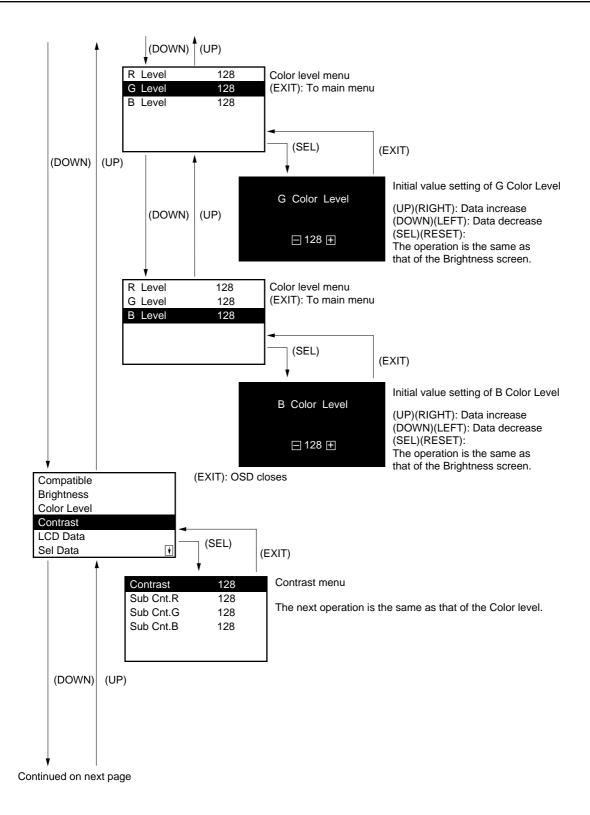
#### Flow chart of initial value change mode and OSD image



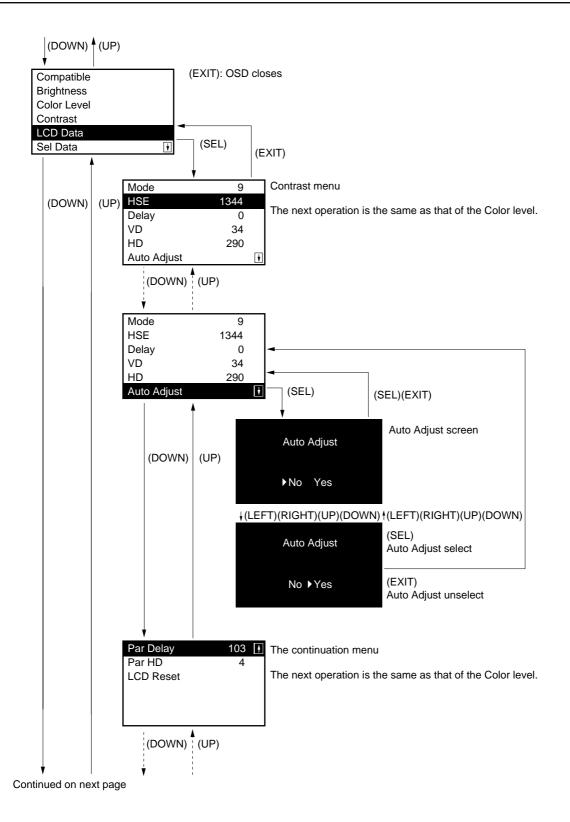




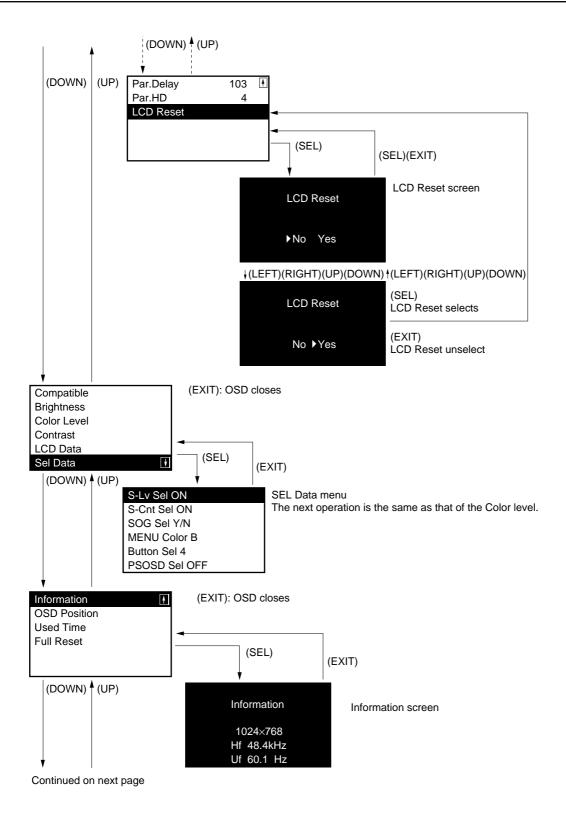
30



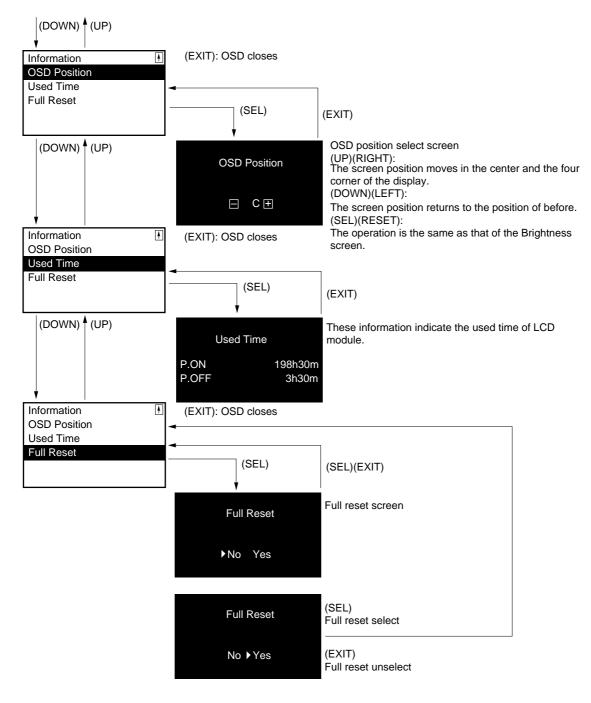








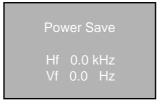




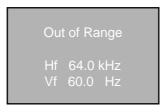
Remark: There is no Time out in the OSD function.

# (7) Information Display by OSD

(a) Power save: The frequency of horizontal and vertical synchronous signal is displayed for four seconds at a Power Save mode switchover.



- \* It is displayed when the horizontal synchronous signal frequency (Hf) and vertical synchronous signal frequency (Vf) are as follows:
  - (1) Hf < 10 KHz
  - (2) Vf < 40 Hz
  - (3) (1) and (2)
- \* It is not displayed when the "Power Save OSD no display" form is selected (See page 27, Initial Value Change Functions 6. Sel Data, PSOSD Sel).
- (b) Out of Range: Each frequency is displayed for four seconds when the horizontal or vertical synchronous signal is off from the corresponding range.



- \* It is displayed when the horizontal synchronous signal frequency (Hf) and vertical synchronous signal frequency (Vf) are as follows:
  - (1)  $10 \text{ KHz} \le \text{Hf} < 23 \text{ KHz} \text{ or } 61.5 \text{ KHz} < \text{Hf}$
  - (2)  $40 \text{ Hz} \le \text{Vf} < 50 \text{ Hz} \text{ or } 200 \text{ Hz} < \text{Vf}$
  - (3) (1) and (2) either Hf or Vf is in

It is not displayed when Power Save mode.

- \* Display or no display for Out of Range cannot be selected.
- (c) Disable: It is displayed for four seconds that the switches for adjustment are disabled.

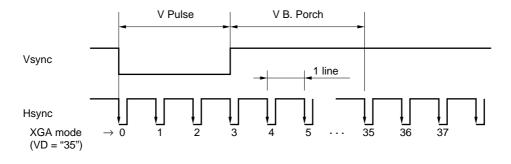


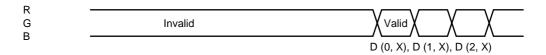


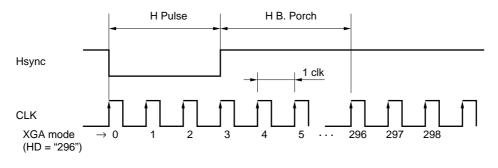
# (8) Input Signal and Display Position - XGA Standard Timing -

# Pixels

D (0, 0)	D (1, 0)	D (2, 0)	•••	D (1023, 0)
	D (1, 1)	D 2, 1)	•••	D (1023, 1)
D (0, 1)				
D (0, 2)	D (1, 2)	D (2, 2)	•••	D (1023, 2)
•	•	•		•
•	•	•		•
•	•	•		•
•	•	•		•
D (0, 767)	D (1, 767)	D (2, 767)	•••	D (1023, 767)









Note: The tda should be more than 4 ns.



#### **OPTICAL CHARACTERISTICS**

 $(Ta = 25^{\circ}C, V_{DD} = 12 \text{ V}, V_{DDB} = 12 \text{ V}, \text{Note 1})$ 

Item	Symbol	Condition	Min.	Тур.	Max.	Unit	Remark
Contrast ratio	CR	$\gamma$ = 2.2 viewing angle $\theta$ x+ = 0°, $\theta$ x- = 0°, $\theta$ y- = 0°, White/Black, at center	80	250	-	-	Note 2
Luminance	Lumax	White, at center	220	350	_	cd/m²	Note 3
Luminance uniformity	-	White	_	-	1.30	-	Note 4

#### Reference data

 $(Ta = 25^{\circ}C. V_{DD} = 12 \text{ V. } V_{DDB} = 12 \text{ V. } Note 1)$ 

	ı		(1a = 25  C,  VDD = 12  V,  VDDB = 12  V,  Note 1)				v, 140to 1)
Item	Symbol	Condition	Min.	Тур.	Max.	Unit	Remark
Contrast ratio	CR	Best contrast angle $\theta x + = 0^{\circ}$ , $\theta x - = 0^{\circ}$ , $\theta y - = 5^{\circ}$ White/Black, at center	_	300	_	_	Note 2
Color gamut	С	$\theta x + = 0^{\circ}, \ \theta x - = 0^{\circ}, \ \theta y + = 0^{\circ},$ $\theta y - = 0^{\circ}$ at center, to NTSC	35	40	_	%	-
Viewing angle range	<i>θ</i> x+	CR > 10, White/Black $\theta y = 0^{\circ}$	50	55	_	deg.	Note 5
	<i>ө</i> х–		50	55	_	deg.	
	<i>θ</i> y+	CR > 10, White/Black $\theta x = 0^{\circ}$	35	45	_	deg.	
	<i>ө</i> у–		30	45	_	deg.	
Response time	Ton	White (100%) to Black (10%)	-	15	40	ms	Note 6
	Toff	Black (0%) to White (90%)	_	40	50		
Luminance control range	_	Maximum luminance: 100%	-	20 to 100	-	%	-

Notes: 1. The luminance is measured after the module has been working for 20 minutes, with all pixels in white.

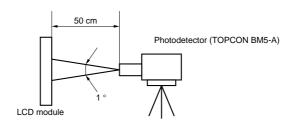
The typical value is measured after luminance saturation.

Display mode: VESA XGA-75 Hz RGB input voltage: 0.7 V p-p Contrast level: Default (128)

2. The contrast ratio is calculated by using the following formula.

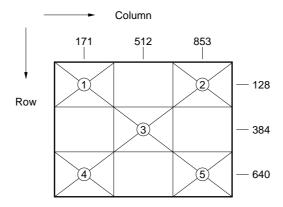
Contrast ratio (CR) = 
$$\frac{\text{Luminance with all pixels in white}}{\text{Luminance with all pixels in black}}$$

3. The luminance is measured after the module has been working for 20 minutes, with all pixels in white. The typical value is measured after luminance saturation, more than one hour after powered-on. The timing is SXGA standard mode, preset No. 9.

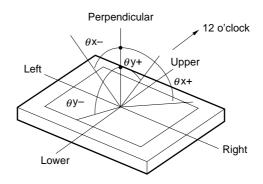


4. Luminance uniformity is calculated by using the following formula.

The luminance is measured at or near the five points shown below.

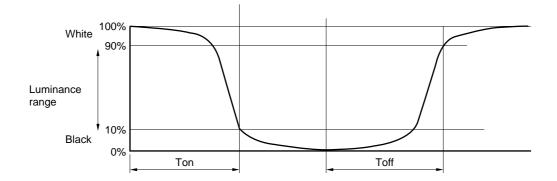


5. Definitions of viewing angles are as follows.



6. Definitions of response time is as follows.

The photodetector output signal is measured when the luminance changes from white to black or from black to white.



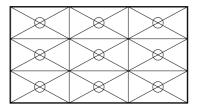


# **RELIABILITY TESTS**

Test item	Test conditions	Judgment
High temperature/humidity operation	$50 \pm 2^{\circ}$ C, RH = 85% 240 hours; Display data is black.	Note 1
Heat cycle (operation)	<1> 0°C ±3°C 1 hour 55°C ±3°C 1 hour <2> 50 cycles, 4 hours/cycle <3> Display data is black.	Note 1
Thermal shock (nonoperation)	<1> -20°C ±3°C 30 minutes 60°C ±3°C 30 minutes <2> 100 cycles <3> Temperature transition time is within 5 minutes.	Note 1
Vibration (nonoperation)	<1> 5-100 Hz, 19.6 m/s² (2G) <2> 1 minute/cycle, X, Y, Z direction <3> 50 times each direction	Notes 1, 2
Mechanical shock (nonoperation)	<1> 294 m/s² (30G), 11 ms X, Y, Z direction <2> 3 times each direction	Notes 1, 2
ESD (operation)	150 pF, 150 $\Omega$ , ±10 KV 9 places on a panel 10 times each place at one-second intervals	Note 1 Note 3
Dust (operation)	15 kinds of dust (JIS-Z 8901) Hourly 15 seconds stir, 8 times repeat	Note 1

**Notes: 1**. Display function is checked by the same condition as the LCD module outgoing inspection.

- 2. Physical damage.
- 3. Discharge points are shown in the following figure.





#### **GENERAL CAUTIONS**

Because the following figures and statements are very important, please be sure you understand their contents completely.



# **CAUTION**

This figure is a warning that you will get hurt and/or the module will be damaged if you make a mistake in operation.



This figure is a warning that you will get an electric shock if you make a mistake in operation.



This figure is a warning that you will get hurt if you make a mistake in operation.



# CAUTION



Do not touch an inverter on which there is a caution label while the LCD module is in operation, because of dangerous high voltage.

#### (1) Caution when taking out the module

Pick up the pouch only, when removing the module from a carrier box.

#### (2) Cautions for handling the module

- a) As the electrostatic discharges may break the LCD module, handle the LCD module with care against electrostatic discharges.
- b) As the LCD panel and backlight element are made from fragile glass material, impulse and pressure to the LCD module should be avoided.
- As the surface of polarizer is very soft and easily scratched, use a soft dry cloth without chemicals for cleaning.
- d) Do not pull the interface connectors in or out while the LCD module is operating.
- e) Put the module display side down on a flat horizontal plane.
- f) Handle connectors and cables with care.
- g) The torque to mounting screw should never exceed 0.392 N·m (4 kgf·cm).
- h) Do not give the stress to interface connectors. The module may become function deficiency by a contact defective and damages. Pay attention to handling at the time of matching connector connection and in the connection condition.

# (3) Cautions regarding atmosphere

- a) Dew drop atmosphere should be avoided.
- b) Do not store and/or operate the LCD module in a high-temperature and/or high-humidity atmosphere. Storage in an electro-conductive polymer packing pouch and in a relatively low-temperature atmosphere is recommended.
- c) This module uses cold cathode fluorescent lamps. The life time of the lamps is shortened conspicuously at low temperatures.
- d) Do not operate the LCD module in high magnetic field.

# (4) Cautions about the module characteristics

a) Do not apply a fixed pattern data signals to the LCD module at product aging. Applying a fixed pattern for a long time may cause image sticking.

#### (5) Other cautions

- a) Do not disassemble and/or reassemble the LCD module.
- b) Do not readjust variable resistor or switch, etc.
- c) When returning the module for repair, etc., please pack the module so it will not be broken. We recommend using the original shipping packages.

The liquid crystal display has the following specific characteristics. These are neither defects nor malfunctions.

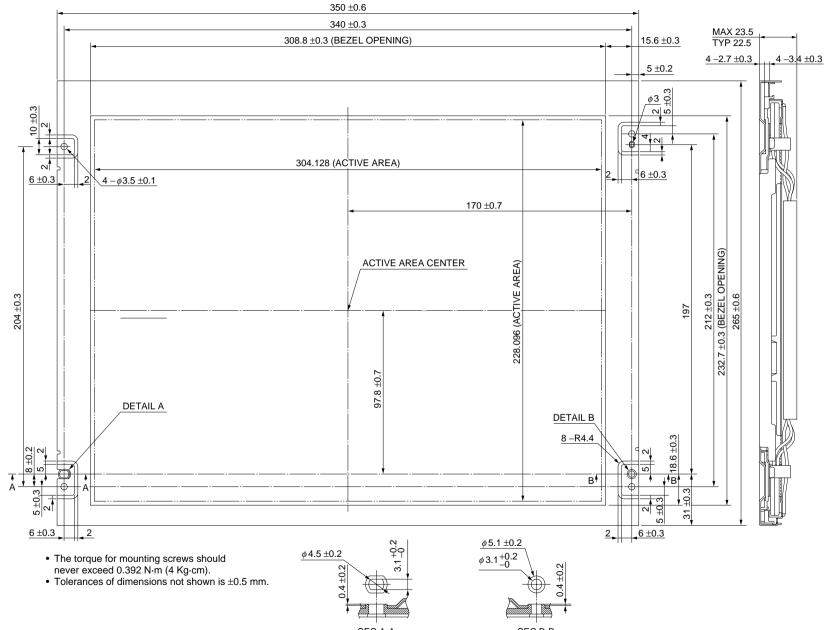
The display condition of the LCD module may be affected by the ambient temperature. The LCD module has cold cathode tube for backlight. Optical characteristics, like luminance or uniformity, will change over time.

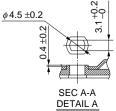
Uneven brightness and/or small spots may be noticed, depending on different display patterns.

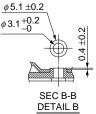
# **OUTLINE DRAWINGS**

NEC.









Rear View (Unit in mm)

11.8

13.8

#### Max23.5 TYP22.5 Max23.5 24 4-3.4 ±0.3 4-2.7 ±0.3 171.2 Printed Circuit Board FPC 31 62.8 ±1 54 130.6 ±1 TYP22.5 48.6 MODEL NL:102768C30-04D SERIAL: B0410230\* PANTEL: A100802018301 NEC 00. 4. 10/ E170632 MADE IN JAPAN 54.5 ±1 20 81.1 0.3 Inverter 9.7 153±1 (B) 199 ±1 233 11 CN201 see DETAIL A 139.8 ±1 28 7.6 7.6 9 108.7 ±1 4 1.5 Material Information Light guide:>PMMA< 0 0.3 35.7 A 1 0 0 0 A 7 6 3 0 C 3 13.5

80

13.4

104

16

• The torque for mounting screws should never exceed 0.392 N·m (4 Kg·cm).

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• Tolerances of the dimensions not shown are ±0.5 mm.

15.7

DETAIL A

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While NEC Corporation has been making continuous effort to enhance the reliability of its electronic components, the possibility of defects cannot be eliminated entirely. To minimize risks of damage or injury to persons or property arising from a defect in an NEC electronic component, customers must incorporate sufficient safety measures in its design, such as redundancy, fire-containment, and anti-failure features. NEC devices are classified into the following three quality grades:

"Standard," "Special," and "Specific". The Specific quality grade applies only to devices developed based on a customer designated "quality assurance program" for a specific application. The recommended applications of a device depend on its quality grade, as indicated below. Customers must check the quality grade of each device before using it in a particular application.

Standard: Computers, office equipment, communications equipment, test and measurement equipment, audio and visual equipment, home electronic appliances, machine tools, personal electronic equipment and industrial robots

Special: Transportation equipment (automobiles, trains, ships, etc.), traffic control systems, anti-disaster systems, anti-crime systems, safety equipment and medical equipment (not specifically designed for life support)

Specific: Aircrafts, aerospace equipment, submersible repeaters, nuclear reactor control systems, life support systems or medical equipment for life support, etc.

The quality grade of NEC devices is "Standard" unless otherwise specified in NEC's Data Sheets or Data Books. If customers intend to use NEC devices for applications other than those specified for Standard quality grade, they should contact an NEC sales representative in advance.

#### (Note)

- (1) "NEC" as used in this statement means NEC Corporation and also includes its majorityowned subsidiaries.
- (2) "NEC electronic component products" means any electronic component product developed or manufactured by or for NEC (as defined above).

DE0202