# **Single Inverter**

The NL17SV04 is an ultra-high performance inverter manufactured in 0.35  $\mu$ m technology, with excellent performance down to 0.9 V. This device is ideal for extremely high speed and high-drive applications. Additionally, limitations of board space are no longer a constraint. The very small SOT-553 makes this device fit most tight designs and spaces.

#### Features

- Extremely High Speed:  $t_{PD} = 1.0 \text{ ns} (Typ) @ V_{CC} = 3.3 \text{ V}$
- Designed for 0.9 to 3.6 V Operation
- Overvoltage Tolerance (OVT)\* Input Pins Permits Logic Translation
- Balanced  $\pm 24$  mA Output Drive @ V<sub>CC</sub> 3.3 Volts
- Near Zero Static Supply Current
- Ultra-Tiny SOT-553 5 Pin Package only 1.6 x 1.6 x 0.6 mm
- All Devices in Package SOT-553 are Inherently Pb-Free\*\*

#### **Typical Applications**

- Cellular
- Digital Camera
- PDA
- Digital Video

#### Industry Leadership

• Functionally Similar to NC7SV04 and SN74AUC1G04



Figure 1. Logic Symbol

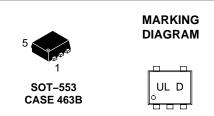
#### FUNCTION TABLE

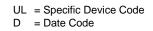
Output Y
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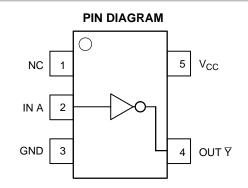


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#### PIN ASSIGNMENT

PIN #	FUNCTION
1	NC
2	IN A
3	GND
4	OUT Y
5	V <sub>CC</sub>

#### ORDERING INFORMATION

Device	Package	Shipping†
NL17SV04XV5T2	SOT-553 (Pb-Free)	4000 Tape & Reel (178 mm)

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

\*Overvoltage Tolerance (OVT) enables input pins to function outside (higher) of their operating voltages, with no damage to the devices or to signal integrity.

\*\*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

#### **MAXIMUM RATINGS**

Symbol	Rating		Value	Unit
V <sub>CC</sub>	DC Supply Voltage		-0.5 to + 4.6	V
VI	DC Input Voltage		-0.5 to + 4.6	V
Vo	DC Output Voltage		–0.5 to V <sub>CC</sub> +0.5	V
I <sub>IK</sub>	DC Input Diode Current V <sub>I</sub> < G	SND	±50	mA
Ι <sub>ΟΚ</sub>	DC Output Diode Current $V_{O} = G$ $V_{O} = V$	SND √ <sub>CC</sub>	-50 +50	mA
Ι <sub>Ο</sub>	DC Output Sink Current		±50	mA
Icc	DC Supply Current per Supply Pin		±50	mA
I <sub>GND</sub>	DC Ground Current per Ground Pin		±50	mA
T <sub>STG</sub>	Storage Temperature Range		- 65 to +150	°C
ΤL	Lead Temperature, 1.0 mm from Case for 10 seconds		260	°C
TJ	Junction Temperature Under Bias		+150	°C
$\theta_{JA}$	Thermal Resistance (Note 1)		250	°C/W
PD	Power Dissipation in Still Air at 85°C		250	mW
MSL	Moisture Sensitivity		Level 1	
F <sub>R</sub>	Flammability Rating Oxygen index: 28 to	o 34	UL 94 V–0 @ 0125 in	
$V_{ESD}$	ESD Withstand Voltage Human Body Model (Not Machine Model (Not		4000 150	V

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

1. Measured with minimum pad spacing on an FR4 board, using 10 mm-by-1 inch, 2 ounce copper trace no air flow.

Tested to EIA/JESD22–A114–A.
Tested to EIA/JESD22–A115–A.

#### **RECOMMENDED OPERATING CONDITIONS**

Symbol	Parameter		Min	Max	Unit
V <sub>CC</sub>	Positive DC Supply Voltage		0.9	3.6	V
V <sub>IN</sub>	Digital Input Voltage		0	3.6	V
Vout	Output Voltage		0	V <sub>CC</sub>	V
I <sub>OH</sub> /I <sub>OL</sub>				$\pm 24$ $\pm 18$ $\pm 6$ $\pm 4$ $\pm 2$ $\pm 0.1$	mA
t <sub>A</sub>	Operating Temperature Range. All Package Types		-40	+85	°C
t <sub>r</sub> , t <sub>f</sub>	Input Rise or Fall Time V	<sub>CC</sub> = 3.3V ± 0.3 V	0	10	nS/V

#### **DEVICE JUNCTION TEMPERATURE VERSUS TIME TO 0.1% BOND FAILURES**

Junction Temperature °C	Time, Hours	Time, Years
80	1,032,200	117.8
90	419,300	47.9
100	178,700	20.4
110	79,600	9.4
120	37,000	4.2
130	17,800	2.0
140	8,900	1.0

#### DC CHARACTERISTICS- Digital Section (Voltages Referenced to GND)

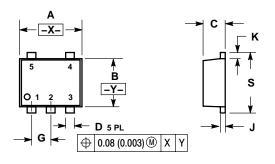
			$T_A = 25^{\circ}C$ $T_A = -40 \text{ to } 85^{\circ}$		to 85°C			
Symbol	Parameter	Condition	V <sub>CC</sub>	Min	Мах	Min	Max	Unit
VIH	High Level		0.90	0.65 x V <sub>CC</sub>		0.65 x V <sub>CC</sub>		V
	Input Voltage		$1.10 \le = V_{CC} \le 1.30$	0.65 x V <sub>CC</sub>		0.65 x V <sub>CC</sub>		
			$1.40 \le V_{CC} \le 1.60$	$0.65 \times V_{CC}$		$0.65 \times V_{CC}$		
			$1.65 \le V_{CC} \le 1.95$	$0.65 \times V_{CC}$		$0.65 \times V_{CC}$		
			$2.30 \le V_{CC} \le 2.70$	1.6		1.6		
			$2.70 \le V_{CC} \le 3.60$	2.0		2.0		
VIL	Low Level		0.90		$0.35 \times V_{CC}$		0.35 x V <sub>CC</sub>	V
	Input Voltage		$1.10 \le V_{CC} \le 1.30$		0.35 x V <sub>CC</sub>		0.35 x V <sub>CC</sub>	
			$1.40 \le V_{CC} \le 1.60$		0.35 x V <sub>CC</sub>		0.35 x V <sub>CC</sub>	
			$1.65 \le V_{CC} \le 1.95$ $2.30 \le V_{CC} \le 2.70$		0.35 x V <sub>CC</sub> 0.7		0.35 x V <sub>CC</sub> 0.7	
			$2.70 \le V_{CC} \le 2.70$ $2.70 \le V_{CC} \le 3.60$		0.8		0.8	
V <sub>OH</sub>	High Level	I <sub>OH</sub> = –100 μA	0.90	V <sub>CC</sub> – 0.1		V <sub>CC</sub> – 0.1		V
VОН	Output Voltage	10H = -100 μΛ	$1.10 \le V_{CC} \le 1.30$	V <sub>CC</sub> – 0.1 V <sub>CC</sub> – 0.1		V <sub>CC</sub> – 0.1 V <sub>CC</sub> – 0.1		v
			$1.40 \le V_{CC} \le 1.60$	$V_{CC} - 0.2$		V <sub>CC</sub> – 0.2		
			$1.65 \le V_{CC} \le 1.95$	V <sub>CC</sub> – 0.2		V <sub>CC</sub> – 0.2		
			$2.30 \le V_{CC} \le 2.70$	V <sub>CC</sub> – 0.2		V <sub>CC</sub> – 0.2		
			$2.70 \leq V_{CC} \leq 3.60$	$V_{CC} - 0.2$		V <sub>CC</sub> – 0.2		
		I <sub>OH</sub> = -2.0 mA	$1.10 \le V_{CC} \le 1.30$	$0.75 \times V_{CC}$		$0.75 \times V_{CC}$		
		I <sub>OH</sub> = -4.0 mA	$1.40 \le V_{CC} \le 1.60$	$0.75 \times V_{CC}$		0.75 x V <sub>CC</sub>		
		I <sub>OH</sub> = -6.0 mA	$1.65 \le V_{CC} \le 1.95$	1.25		1.25		
			$2.30 \le V_{CC} \le 2.70$	2.0		2.0		
		I <sub>OH</sub> = -12 mA	$2.30 \leq V_{CC} \leq 2.70$	1.8		1.8		
			$2.70 \le V_{CC} \le 3.60$	2.2		2.2		
		I <sub>OH</sub> = –18 mA	$2.30 \le V_{CC} \le 2.70$	1.7		1.7		
			$2.70 \le V_{CC} \le 3.60$	2.4		2.4		
		I <sub>OH</sub> = -24 mA	$2.70 \le V_{CC} \le 3.60$	2.2		2.2		
V <sub>OL</sub>	Low Level Output Voltage	I <sub>OL</sub> = 100 μA	0.90		0.1		0.1	V
	Output voltage		$1.10 \le V_{CC} \le 1.30$		0.1		0.1	
			$1.40 \le V_{CC} \le 1.60$ $1.65 \le V_{CC} \le 1.95$		0.2 0.2		0.2 0.2	
			$2.30 \le V_{CC} \le 2.70$		0.2		0.2	
			$2.70 \le V_{CC} \le 3.60$		0.2		0.2	
		I <sub>OL</sub> = 2.0 mA	$1.10 \le V_{CC} \le 1.30$		0.25 x V <sub>CC</sub>		0.25 x V <sub>CC</sub>	
		I <sub>OL</sub> = 4.0 mA	$1.40 \le V_{CC} \le 1.60$		0.25 x V <sub>CC</sub>		0.25 x V <sub>CC</sub>	
		I <sub>OL</sub> = 6.0 mA	$1.65 \le V_{CC} \le 1.95$		0.3		0.3	
		I <sub>OL</sub> = 12 mA	$2.30 \le V_{CC} \le 2.70$		0.4		0.4	
			$2.70 \le V_{CC} \le 3.60$		0.4		0.4	
		I <sub>OL</sub> = 18 mA	$2.30 \le V_{CC} \le 2.70$		0.6		0.6	
			$2.70 \le V_{CC} \le 3.60$		0.4		0.4	
		I <sub>OL</sub> = 24 mA	$2.70 \le V_{CC} \le 3.60$		0.55		0.55	
I <sub>IN</sub>	Input Leakage Current	0 = V <sub>I</sub> = 3.6 V	0.90 to 3.60		±0.1		±0.9	μA
I <sub>OFF</sub>	Power Off Leakage Current		0		1		5	μA
I <sub>CC</sub>	Quiescent Supply Current	$V_{I} = V_{CC}$ or GND	0.90 to 3.60		0.9		5	μA

		-40°	–40°C 25°C		25°C 85°C			°C	
Symbol	Parameter	Condition	V <sub>CC</sub>	Min	Тур	Max	Min	Max	Unit
T <sub>PHL,</sub>	Propagation Delay	$C_L$ = 15 pF, $R_L$ = 1.0 M $\Omega$	0.90		12				nS
T <sub>PLH</sub>		$C_L$ = 15 pF, $R_L$ = 2.0 k $\Omega$	$\begin{array}{l} 1.10  \leq  V_{CC}  \leq  1.30 \\ 1.40  \leq  V_{CC}  \leq  1.60 \end{array}$	2.0 1.0	6.0 3.2	9.0 5.1	1.0 1.0	13.9 6.0	nS
		$C_L$ = 30 pF, $R_L$ = 500 k $\Omega$	$\begin{array}{l} 1.65 \leq V_{CC} \leq 1.95 \\ 2.30 \leq V_{CC} \leq 2.70 \\ 2.70 \leq V_{CC} \leq 3.60 \end{array}$	1.0 0.8 0.7	2.0 1.2 1.0	4.2 2.7 2.3	1.0 0.7 0.6	5.2 3.4 2.8	nS
C <sub>IN</sub>	Input Capacitance		0		2.0				pF
C <sub>OUT</sub>	Output Capacitance		0		4.5				pF
C <sub>PD</sub>	Power Dissipation Capacitance	$V_I = 0 V \text{ or } V_{CC}$ F = 10 MHz	0.90 to 3.60		20				pF

#### **AC CHARACTERISTICS** (Input $t_r = t_f = 3.0 \text{ nS}$ )

#### PACKAGE DIMENSIONS

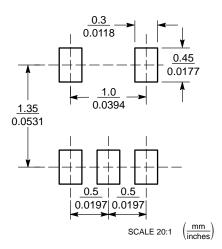
SOT-553 5-LEAD PACKAGE CASE 463B-01 **ISSUE A** 



NOTES: 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. 2. CONTROLLING DIMENSION: MILLIMETERS 3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.

	MILLIN	IETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
Α	1.50	1.70	0.059	0.067
В	1.10	1.30	0.043	0.051
С	0.50	0.60	0.020	0.024
D	0.17	0.27	0.007	0.011
G	0.50	BSC	0.020	BSC
J	0.08	0.18	0.003	0.007
K	0.10	0.30	0.004	0.012
S	1.50	1.70	0.059	0.067

**SOLDERING FOOTPRINT\*** 



\*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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