

DATA SHEET

MOS FIELD EFFECT TRANSISTOR NP70N04MUG

SWITCHING **N-CHANNEL POWER MOS FET**

DESCRIPTION

The NP70N04MUG is N-channel MOS Field Effect Transistor designed for high current switching applications.

ORDERING INFORMATION

PART NUMBER	LEAD PLATING	PACKING	PACKAGE
NP70N04MUG-S18-AY Note	Pure Sn (Tin)	Tube 50 p/tube	TO-220 (MP-25K) typ. 1.9 g

Note Pb-free (This product does not contain Pb in the external electrode).

FEATURES

• Super low on-state resistance

 $R_{DS(on)} = 5.0 \text{ m}\Omega \text{ MAX.}$ (Vgs = 10 V, ID = 35 A)

• Channel temperature 175 degree rated

ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

Drain to Source Voltage (VGs = 0 V)	VDSS	40	V
Gate to Source Voltage (VDs = 0 V)	Vgss	±20	V
Drain Current (DC) (Tc = 25°C)	ID(DC)	±70	А
Drain Current (pulse)	ID(pulse)	±280	А
Total Power Dissipation (Tc = 25°C)	Рт1	115	W
Total Power Dissipation ($T_A = 25^{\circ}C$)	PT2	1.8	W
Channel Temperature	Tch	175	°C
Storage Temperature	Tstg	–55 to +175	°C
Repetitive Avalanche Current Note2	lar	37	А
Repetitive Avalanche Energy Note2	Ear	137	mJ

Notes 1. PW \leq 10 μ s, Duty Cycle \leq 1%

2. T_{ch} \leq 150°C, V_{DD} = 20 V, R_G = 25 Ω , V_{GS} = 20 \rightarrow 0 V, L = 100 μ H

THERMAL RESISTANCE

Channel to Case Thermal Resistance	Rth(ch-C)	1.30	°C/W
Channel to Ambient Thermal Resistance	Rth(ch-A)	83.3	°C/W

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The mark <R> shows major revised points.

(TO-220)



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The revised points can be easily searched by copying an "<R>" in the PDF file and specifying it in the "Find what:" field.

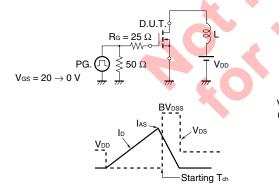
ELECTRICAL CHARACTERISTICS (TA = 25°C)

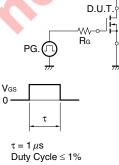
CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	IDSS	V _{DS} = 40 V, V _{GS} = 0 V			1	μA
Gate Leakage Current	lgss	V _{GS} = ±20 V, V _{DS} = 0 V			±100	nA
Gate to Source Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	2.0		4.0	V
Forward Transfer Admittance Note	y _{fs}	V _{DS} = 5 V, I _D = 35 A	25	49		S
Drain to Source On-state Resistance Note	RDS(on)	V _{GS} = 10 V, I _D = 35 A		4.0	5.0	mΩ
Input Capacitance	Ciss	V _{DS} = 25 V,		4900		pF
Output Capacitance	Coss	V _{GS} = 0 V,		480		pF
Reverse Transfer Capacitance	Crss	f = 1 MHz		310		pF
Turn-on Delay Time	td(on)	V _{DD} = 20 V, I _D = 35 A,		25		ns
Rise Time	tr	V _{GS} = 10 V,		18		ns
Turn-off Delay Time	td(off)	Rg = 0 Ω	\mathbf{O}	63		ns
Fall Time	tr			12		ns
Total Gate Charge	QG	V _{DD} = 32 V,		90		nC
Gate to Source Charge	Q _{GS}	Vgs = 10 V,		21		nC
Gate to Drain Charge	Qgd	Io = 70 A		31		nC
Body Diode Forward Voltage Note	V _{F(S-D)}	I⊧ = 70 A, V₀s = 0 V		0.96	1.5	V
Reverse Recovery Time	trr	I⊧ = 70 A, V _{GS} = 0 V,		37		ns
Reverse Recovery Charge	Qrr	di/dt = 100 A/ <i>µ</i> s		42		nC

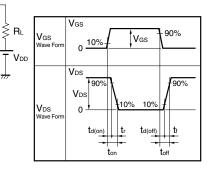
Note Pulsed

TEST CIRCUIT 1 AVALANCHE CAPABILITY

TEST CIRCUIT 2 SWITCHING TIME



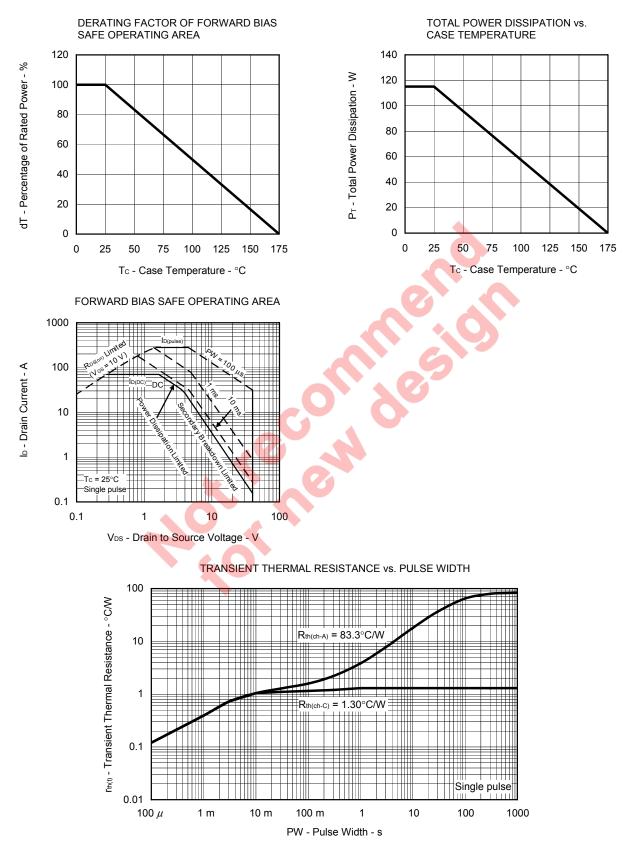




TEST CIRCUIT 3 GATE CHARGE

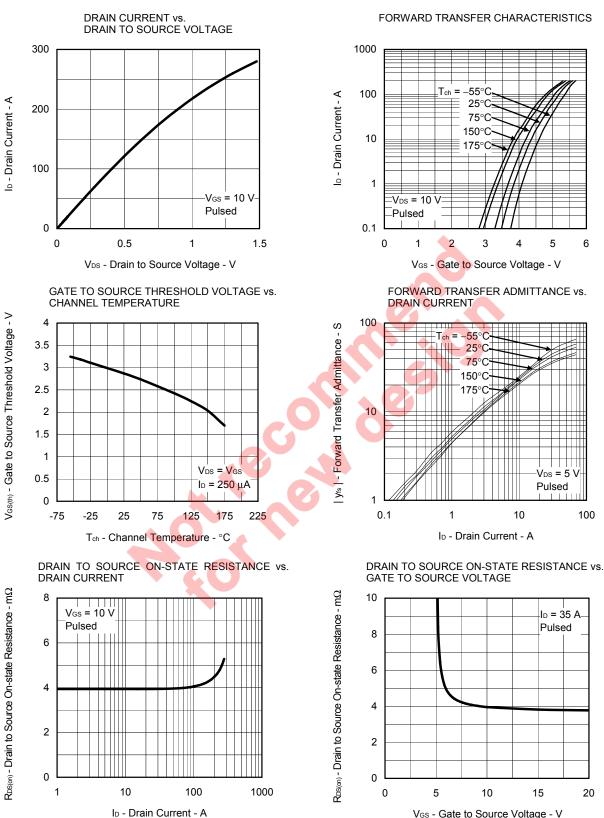
 $PG. \bigoplus_{m=1}^{D.U.T.} \underbrace{\downarrow_{G} = 2 \ mA}_{m} \underbrace{\downarrow_{F}}_{m} \underbrace{\downarrow_{F}}_{m}$

TYPICAL CHARACTERISTICS (TA = 25°C)



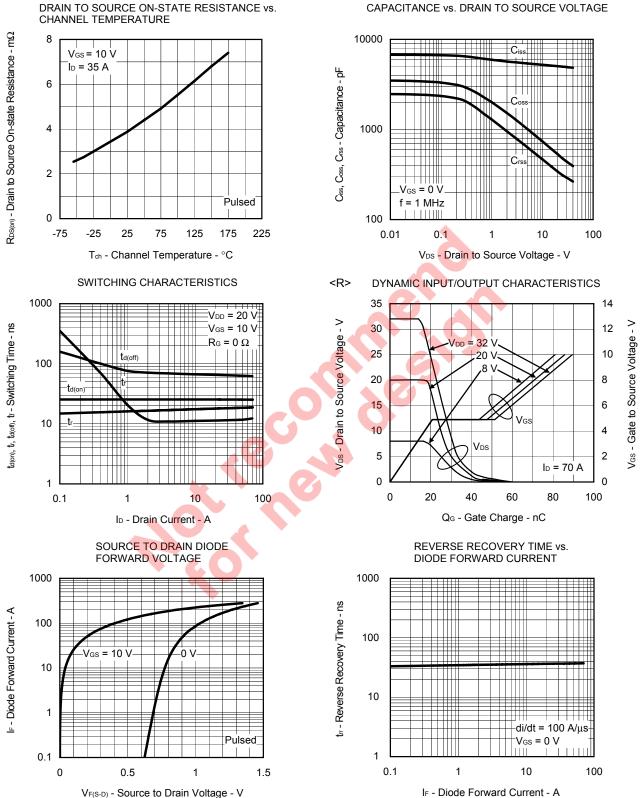
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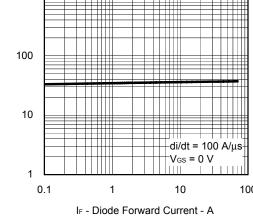
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VGS - Gate to Source Voltage - V

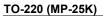
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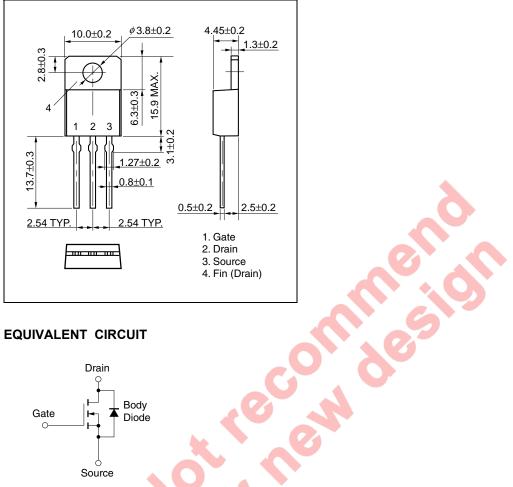




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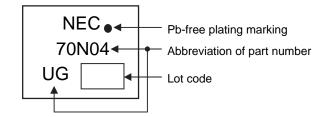
PACKAGE DRAWING (Unit: mm)





Remark Strong electric field, when exposed to this device, can cause destruction of the gate oxide and ultimately degrade the device operation. Steps must be taken to stop generation of static electricity as much as possible, and quickly dissipate it once, when it has occurred.

MARKING INFORMATION



RECOMMENDED SOLDERING CONDITIONS

The NP70N04MUG should be soldered and mounted under the following recommended conditions.

For soldering methods and conditions other than those recommended below, please contact an NEC Electronics sales representative.

For technical information, see the following website.

Semiconductor Device Mount Manual (http://www.necel.com/pkg/en/mount/index.html)

Soldering Method	Soldering Conditions	Recommended Condition Symbol
Wave soldering MP-25K	Maximum temperature (Solder temperature): 260°C or below Time: 10 seconds or less Maximum chlorine content of rosin flux: 0.2% (wt.) or less	THDWS
Partial heating MP-25K	Maximum temperature (Pin temperature): 350°C or below Time (per side of the device): 3 seconds or less Maximum chlorine content of rosin flux: 0.2% (wt.) or less	P350

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Caution Do not use different soldering methods together (except for partial heating).

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