Schottky Barrier Rectifier Trench-based, High Performance

NRTS30100MFS

This Trench Schottky rectifier is high performance device in SO-8 FL package. The lower forward voltage, less leakage current, and small junction capacitance are suitable to high switching frequency high density DC to DC conversion application. Offering higher avalanche energy capability for Oring or reverse protection application. The SO-8 FL package provides an excellent thermal performance, less land area of board space, and low profile.

Features

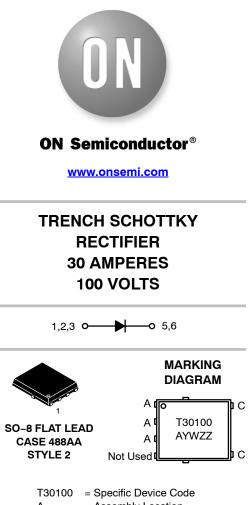
- Lower Forward Voltage Drop
- Less Leakage Current in High Temperature
- Small Junction Capacitance for High Switching Frequency
- Higher Avalanche Energy Capability
- 175°C Operating Junction Temperature
- Good Alternative Solution of SMC and DPAK Package
- Small Footprint Land Area: 31.2 mm²
- Low Profile Maximum Height of 1.1 mm
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

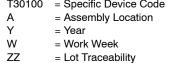
Mechanical Characteristics:

- Case: Molded Epoxy
- Epoxy Meets UL 94 V-0 @ 0.125 in
- Weight: 95 mg (Approximately)
- Lead and Mounting Surface Temperature for Soldering Purposes: 260°C Maximum for 10 Seconds
- MSL 1

Applications

- High Switching Frequency DC/DC Converter
- 2nd Rectifier
- Freewheeling Diode used with Inductive Load
- Oring / Reverse Protection





ORDERING INFORMATION

Device	Package	Shipping [†]
NRTS30100MFST3G	SO-8 FL (Pb-Free)	5000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

MAXIMUM RATINGS

Rating		Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage		V _{RRM} V _{RWM} V _R	100	V
Continuous Forward Current (T _C = 163°	C, DC)	I _{F(DC)}	30	А
Peak Repetitive Forward Current (T _C =	161°C, Square Wave, Duty = 0.5)	I _{FRM}	60	А
Non-Repetitive Peak Surge Current	Sinusoidal Halfwave, 8.3 ms	I _{FSM}	350	А
	Square wave, 1 ms		550	
	Square wave, 100 µs		1200	
Non-Repetitive Avalanche Energy (T_J =	25°C)	E _{AS}	450	mJ
Storage Temperature Range		T _{stg}	-65 to +175	°C
Operating Junction Temperature Range	(Note 1)	TJ	-55 to +175	°C
ESD Rating (Human Body Model)			3B	
ESD Rating (Machine Model)			M4	

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected. 1. The heat generated must be less than the thermal conductivity from Junction-to-Ambient $dP_D/dT_J < 1/R_{\theta JA}$

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{ hetaJA}$	56	°C/W
Thermal Resistance, Junction-to-Case Bottom (Note 2)	$R_{ heta JCB}$	0.65	°C/W
Thermal Characterization, Junction-to-Case Top (Note 2)	ΨЈСТ	3.72	°C/W
Thermal Characterization, Junction-to-Lead of Cathode (Note 2)	ΨJLC	1.44	°C/W

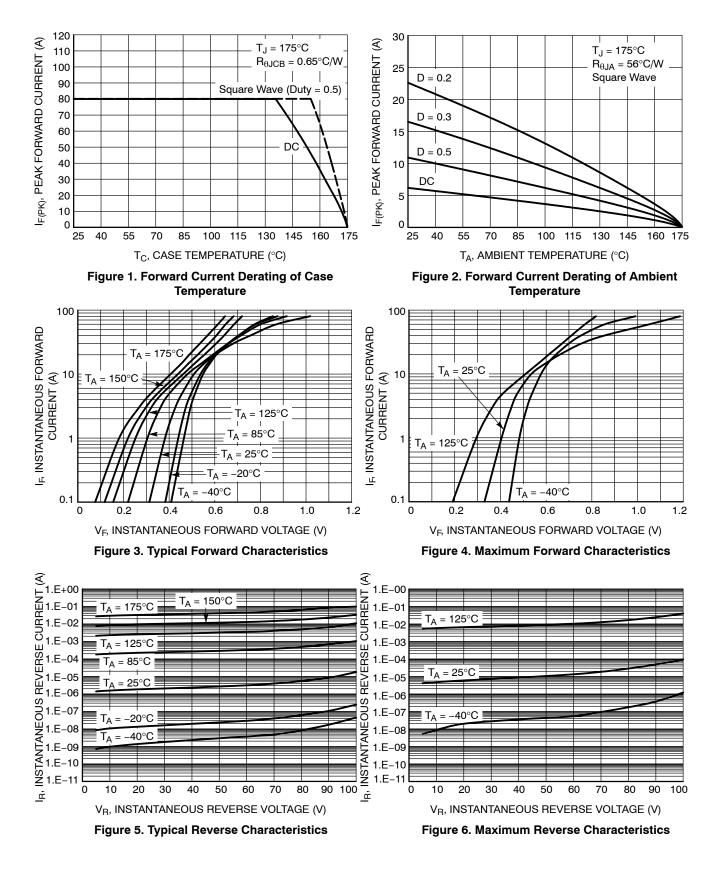
2. Assume 600 mm², 1 oz. copper bond pad on a FR4 board.

ELECTRICAL CHARACTERISTICS

Characteristic	Symbol	Тур	Max	Unit
Instantaneous Forward Voltage	V _F			V
(I _F = 15 A, T _J = 25°C)		0.56	-	
(I _F = 15 A, T _J = 125°C)		0.52	-	
(I _F = 30 A, T _J = 25°C)		0.68	0.76	
(I _F = 30 A, T _J = 125°C)		0.61	0.68	
Instantaneous Reverse Current	I _R			
(V _R = Rated DC Voltage, $T_J = 25^{\circ}C$)		18	100	μΑ
(V _R = Rated DC Voltage, T_J = 125°C)		11	40	mA
Junction Capacitance	CJ			рF
(V _R = 1 V, T _J = 25°C, f = 1 MHz)		2540	-	

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions. 3. Pulse Test: Pulse Width = $300 \ \mu$ s, Duty Cycle $\le 2.0\%$.

TYPICAL CHARACTERISTICS



TYPICAL CHARACTERISTICS

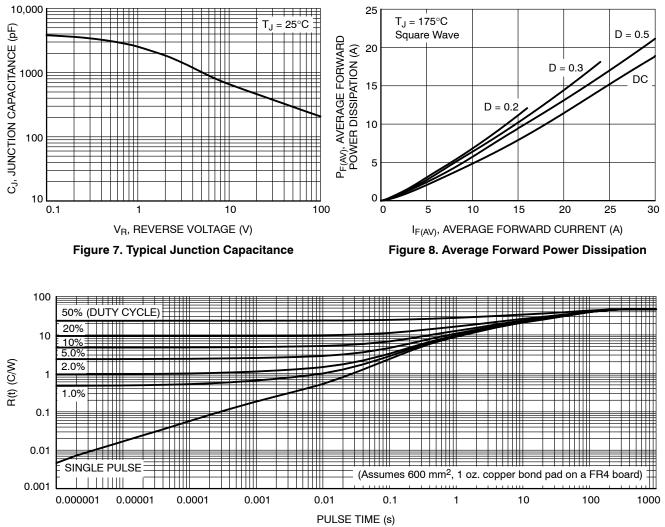
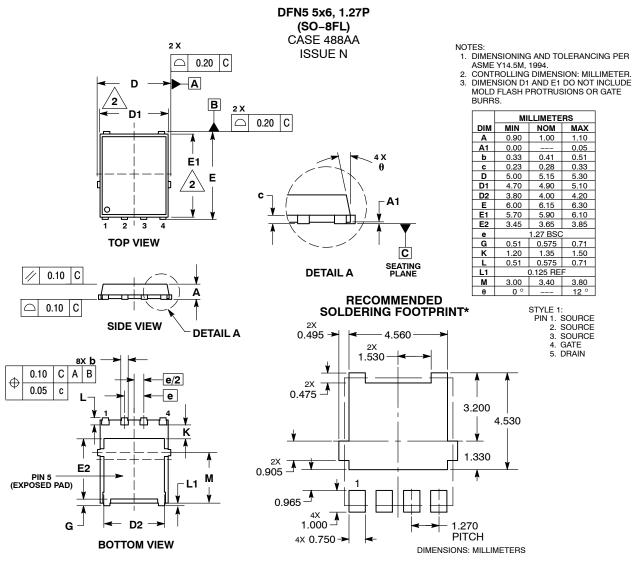


Figure 9. Typical Thermal Characteristics, Junction-to-Ambient

PACKAGE DIMENSIONS



*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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