SWITCHMODE Power Rectifiers

DPAK-3 Surface Mount Package

These state-of-the-art devices are designed for use in switching power supplies, inverters and as free wheeling diodes.

Features

- Extremely Fast Switching
- Extremely Low Forward Drop
- Platinum Barrier with Avalanche Guardrings
- NRVBD Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb-Free and are RoHS Compliant*

Mechanical Characteristics:

- Case: Epoxy, Molded
- Weight: 0.4 Gram (Approximately)
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead and Mounting Surface Temperature for Soldering Purposes: 260°C Max. for 10 Seconds
- ESD Ratings:
 - Machine Model = C
 - Human Body Model = 3B



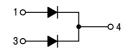
ON Semiconductor®

http://onsemi.com

SCHOTTKY BARRIER RECTIFIERS 6.0 AMPERES, 20 – 60 VOLTS



DPAK CASE 369C



MARKING DIAGRAM



 $\begin{array}{lll} Y &= Year \\ WW &= Work Week \\ B6x0T &= Device Code \\ x &= 2, 3, 4, 5, or 6 \\ G &= Pb-Free Package \end{array}$

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 5 of this data sheet.

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

MAXIMUM RATINGS

| | | MBRD/NRVBD/SBR | | | | | |
|--|--|----------------|-------|-------|-------|-------|------|
| Rating | Symbol | 620CT | 630CT | 640CT | 650CT | 660CT | Unit |
| Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage | V _{RRM} V _{RWM} V _R | 20 | 30 | 40 | 50 | 60 | V |
| Average Rectified Forward Current $T_{C} = 130^{\circ}C$ (Rated V_{R}) Per Diode Per Device | I _{F(AV)} | 3 6 | | | | A | |
| Peak Repetitive Forward Current, T _C = 130°C (Rated V _R , Square Wave, 20 kHz) Per Diode | I _{FRM} | 6 | | | | A | |
| Nonrepetitive Peak Surge Current – (Surge applied at rated load conditions halfwave, single phase, 60 Hz) | I _{FSM} | 75 | | | | А | |
| Peak Repetitive Reverse Surge Current (2 µs, 1 kHz) | I _{RRM} | 1 | | | А | | |
| Operating Junction Temperature (Note 1) | TJ | -65 to +175 | | | | °C | |
| Storage Temperature | T _{stg} | -65 to +175 | | | | °C | |
| Voltage Rate of Change (Rated V _R) | dv/dt | 10,000 | | | V/μs | | |

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

1. The heat generated must be less than the thermal conductivity from Junction-to-Ambient: $dP_D/dT_J < 1/R_{\theta JA}$.

THERMAL CHARACTERISTICS PER DIODE

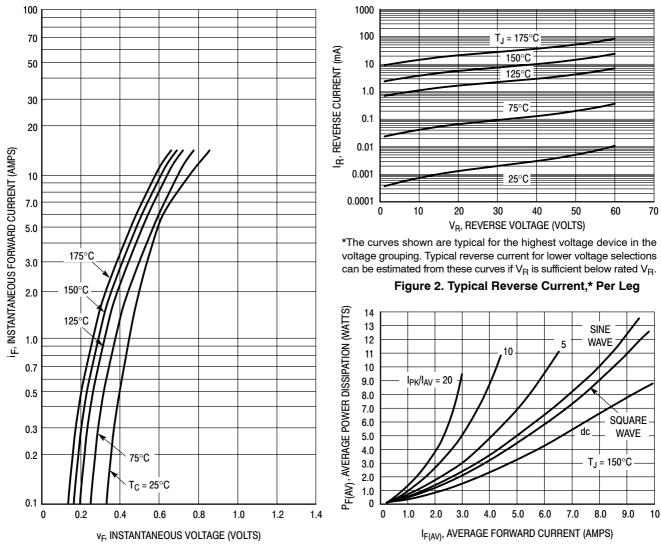
| Characteristic | Symbol | Value | Unit |
|--|-----------------|-------|------|
| Maximum Thermal Resistance, Junction-to-Case | $R_{\theta JC}$ | 6 | °C/W |
| Maximum Thermal Resistance, Junction-to-Ambient (Note 2) | $R_{	hetaJA}$ | 80 | °C/W |

2. Rating applies when surface mounted on the minimum pad size recommended.

ELECTRICAL CHARACTERISTICS PER DIODE

| Characteristic | Symbol | Value | Unit |
|---|----------------|----------------------------|------|
| | V _F | 0.7 0.65 0.9 0.85 | V |
| Maximum Instantaneous Reverse Current (Note 3) (Rated dc Voltage, $T_C = 25^{\circ}C$) (Rated dc Voltage, $T_C = 125^{\circ}C$) | İR | 0.1 15 | mA |

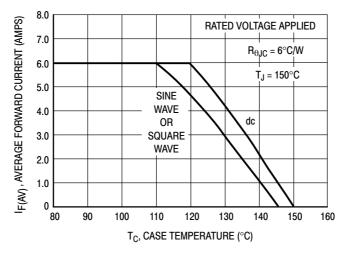
3. Pulse Test: Pulse Width = 300 μ s, Duty Cycle \leq 2.0%.



TYPICAL CHARACTERISTICS

Figure 1. Typical Forward Voltage, Per Leg

Figure 3. Average Power Dissipation, Per Leg



TYPICAL CHARACTERISTICS

Figure 4. Current Derating, Case, Per Leg

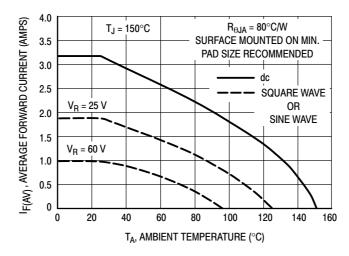
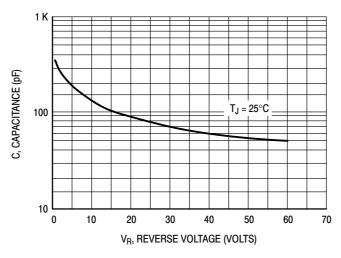


Figure 5. Current Derating, Ambient, Per Leg





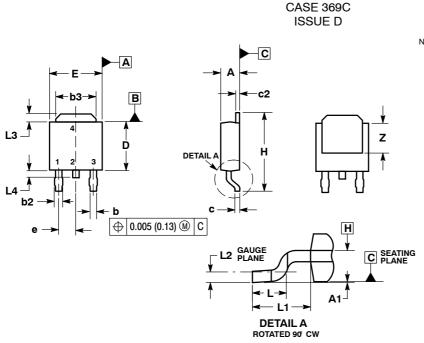
ORDERING INFORMATION

| Device | Package | Shipping [†] |
|---------------|-----------|-----------------------|
| MBRD620CTT4G | | 2500 / Tape & Reel |
| MBRD630CTT4G | | 2500 / Tape & Reel |
| MBRD640CTG | | 75 Units / Rail |
| NRVBD640CTG | | 75 Units / Rail |
| MBRD640CTT4G | | 2500 / Tape & Reel |
| NRVBD640CTT4G | | 2500 / Tape & Reel |
| MBRD650CTG | DPAK | 75 Units / Rail |
| MBRD650CTT4G | (Pb-Free) | 2500 / Tape & Reel |
| NRVBD650CTT4G | | 2500 / Tape & Reel |
| MBRD660CTG | | 75 Units / Rail |
| NRVBD660CTG | - | 75 Units / Rail |
| MBRD660CTRLG | - | 1800 / Tape & Reel |
| MBRD660CTT4G | | 2500 / Tape & Reel |
| NRVBD660CTT4G | | 2500 / Tape & Reel |

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

PACKAGE DIMENSIONS

DPAK (SINGLE GAUGE)

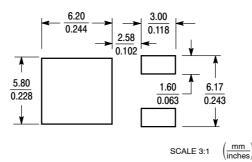


NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994
- CONTROLLING DIMENSION: INCHES.
 THERMAL PAD CONTOUR OPTIONAL WITHIN DI-MENSIONS b3, L3 and Z. 4. DIMENSIONS D AND E DO NOT INCLUDE MOLD
- FLASH, PROTRUSIONS, OR BURRS. MOLD FLASH, PROTRUSIONS, OR GATE BURRS SHALL
- NOT EXCEED 0.006 INCHES PER SIDE. 5. DIMENSIONS D AND E ARE DETERMINED AT THE OUTERMOST EXTREMES OF THE PLASTIC BODY.
- 6. DATUMS A AND B ARE DETERMINED AT DATUM PLANE H.

| | INCHES | | MILLIMETERS | | |
|-----|--------|-------|-------------|-------|--|
| DIM | MIN | MAX | MIN | MAX | |
| Α | 0.086 | 0.094 | 2.18 | 2.38 | |
| A1 | 0.000 | 0.005 | 0.00 | 0.13 | |
| b | 0.025 | 0.035 | 0.63 | 0.89 | |
| b2 | 0.030 | 0.045 | 0.76 | 1.14 | |
| b3 | 0.180 | 0.215 | 4.57 | 5.46 | |
| с | 0.018 | 0.024 | 0.46 | 0.61 | |
| c2 | 0.018 | 0.024 | 0.46 | 0.61 | |
| D | 0.235 | 0.245 | 5.97 | 6.22 | |
| Е | 0.250 | 0.265 | 6.35 | 6.73 | |
| е | 0.090 | BSC | 2.29 BSC | | |
| Н | 0.370 | 0.410 | 9.40 | 10.41 | |
| L | 0.055 | 0.070 | 1.40 | 1.78 | |
| L1 | 0.108 | REF | 2.74 REF | | |
| L2 | 0.020 | BSC | 0.51 | BSC | |
| L3 | 0.035 | 0.050 | 0.89 | 1.27 | |
| L4 | | 0.040 | | 1.01 | |
| Z | 0.155 | | 3.93 | | |

SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ON Semiconductor and 💷 are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILIC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILIC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILIC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILIC obsent or any liability nor the rights of others. SCILIC products are not designed, intended, or authorized for use a components in systems intended for surgical implant into the body, or other applications are specified to the SCILIC of the S intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free USA/Canada Europe, Middle East and Africa Technical Support:

Phone: 421 33 790 2910 Japan Customer Focus Center Phone: 81-3-5817-1050

ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative