NSF030120T2A0

1200 V, 30 mΩ, N-channel SiC MOSFET

24 March 2025 **Product data sheet**

1. General description

The NSF030120T2A0 is a Silicon Carbide based 1200 V power MOSFET in a 7-pin X.PAK plastic package for surface mounted, topside-cooled technology. Its excellent R_{DSon} temperature stability, combined with fast switching speed, makes it a product of choice in high power and high voltage industrial applications, such as E-vehicle charging infrastructure, photovoltaic inverters and motor drives.

2. Features and benefits

- Excellent R_{DSon} temperature stability
- Very low switching losses
- Fast reverse recovery
- Fast switching speed
- Temperature independent turn-off switching losses
- Very fast and robust intrinsic body diode
- Faster commutation and improved switching due to the additional Kelvin source pin

3. Applications

- E-vehicle charging infrastructure
- Photovoltaic inverters
- Switch mode power supply
- Uninterruptable power supply
- Motor drives

4. Quick reference data

Table 1. Quick reference data

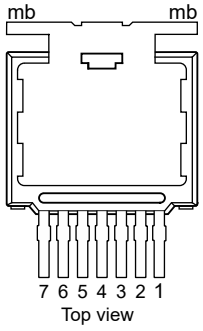
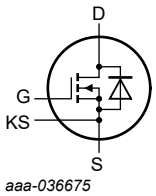
Symbol	Parameter	Conditions		Min	Typ	Max	Unit
V_{DS}	drain-source voltage			-	-	1200	V
V_{GS}	gate-source voltage		[1]	-10	-	22	V
I_D	drain current	T_c = 25 °C	[2]	-	-	68	A
		T_c = 100 °C	[2]	-	-	48	A
I_{DM}	peak drain current	pulsed; t_p limited by T_j (max)	[3]	-	-	160	A
Static characteristics							
R_{DSon}	drain-source on-state resistance	V_{GS} = 18 V; I_D = 40 A; T_j = 25 °C		-	30	45	mΩ

[1] Recommended turn off gate voltage is -5 V to 0 V. Recommended turn on gate voltage is 15 V to 18 V. Do not use with V_{GSon} < 13 V.
[2] Limited by the maximum values of T_j, $R_{th(j-c)}$ and $R_{DSon}(T_j)$.
[3] Designed value (not tested).

nexperia

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate	 **X.PAK (SOT8107-2)**	
2	KS	kelvin source		
3 to 7	S	source		
mb	D	mounting base; connected to drain		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
NSF030120T2A0	X.PAK	Plastic, surface-mounted Top Side Cooling (TSC) Package; 1.27 mm pitch; 14.0 mm x 11.8 mm x 3.5 mm body	SOT8107-2

7. Marking

Table 4. Marking codes

Type number	Marking code
NSF030120T2A0	NSF030120T2A0

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_{DS}	drain-source voltage			-	1200	V
V_{GS}	gate-source voltage		[1]	-10	22	V
I_D	drain current	T_c = 25 °C	[2]	-	68	A
		T_c = 100 °C	[2]	-	48	A
I_{DM}	peak drain current	pulsed; t_p limited by T_j (max)	[3]	-	160	A
P_{tot}	total power dissipation	T_c = 25 °C	[2]	-	294	W
T_j	junction temperature			-55	175	°C
T_{stg}	storage temperature			-55	150	°C
Source-drain diode						
I_S	source current	T_c = 25 °C; pulsed; limited by T_j (max)	[2]	-	69	A
I_{SM}	peak source current	pulsed; limited by T_j (max)	[3]	-	120	A

[1] Recommended turn off gate voltage is -5 V to 0 V. Recommended turn on gate voltage is 15 V to 18 V. Do not use with V_{GSon} < 13 V.
[2] Limited by the maximum values of T_j, $R_{th(j-c)}$ and $R_{DSon}(T_j)$.
[3] Designed value (not tested).

9. Thermal characteristics

Table 6. Thermal characteristics

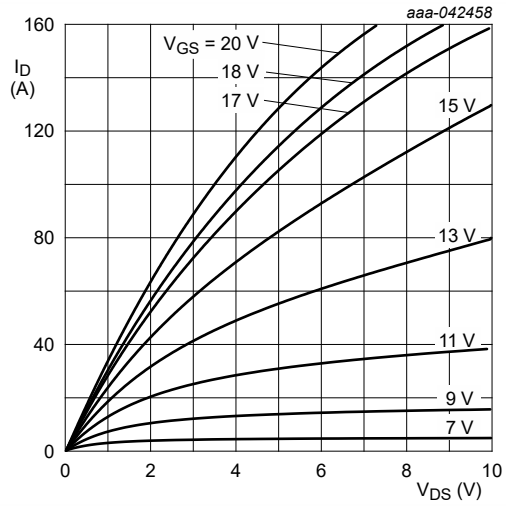
Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$R_{th(j-c)}$	thermal resistance from junction to case			-	0.39	0.51	K/W

10. Characteristics

Table 7. Characteristics

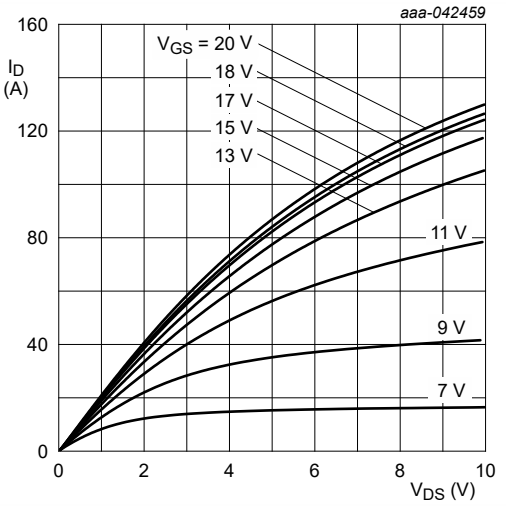
Symbol	Parameter	Conditions		Min	Typ	Max	Unit
Static characteristics							
$V_{(BR)DSS}$	drain-source breakdown voltage	I_D = 1 mA; V_{GS} = 0 V; T_j = 25 °C		1200	-	-	V
$V_{GS(th)}$	gate-source threshold voltage	I_D = 2 mA; V_{DS} = V_{GS}; T_j = 25 °C	[1]	1.7	2.3	2.9	V
		I_D = 20 mA; V_{DS} = V_{GS}; T_j = 25 °C	[1]	-	2.77	-	V
I_{DSS}	drain leakage current	V_{DS} = 1200 V; V_{GS} = 0 V; T_j = 25 °C		-	-	100	µA
I_{GSS}	gate leakage current	V_{GS} = 22 V; V_{DS} = 0 V; T_j = 25 °C		-	-	100	nA
		V_{GS} = -10 V; V_{DS} = 0 V; T_j = 25 °C		-	-	100	nA
R_{DSon}	drain-source on-state resistance	V_{GS} = 18 V; I_D = 40 A; T_j = 25 °C		-	30	45	mΩ
		V_{GS} = 18 V; I_D = 40 A; T_j = 175 °C		-	49	-	mΩ
		V_{GS} = 15 V; I_D = 40 A; T_j = 25 °C		-	40	-	mΩ
		V_{GS} = 15 V; I_D = 40 A; T_j = 175 °C		-	53	-	mΩ
g_{fs}	forward transconductance	V_{DS} = 10 V; I_D = 40 A; T_j = 25 °C		-	19	-	S
$R_{G(int)}$	internal gate resistance	f = 0.5 MHz; T_j = 25 °C		-	2.3	-	Ω
Dynamic characteristics							
$Q_{G(tot)}$	total gate charge	V_{DD} = 800 V; I_D = 40 A; V_{GS} = -5/+18 V; T_j = 25 °C		-	113	-	nC
Q_{GS}	gate-source charge			-	44	-	nC
Q_{GD}	gate-drain charge			-	34	-	nC
C_{iss}	input capacitance	V_{DD} = 800 V; f = 0.5 MHz; V_{GS} = 0 V; T_j = 25 °C		-	2600	-	pF
C_{oss}	output capacitance			-	136	-	pF
C_{rss}	reverse transfer capacitance			-	6	-	pF
$t_{d(on)}$	turn-on delay time	V_{DD} = 800 V; I_D = 40 A; $R_{G(ext)}$ = 2.2 Ω; L_L = 82 µH; V_{GS} = -5/+18 V; T_j = 25 °C		-	19	-	ns
t_r	rise time			-	14	-	ns
$t_{d(off)}$	turn-off delay time			-	22	-	ns
t_f	fall time			-	8	-	ns
E_{on}	turn-on switching loss			-	633	-	µJ
E_{off}	turn-off switching loss			-	55	-	µJ
Source-drain diode							
V_{SD}	source-drain voltage	I_S = 40 A; V_{GS} = -5 V; T_j = 25 °C		-	4.4	-	V
t_{rr}	reverse recovery time	V_{DD} = 800 V; I_S = 40 A; dI_S/dt = 5495 A/µs; V_{GS} = -5 V; T_j = 25 °C		-	11	-	ns
Q_r	recovered charge			-	243	-	nC

[1] Measured according to JEP183.



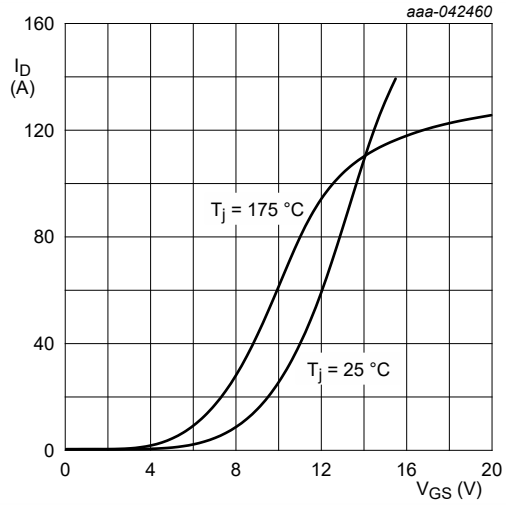
t_p = 300 µs; T_j = 25 °C

Fig. 1. Output characteristics: drain current as a function of drain-source voltage; typical values



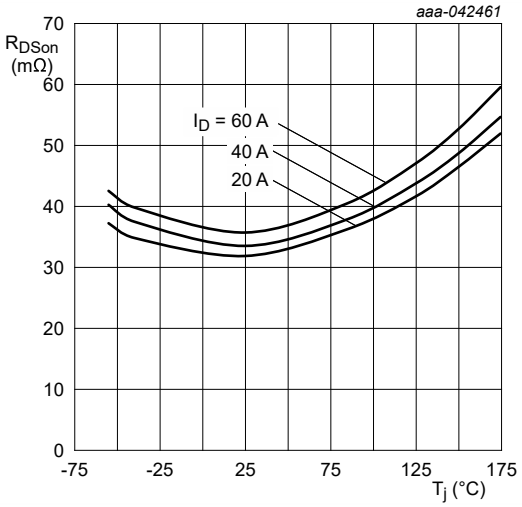
t_p = 300 µs; T_j = 175 °C

Fig. 2. Output characteristics: drain current as a function of drain-source voltage; typical values



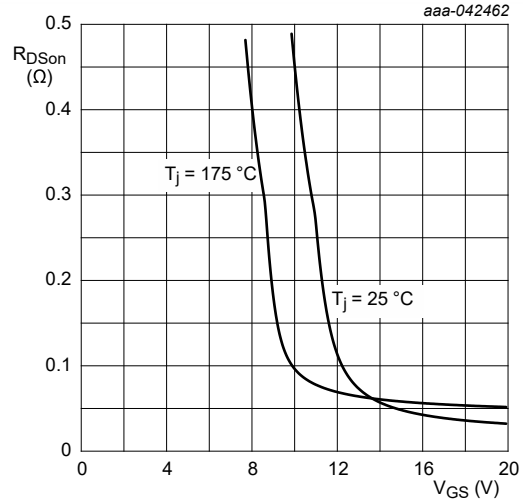
V_{DS} = 10 V; t_p = 300 µs

Fig. 3. Transfer characteristics: drain current as a function of gate-source voltage; typical values



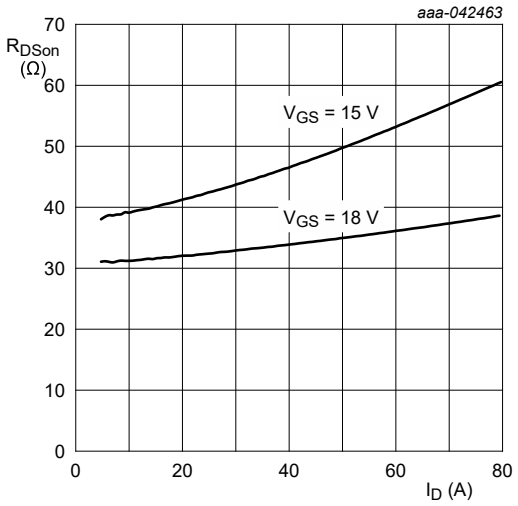
V_{GS} = 18 V; t_p = 300 µs

Fig. 4. Drain-source on-state resistance as a function of junction temperature; typical values



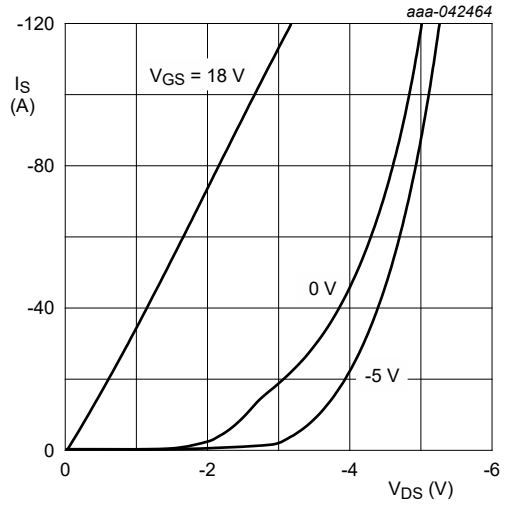
I_D = 40 A

Fig. 5. Drain-source on-state resistance as a function of gate-source voltage



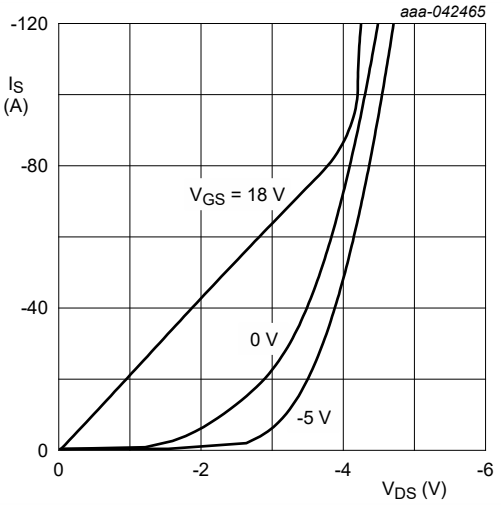
t_p = 300 µs; T_j = 25 °C

Fig. 6. Drain-source on-state resistance as a function of drain current; typical values



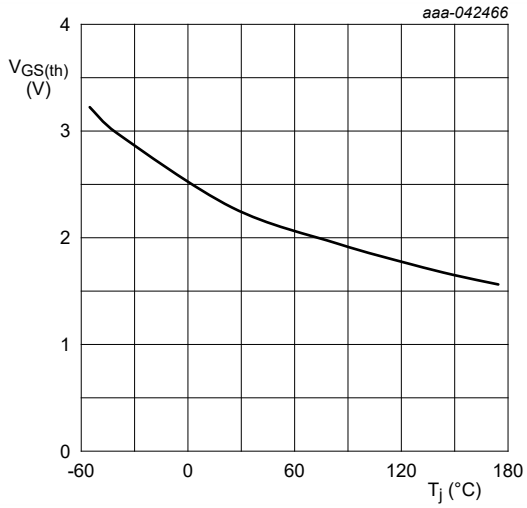
t_p = 300 µs; T_j = 25 °C

Fig. 7. Source current as a function of source-drain voltage; typical values (third quadrant characteristics)



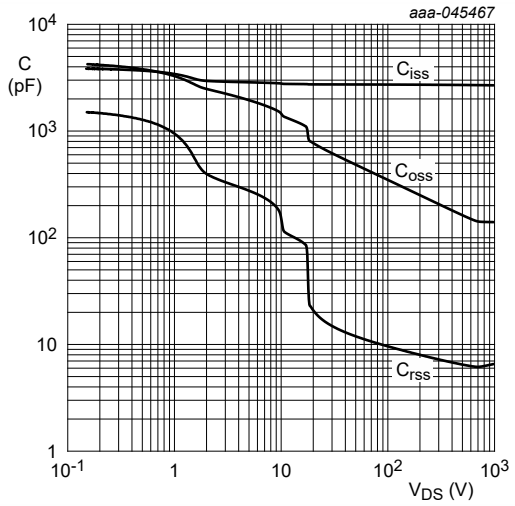
t_p = 300 µs; T_j = 175 °C

Fig. 8. Source current as a function of source-drain voltage; typical values (third quadrant characteristics)



I_D = 4 mA

Fig. 9. Gate-source threshold voltage as a function of junction temperature; typical values



f = 0.5 MHz; V_{GS} = 0 V; T_j = 25 °C

Fig. 10. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values

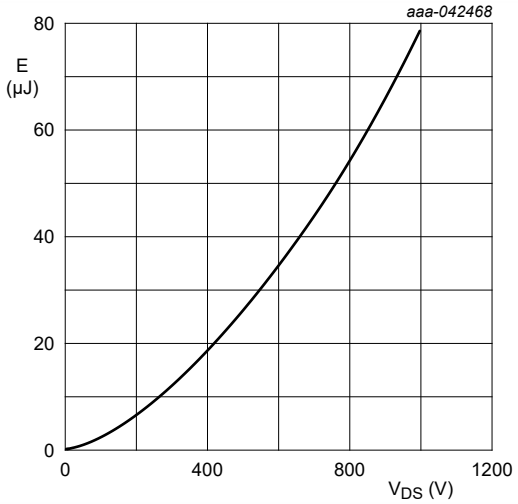
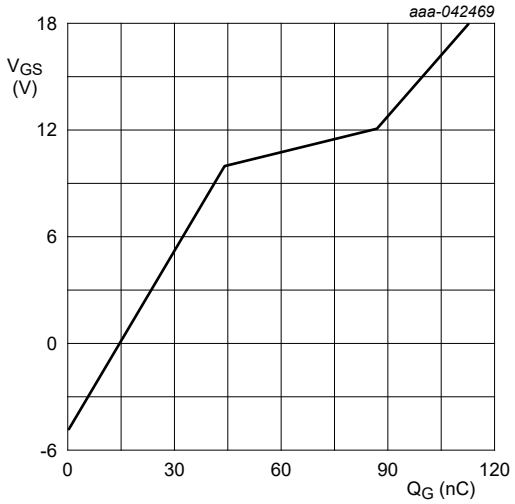
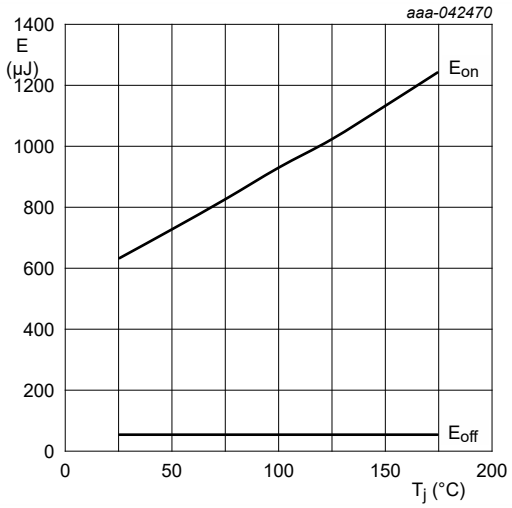


Fig. 11. C_{oss} stored energy as a function of drain-souce voltage; typical values



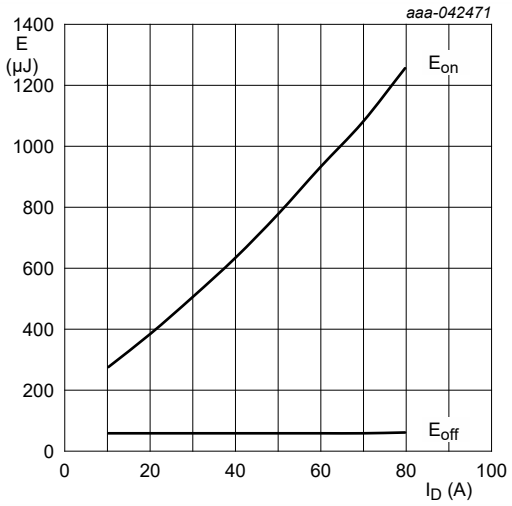
V_{DD} = 800 V; I_D = 40 A; T_j = 25 °C

Fig. 12. Gate-source voltage as a function of gate charge; typical values



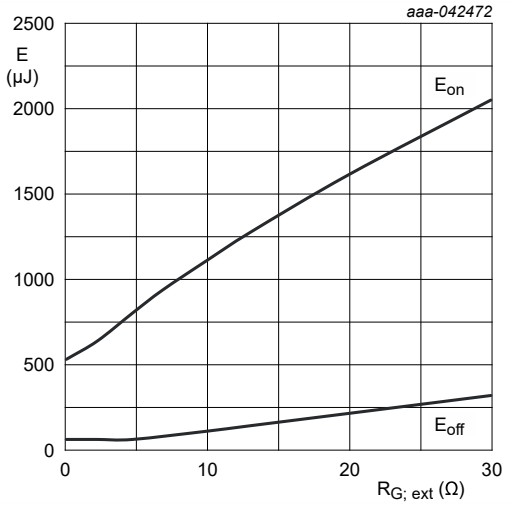
V_{DD} = 800 V; I_D = 40 A; V_{GS} = -5/18 V; $R_{G(ext)}$ = 2.2 Ω; L_L = 82 µH

Fig. 13. Switching loss as a function of junction temperature; typical values



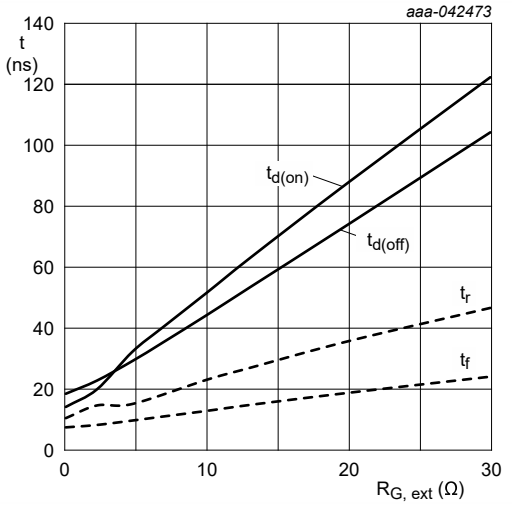
V_{DD} = 800 V; V_{GS} = -5/18 V; $R_{G(ext)}$ = 2.2 Ω; L_L = 82 µH; T_j = 25 °C

Fig. 14. Switching loss as a function of drain current; typical values



V_{DD} = 800 V; I_D = 40 A; V_{GS} = -5/18 V; L_L = 82 µH; T_j = 25 °C

Fig. 15. Switching loss as a function of external gate resistance; typical values



V_{DD} = 800 V; I_D = 40 A; V_{GS} = -5/18 V; L_L = 82 µH; T_j = 25 °C

Fig. 16. Switching times as a function of external gate resistance; typical values

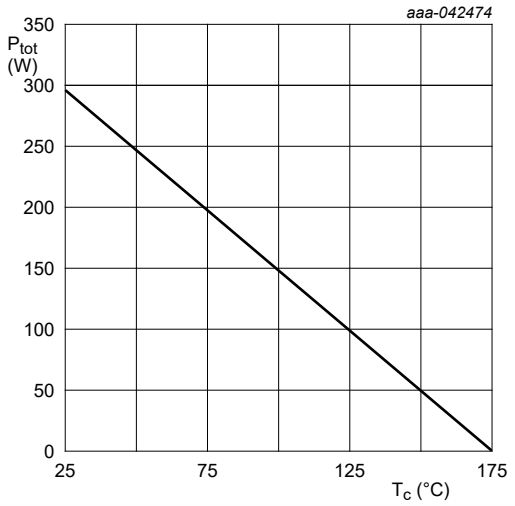


Fig. 17. Power dissipation derating as a function of case temperature; maximum values

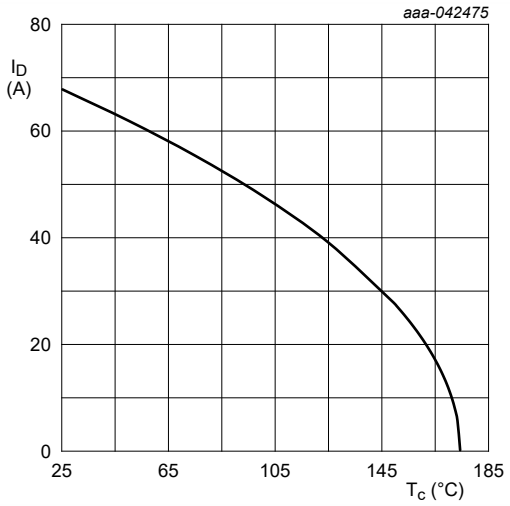
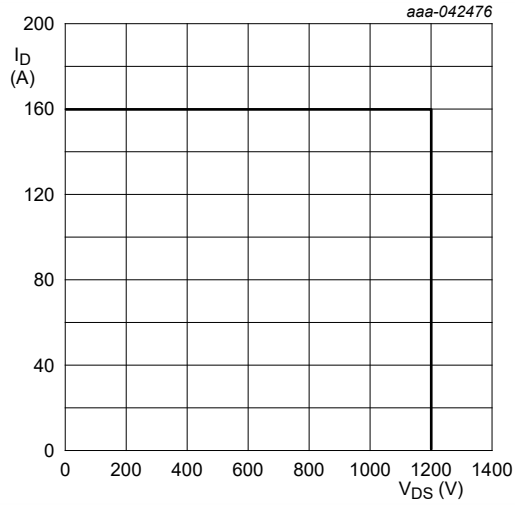


Fig. 18. Continuous drain current as a function of case temperature; maximum values



V_{GS} = -5/+18 V; T_c = 25 °C; T_j ≤ 175 °C; not for linear use

Fig. 19. Reverse bias safe operating area (RBSOA)

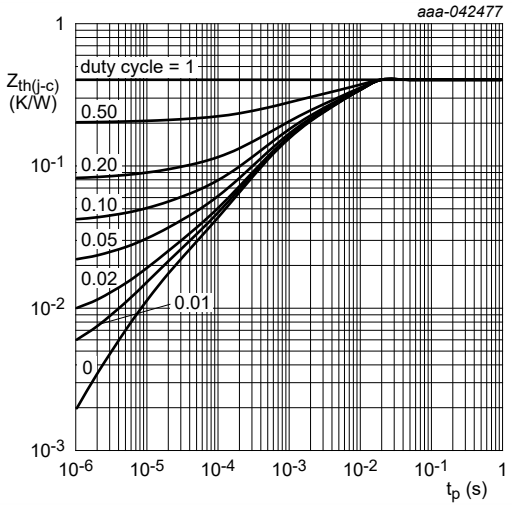


Fig. 20. Transient thermal impedance from junction to case as a function of pulse duration; typical values

11. Test information

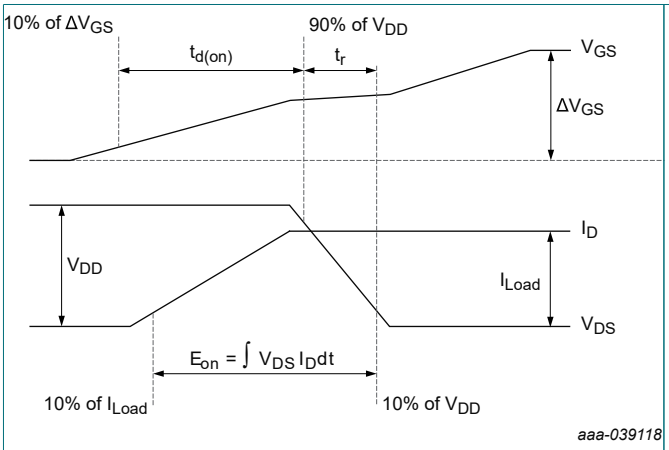


Fig. 21. Definition of switching times and losses during channel turn-on

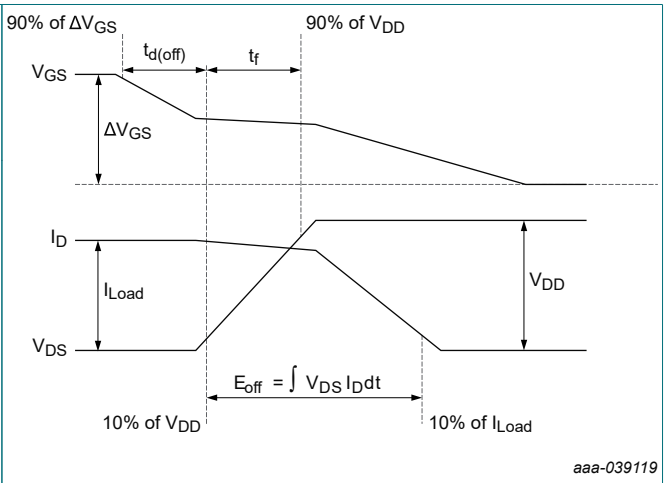


Fig. 22. Definition of switching times and losses during channel turn-off

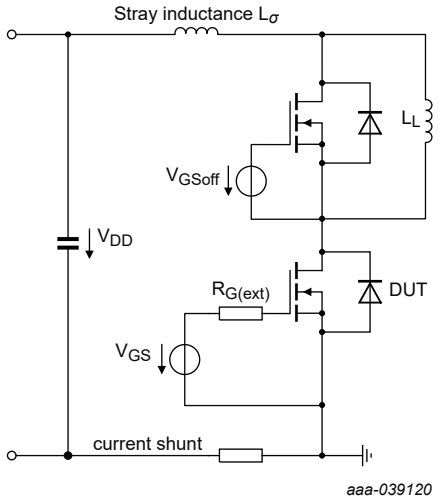


Fig. 23. Test circuit for dynamic characterization of channel and gate charge characteristics

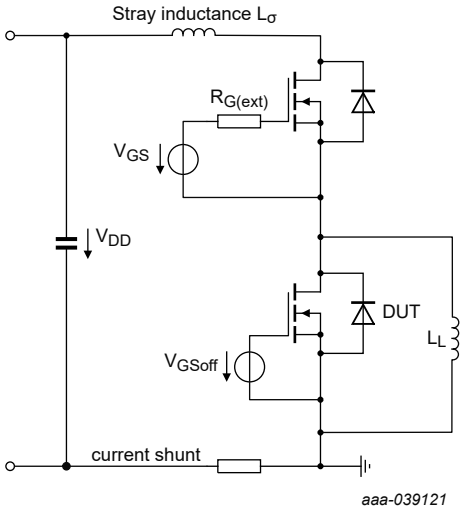


Fig. 24. Test circuit for dynamic characterization of body diode

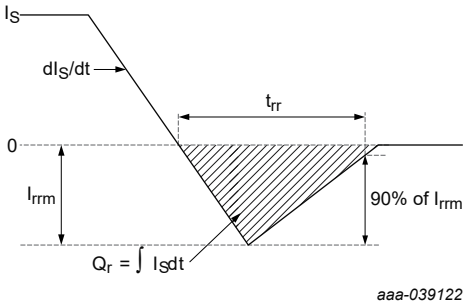


Fig. 25. Definition of dynamic characteristics of body diode

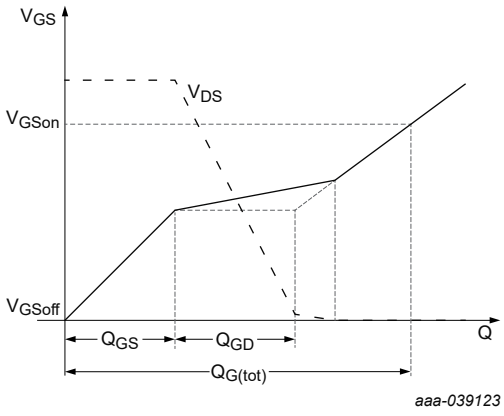


Fig. 26. Definition of gate charge characteristics

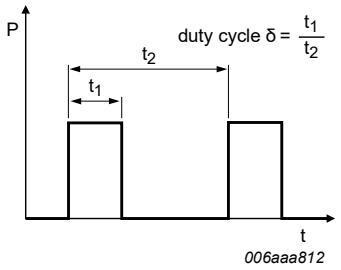
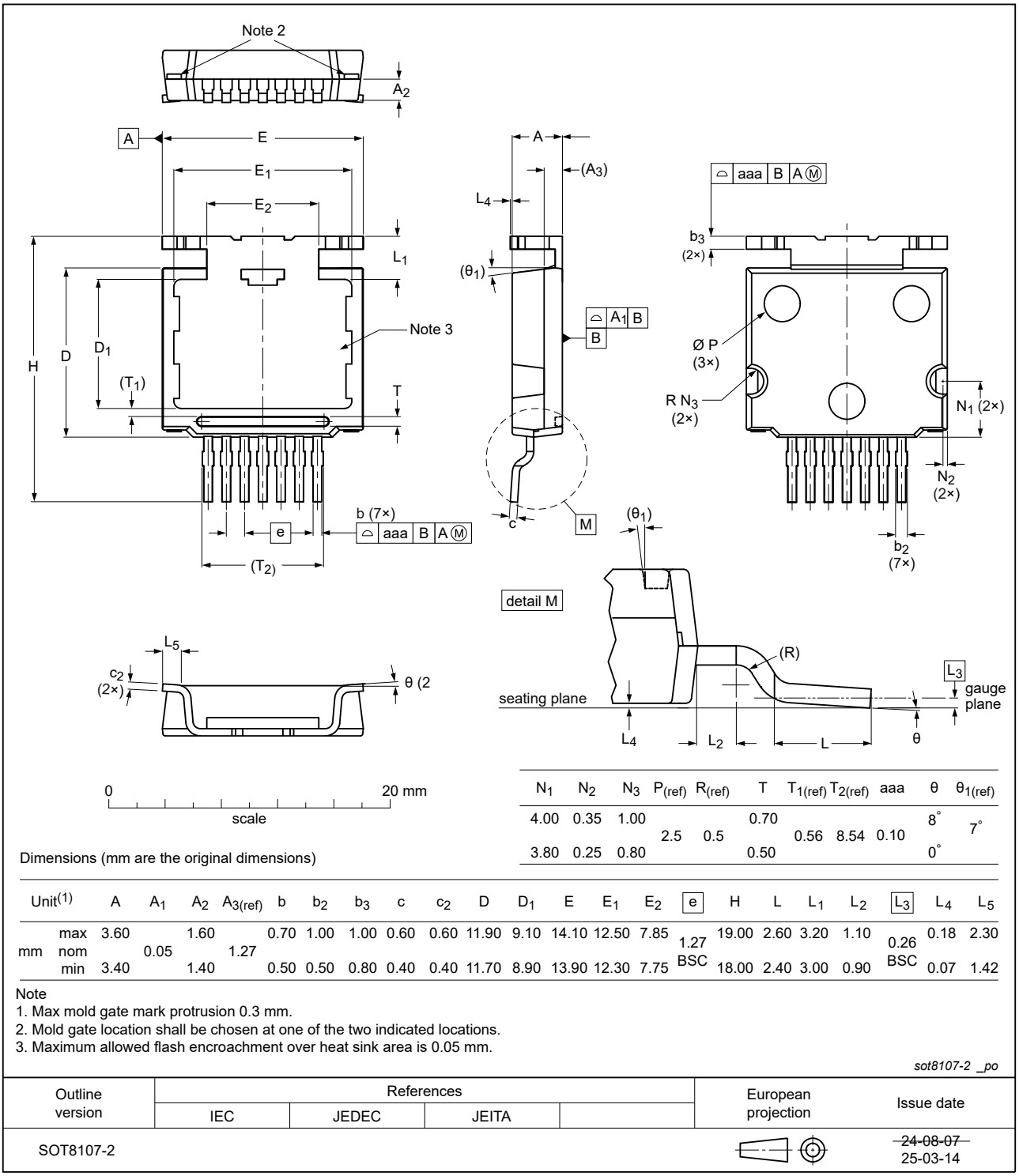


Fig. 27. Duty cycle definition

12. Package outline

X.PAK : Plastic, surface-mounted Top Side Cooling (TSC) Package; 1.27 mm pitch; 14.0 mm x 11.8 mm x 3.5 mm body **SOT8107-2**



N_1	N_2	N_3	$P_{(ref)}$	$R_{(ref)}$	T	$T_{1(ref)}$	$T_{2(ref)}$	aaa	θ	$\theta_{1(ref)}$
4.00	0.35	1.00	2.5	0.5	0.70	0.56	8.54	0.10	8°	7°
3.80	0.25	0.80			0.50				0°	

Dimensions (mm are the original dimensions)

Unit[1]		A	A_1	A_2	$A_{3(ref)}$	b	b_2	b_3	c	c_2	D	D_1	E	E_1	E_2	e	H	L	L_1	L_2	L_3	L_4	L_5
mm	max	3.60		1.60		0.70	1.00	1.00	0.60	0.60	11.90	9.10	14.10	12.50	7.85	1.27 BSC	19.00	2.60	3.20	1.10	0.26 BSC	0.18	2.30
	nom		0.05		1.27																		
	min	3.40		1.40		0.50	0.50	0.80	0.40	0.40	11.70	8.90	13.90	12.30	7.75		18.00	2.40	3.00	0.90		0.07	1.42

Note
1. Max mold gate mark protrusion 0.3 mm.
2. Mold gate location shall be chosen at one of the two indicated locations.
3. Maximum allowed flash encroachment over heat sink area is 0.05 mm.

sot8107-2 _po

Outline version	References				European projection	Issue date
	IEC	JEDEC	JEITA			
SOT8107-2						~~24-08-07~~ 25-03-14

Fig. 28. Package outline X.PAK (SOT8107-2)

13. Soldering

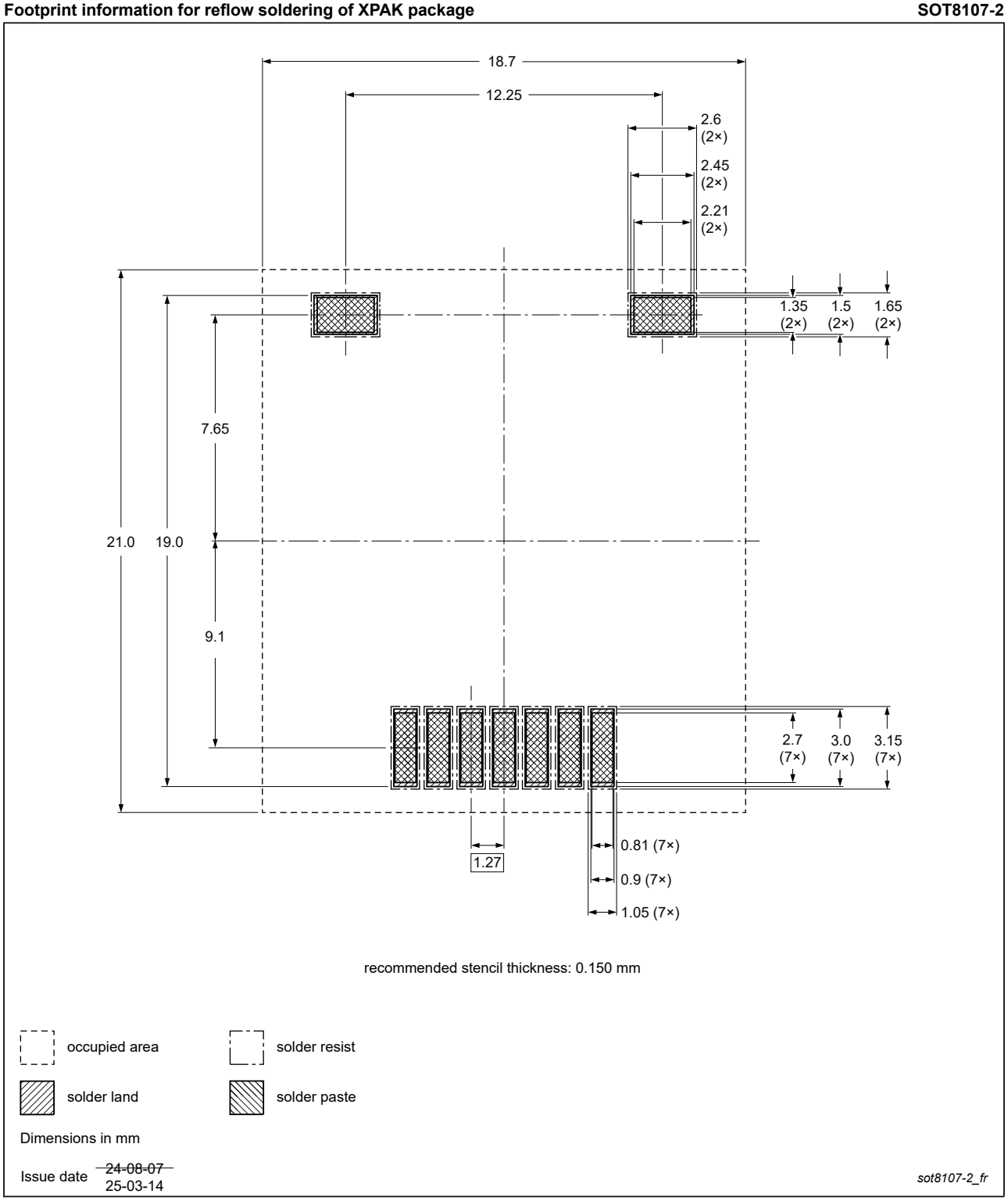


Fig. 29. Reflow soldering footprint for X.PAK (SOT8107-2)

14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
NSF030120T2A0 v.2	20250324	Product data sheet	-	NSF030120T2A0 v.1
Modifications:	• Package outline changed • Soldering changed			
NSF030120T2A0 v.1	20250314	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.
[2] The term 'short data sheet' is explained in section "Definitions".
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