**Product data sheet** 

## 1. General description

The NSF060120T2A0 is a Silicon Carbide based 1200 V power MOSFET in a 7-pin X.PAK plastic package for surface mounted, topside-cooled technology. Its excellent  $R_{DSon}$  temperature stability, combined with fast switching speed, makes it a product of choice in high power and high voltage industrial applications, such as E-vehicle charging infrastructure, photovoltaic inverters and motor drives

### 2. Features and benefits

- Excellent R<sub>DSon</sub> temperature stability
- Very low switching losses
- Fast reverse recovery
- · Fast switching speed
- · Temperature independent turn-off switching losses
- · Very fast and robust intrinsic body diode
- · Faster commutation and improved switching due to the additional Kelvin source pin

## 3. Applications

- E-vehicle charging infrastructure
- Photovoltaic inverters
- Switch mode power supply
- Uninterruptable power supply
- · Motor drives

### 4. Quick reference data

#### Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V <sub>DS</sub>	drain-source voltage			-	-	1200	V
$V_{GS}$	gate-source voltage		[1]	-10	-	22	V
I <sub>D</sub>	drain current	T <sub>c</sub> = 25 °C	[2]	-	-	36	А
		T <sub>c</sub> = 100 °C	[2]	-	-	26	А
I <sub>DM</sub>	peak drain current	pulsed; t <sub>p</sub> limited by T <sub>j</sub> (max)	[3]	-	-	80	А
Static characte	eristics		·		<u>'</u>		
R <sub>DSon</sub>	drain-source on-state resistance	$V_{GS} = 18 \text{ V}; I_D = 20 \text{ A}; T_j = 25 ^{\circ}\text{C}$		-	60	90	mΩ

- [1] Recommended turn off gate voltage is -5 V to 0 V. Recommended turn on gate voltage is 15 V to 18 V. Do not use with  $V_{GSon}$  < 13 V.
- [2] Limited by the maximum values of  $T_j$ ,  $R_{th(j-c)}$  and  $R_{DSon}(T_j)$ .
- [3] Designed value (not tested).



# 5. Pinning information

#### **Table 2. Pinning information**

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate	mb mb	
2	KS	kelvin source		
3 to 7	S	source		D I
mb	D	connected to drain	7 6 5 4 3 2 1 Top view	G KS S aaa-036675
			X.PAK (SOT8107-2)	

# 6. Ordering information

### **Table 3. Ordering information**

Type number	Package				
	Name	Description	Version		
NSF060120T2A0	X.PAK	Plastic, surface-mounted Top Side Cooling (TSC) Package; 1.27 mm pitch; 14.0 mm x 11.8 mm x 3.5 mm body	SOT8107-2		

# 7. Marking

#### Table 4. Marking codes

Type number	Marking code
NSF060120T2A0	NSF060120T2A0

# 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>DS</sub>	drain-source voltage			-	1200	V
$V_{GS}$	gate-source voltage		[1]	-10	22	V
I <sub>D</sub>	drain current	T <sub>c</sub> = 25 °C	[2]	-	36	Α
		T <sub>c</sub> = 100 °C	[2]	-	26	Α
I <sub>DM</sub>	peak drain current	pulsed; t <sub>p</sub> limited by T <sub>j</sub> (max)	[3]	-	80	А
P <sub>tot</sub>	total power dissipation	T <sub>c</sub> = 25 °C	[2]	-	167	W
Tj	junction temperature			-55	175	°C
T <sub>stg</sub>	storage temperature			-55	150	°C
Source-draii	n diode			·		
I <sub>S</sub>	source current	T <sub>c</sub> = 25 °C	[2]	-	38	А
I <sub>SM</sub>	peak source current	pulsed; limited by T <sub>j</sub> (max)	[3]	-	60	Α

<sup>[1]</sup> Recommended turn off gate voltage is -5 V to 0 V. Recommended turn on gate voltage is 15 V to 18 V. Do not use with  $V_{GSon}$  < 13 V.

## 9. Thermal characteristics

#### **Table 6. Thermal characteristics**

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R <sub>th(j-c)</sub>	thermal resistance from junction to case		-	0.7	0.9	K/W

<sup>[2]</sup> Limited by the maximum values of  $T_j$ ,  $R_{th(j-c)}$  and  $R_{DSon}(T_j)$ .

<sup>[3]</sup> Designed value (not tested).

# 10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Static chara	acteristics						
V <sub>(BR)DSS</sub>	drain-source breakdown voltage	$I_D = 1 \text{ mA}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ °C}$		1200	-	-	V
V <sub>GS(th)</sub>	gate-source threshold	$I_D = 2 \text{ mA}; V_{DS} = V_{GS}; T_j = 25 \text{ °C}$	[1]	1.7	2.3	2.9	V
	voltage	I <sub>D</sub> = 10 mA; V <sub>DS</sub> = V <sub>GS</sub> ; T <sub>j</sub> = 25 °C	[1]	-	2.77	-	V
I <sub>DSS</sub>	drain leakage current	V <sub>DS</sub> = 1200 V; V <sub>GS</sub> = 0 V; T <sub>j</sub> = 25 °C		-	-	100	μΑ
I <sub>GSS</sub>	gate leakage current	V <sub>GS</sub> = 22 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C		-	-	100	nA
		V <sub>GS</sub> = -10 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C		-	-	100	nA
R <sub>DSon</sub>	drain-source on-state	V <sub>GS</sub> = 18 V; I <sub>D</sub> = 20 A; T <sub>j</sub> = 25 °C		-	60	90	mΩ
	resistance	V <sub>GS</sub> = 18 V; I <sub>D</sub> = 20 A; T <sub>j</sub> = 175 °C		-	98	-	mΩ
		V <sub>GS</sub> = 15 V; I <sub>D</sub> = 20 A; T <sub>j</sub> = 25 °C		-	80	-	mΩ
		V <sub>GS</sub> = 15 V; I <sub>D</sub> = 20 A; T <sub>j</sub> = 175 °C		-	110	-	mΩ
9 <sub>fs</sub>	forward transconductance	$V_{DS} = 10 \text{ V}; I_D = 20 \text{ A}; T_j = 25 ^{\circ}\text{C}$		-	9	-	S
R <sub>G(int)</sub>	internal gate resistance	f = 0.5 MHz; T <sub>j</sub> = 25 °C		-	2	-	Ω
	naracteristics	-					
Q <sub>G(tot)</sub>	total gate charge	$V_{DD}$ = 800 V; $I_{D}$ = 20 A; $V_{GS}$ = -5/+18 V; $T_{j}$ = 25 °C		-	57	-	nC
Q <sub>GS</sub>	gate-source charge			-	25	-	nC
Q <sub>GD</sub>	gate-drain charge			-	15	-	nC
C <sub>iss</sub>	input capacitance	V <sub>DD</sub> = 800 V; f = 0.5 MHz; V <sub>GS</sub> = 0 V;		-	1335	-	pF
C <sub>oss</sub>	output capacitance	T <sub>j</sub> = 25 °C		-	74	-	pF
C <sub>rss</sub>	reverse transfer capacitance			-	4	-	pF
t <sub>d(on)</sub>	turn-on delay time	$V_{DD}$ = 800 V; $I_{D}$ = 20 A; $R_{G(ext)}$ = 2.2 $\Omega$ ;		-	13	-	ns
t <sub>r</sub>	rise time	$L_L = 82 \mu H; V_{GS} = -5/+18 \text{ V}; T_j = 25 ^{\circ}\text{C}$		-	8	-	ns
t <sub>d(off)</sub>	turn-off delay time			-	14	-	ns
t <sub>f</sub>	fall time			-	8	-	ns
E <sub>on</sub>	turn-on switching loss			-	276	-	μJ
E <sub>off</sub>	turn-off switching loss			-	30	-	μJ
Source-dra	in diode			1	'	'	
V <sub>SD</sub>	source-drain voltage	I <sub>S</sub> = 20 A; V <sub>GS</sub> = -5 V; T <sub>j</sub> = 25 °C		-	4.4	-	V
t <sub>rr</sub>	reverse recovery time	V <sub>DD</sub> = 800 V; I <sub>S</sub> = 20 A; dI <sub>S</sub> /dt = 7283 A/		-	8	-	ns
Q <sub>r</sub>	recovered charge	μs; V <sub>GS</sub> = -5 V; T <sub>j</sub> = 25 °C		-	160	-	nC

<sup>[1]</sup> Measured according to JEP183.

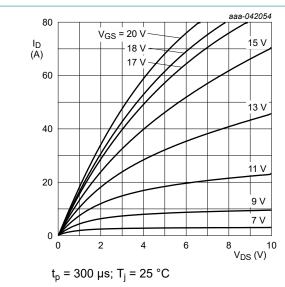


Fig. 1. Output characteristics: drain current as a function of drain-source voltage; typical values

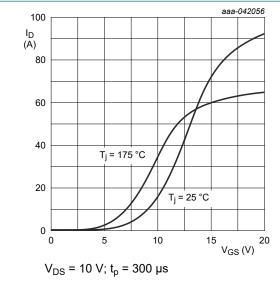


Fig. 3. Transfer characteristics: drain current as a function of gate-source voltage; typical values

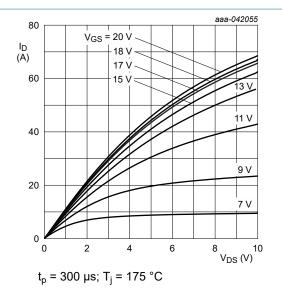


Fig. 2. Output characteristics: drain current as a function of drain-source voltage; typical values

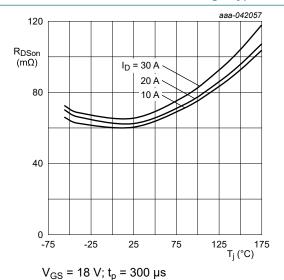


Fig. 4. Drain-source on-state resistance as a function of junction temperature; typical values

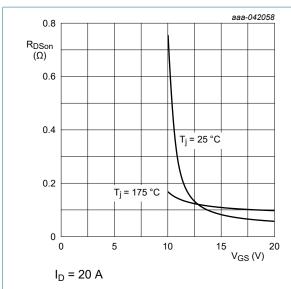


Fig. 5. Drain-source on-state resistance as a function of gate-source voltage

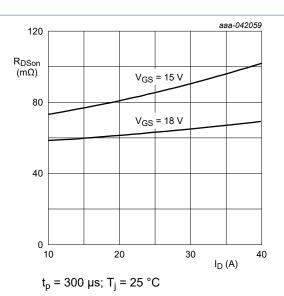


Fig. 6. Drain-source on-state resistance as a function of drain current; typical values

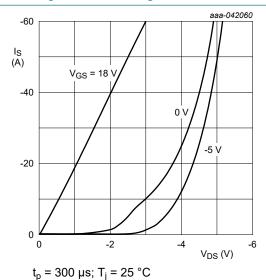


Fig. 7. Source current as a function of sourcedrain voltage; typical values (third quadrant characteristics)

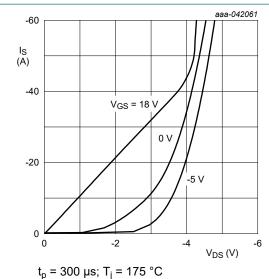


Fig. 8. Source current as a function of sourcedrain voltage; typical values (third quadrant characteristics)

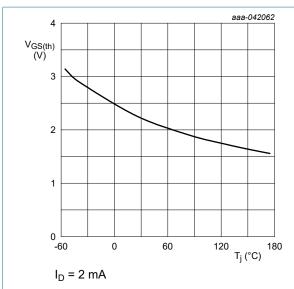
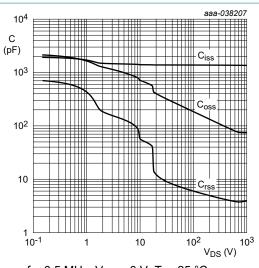


Fig. 9. Gate-source threshold voltage as a function of junction temperature; typical values



f = 0.5 MHz;  $V_{GS} = 0 \text{ V}$ ;  $T_i = 25 ^{\circ}\text{C}$ 

Fig. 10. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values

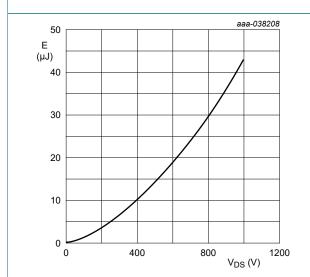
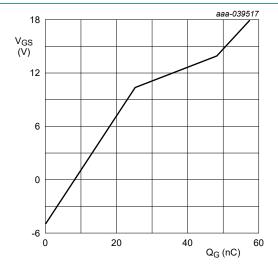


Fig. 11. C<sub>oss</sub> stored energy as a function of drain-souce voltage; typical values



 $V_{DD}$  = 800 V;  $I_{D}$  = 20 A;  $T_{amb}$  = 25 °C

Fig. 12. Gate-source voltage as a function of gate charge; typical values

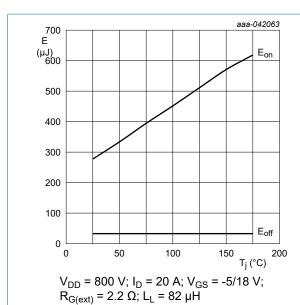


Fig. 13. Switching loss as a function of junction temperature; typical values

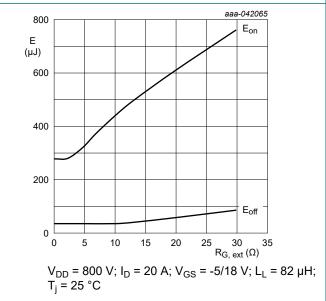
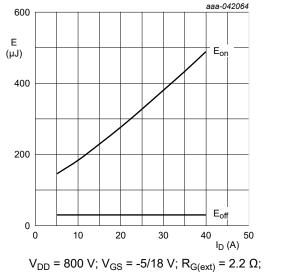


Fig. 15. Switching loss as a function of external gate resistance; typical values



 $L_L = 82 \mu H; T_i = 25 ^{\circ}C$ 

Fig. 14. Switching loss as a function of drain current; typical values

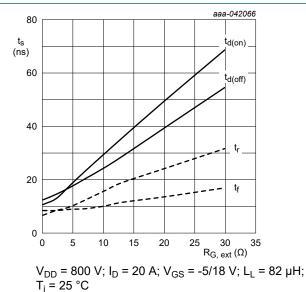
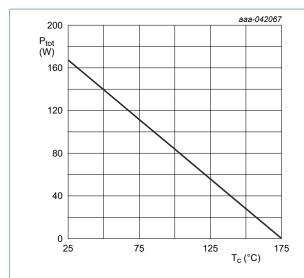
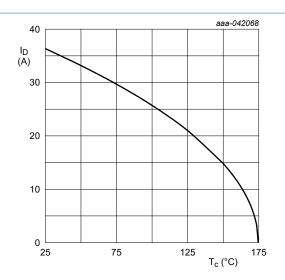


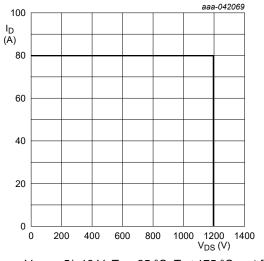
Fig. 16. Switching times as a function of external gate resistance; typical values

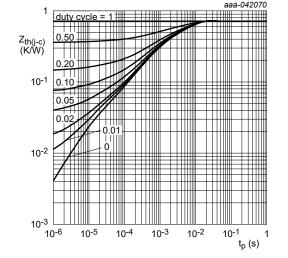




temperature; maximum values

Fig. 17. Power dissipation derating as a function of case Fig. 18. Continuous drain current as a function of case temperature; maximum values





 $V_{GS} = -5/+18 \text{ V}; T_c = 25 \text{ °C}; T_j \le 175 \text{ °C}; \text{ not for}$ linear use

Fig. 20. Transient thermal impedance from junction to case as a function of pulse duration; typical values

Fig. 19. Reverse bias safe operating area (RBSOA)

## 11. Test information

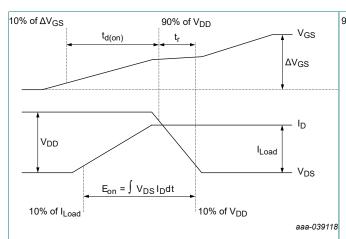


Fig. 21. Definition of switching times and losses during channel turn-on

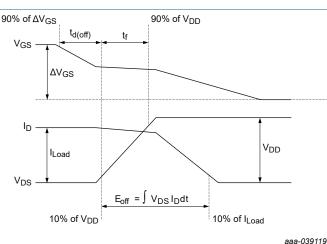


Fig. 22. Definition of switching times and losses during channel turn-off

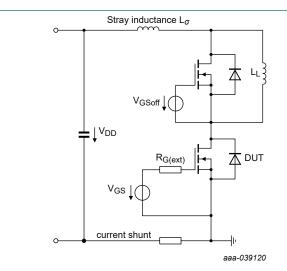


Fig. 23. Test circuit for dynamic characterization of channel and gate charge characteristics

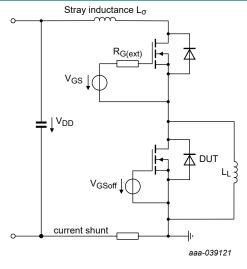


Fig. 24. Test circuit for dynamic characterization of body diode

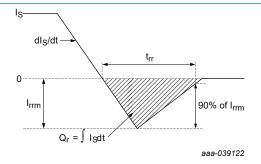


Fig. 25. Definition of dynamic characteristics of body diode

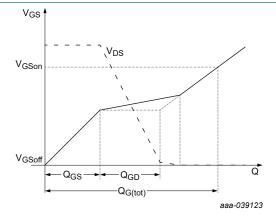
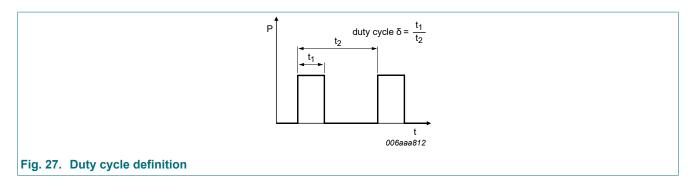


Fig. 26. Definition of gate charge characteristics



## 12. Package outline

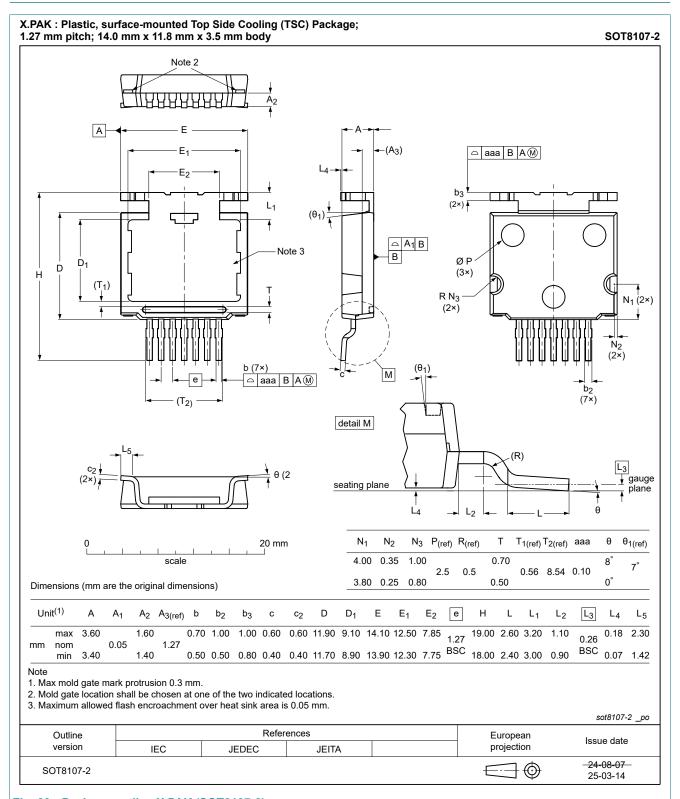
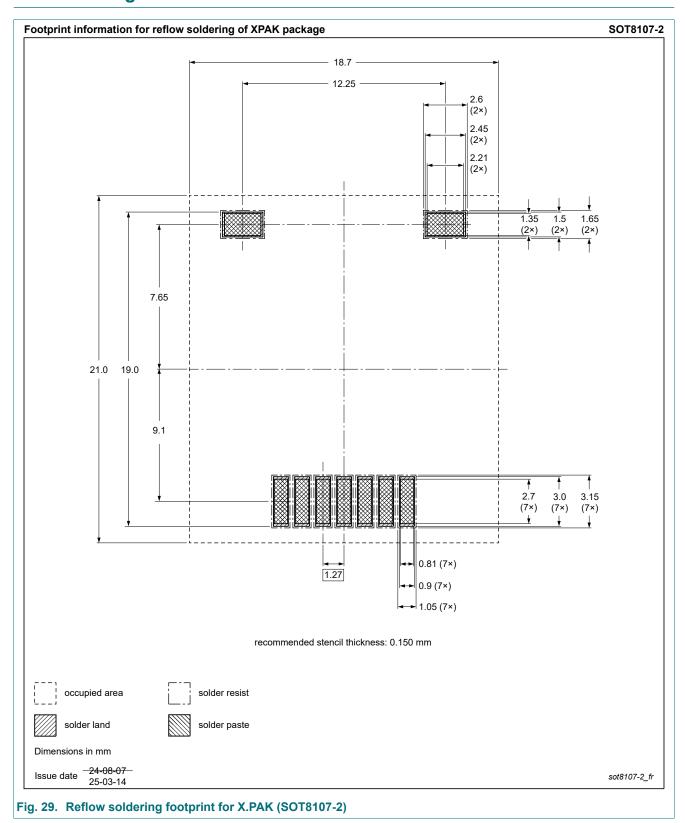


Fig. 28. Package outline X.PAK (SOT8107-2)

# 13. Soldering



# 14. Revision history

### Table 8. Revision history

Table 0. Nevision inistory								
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes				
NSF060120T2A0 v.2	20250324	Product data sheet	-	NSF060120T2A0 v.1				
Modifications:	<ul><li>Package outline cha</li><li>Soldering changed</li></ul>	nged						
NSF060120T2A0 v.1	20250314	Product data sheet	-	-				

## 15. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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For more information, please visit: http://www.nexperia.com For sales office addresses, please send an email to: salesaddresses@nexperia.com Date of release: 24 March 2025

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