

# Silicon Carbide (SiC) MOSFET – 40 mohm, 1200 V, M3S, D<sup>2</sup>PAK-7L

Product Preview

# **NTBG040N120M3S**

### **Features**

- Typ.  $R_{DS(on)} = 40 \text{ m}\Omega$  @  $V_{GS} = 18 \text{ V}$
- Low Switching Losses
- 100% Avalanche Tested
- This Device is Halide Free and RoHS Compliant with exemption 7a, Pb–Free 2LI (on second level interconnection)

# **Typical Applications**

- Solar Inverters
- Electric Vehicle Charging Stations
- UPS (Uninterruptible Power Supplies)
- Energy Storage Systems
- SMPS (Switch Mode Power Supplies)

# MAXIMUM RATINGS (T<sub>J</sub> = 25°C unless otherwise noted)

Parameter			Symbol	Value	Unit
Drain-to-Source Voltage			$V_{DSS}$	1200	V
Gate-to-Source Voltage			$V_{GS}$	-10/+22	V
	Recommended Operation Values of Gate-to-Source Voltage		$V_{GSop}$	-3/+18	>
Continuous Drain Current (Note 1)	Steady State	T <sub>C</sub> = 25°C	I <sub>D</sub>	51	Α
Power Dissipation (Note 1)			P <sub>D</sub>	329	W
Continuous Drain Current (Note 1)	Steady State	T <sub>C</sub> = 100°C	I <sub>D</sub>	37	Α
Power Dissipation (Note 1)			P <sub>D</sub>	165	V
Pulsed Drain Current (Note 2)	T <sub>C</sub>	= 25°C	I <sub>DM</sub>	244	Α
Operating Junction and Storage Temperature Range			T <sub>J</sub> , T <sub>stg</sub>	–55 to +175	°C
Source Current (Body Diode) T <sub>C</sub> = 25°C, V <sub>GS</sub> = -3 V			I <sub>S</sub>	TBD	Α
Single Pulse Drain-to-Source Avalanche Energy			E <sub>AS</sub>	TBD	mJ
Maximum Lead Temperature for Soldering (1/8" from case for 5 s)			TL	245	°C

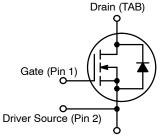
Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

- The entire application environment impacts the thermal resistance values shown, they are not constants and are only valid for the particular conditions noted.
- 2. Repetitive rating, limited by max junction temperature.

This document contains information on a product under development. **onsemi** reserves the right to change or discontinue this product without notice.

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V <sub>(BR)DSS</sub>	R <sub>DS(ON)</sub> MAX	I <sub>D</sub> MAX	
1200 V	52 mΩ @ 18 V	51 A	



Power Source (Pins 3, 4, 5, 6, 7)

### **N-CHANNEL MOSFET**



D2PAK-7L CASE 418BJ

### **MARKING DIAGRAM**

BG040N 120M3S AYWWZZ

BG040N120M3S = Specific Device Code

A = Assembly Location

Y = Year WW = Work Week ZZ = Lot Traceability

### **ORDERING INFORMATION**

Device	Package	Shipping		
NTBG040N120M3S	D2PAK-7L	800 / Tape & Reel		

**Table 1. THERMAL CHARACTERISTICS** 

Parameter	Symbol	Тур	Max	Unit
Junction-to-Case - Steady State (Note 1)	$R_{ heta JC}$	0.46	TBD	°C/W
Junction-to-Ambient - Steady State (Note 1)	$R_{\theta JA}$		40	

# Table 2. ELECTRICAL CHARACTERISTICS (T. J = 25°C unless otherwise specified)

Parameter	Symbol	Test Condition		Min	Тур	Max	Unit
OFF-STATE CHARACTERISTICS		•					
Drain-to-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 1 mA		1200	_	_	V
Drain-to-Source Breakdown Voltage Temperature Coefficient	V <sub>(BR)DSS</sub> /T <sub>J</sub>	I <sub>D</sub> = 1 mA, referenced	to 25°C	-	0.3	-	V/°C
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>GS</sub> = 0 V, V <sub>DS</sub> = 1200 V	T <sub>J</sub> = 25°C	-	-	100	μΑ
Gate-to-Source Leakage Current	I <sub>GSS</sub>	$V_{GS} = +22/-10 \text{ V}, V_{DS}$	s = 0 V	-	_	±1	μΑ
ON-STATE CHARACTERISTICS (Note 2)							
Gate Threshold Voltage	V <sub>GS(TH)</sub>	$V_{GS} = V_{DS}$ , $I_D = 10 \text{ m/s}$	A	2.04	2.72	4.4	V
Recommended Gate Voltage	$V_{GOP}$			-3	_	+18	V
Drain-to-Source On Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 18 V, I <sub>D</sub> = 30 A,	T <sub>J</sub> = 25°C	-	40	52	mΩ
		$V_{GS} = 18 \text{ V}, I_D = 30 \text{ A},$	T <sub>J</sub> = 175°C	-	85	-	
Forward Transconductance	9FS	$V_{DS} = 10 \text{ V}, I_D = 30 \text{ A}$		-	13	_	S
CHARGES, CAPACITANCES & GATE RES	ISTANCE						
Input Capacitance	C <sub>ISS</sub>	$V_{GS} = 0 \text{ V, } f = 1 \text{ MHz,}$	V <sub>DS</sub> = 800 V	-	1699	_	pF
Output Capacitance	C <sub>OSS</sub>			-	76	_	
Reverse Transfer Capacitance	C <sub>RSS</sub>			-	8	_	
Total Gate Charge	Q <sub>G(TOT)</sub>	$V_{GS} = -3/18 \text{ V}, V_{DS} = 800 \text{ V},$ $I_{D} = 30 \text{ A}$ $f = 1 \text{ MHz}$		-	70	_	nC
Threshold Gate Charge	Q <sub>G(TH)</sub>			-	9	_	1 1 1
Gate-to-Source Charge	$Q_{GS}$			-	20	_	
Gate-to-Drain Charge	$Q_{GD}$			-	23	_	
Gate-Resistance	$R_{G}$			-	1.12	_	Ω
SWITCHING CHARACTERISTICS				-	•	•	
Turn-On Delay Time	t <sub>d(ON)</sub>	$V_{GS} = -3/18 \text{ V}, V_{DS} =$	800 V,	-	11	_	ns
Rise Time	t <sub>r</sub>	$I_D$ = 30 A, $R_G$ = 4.5 $\Omega$ Inductive load (Note 3)	)	_	16	_	
Turn-Off Delay Time	t <sub>d(OFF)</sub>	· · · · ·		_	24	_	
Fall Time	t <sub>f</sub>	1		-	10	-	
Turn-On Switching Loss	E <sub>ON</sub>	-		-	179	-	μJ
Turn-Off Switching Loss	E <sub>OFF</sub>			-	96	-	
Total Switching Loss	E <sub>tot</sub>			-	275	-	
SOURCE-DRAIN DIODE CHARACTERIST	ICS	•					
Continuous Source-Drain Diode Forward Current	I <sub>SD</sub>	$V_{GS} = -3 \text{ V}, T_{C} = 25^{\circ}\text{C}$	;	-	-	54	Α
Pulsed Source-Drain Diode Forward Current (Note 2)	I <sub>SDM</sub>	1		-	-	185	
Forward Diode Voltage	$V_{SD}$	$V_{GS} = -3 \text{ V}, I_{SD} = 30 \text{ A}$	A, T <sub>J</sub> = 25°C	-	4.5	_	V

Table 2. ELECTRICAL CHARACTERISTICS ( $T_J = 25^{\circ}C$  unless otherwise specified) (continued)

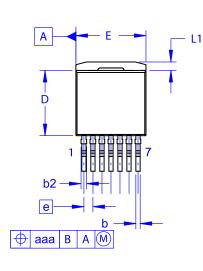
	\ 0	1 / \	,			
Parameter	Symbol	Test Condition	Min	Тур	Max	Unit
SOURCE-DRAIN DIODE CHARACTERIST	ics	•				
Reverse Recovery Time	t <sub>RR</sub>	$V_{GS} = -3/18 \text{ V}, I_{SD} = 30 \text{ A},$ $dI_S/dt = 1000 \text{ A}/\mu\text{s}, V_{DS} = 800 \text{ V}$	-	22	-	ns
Reverse Recovery Charge	$Q_{RR}$	di <sub>S</sub> /at = 1000 A/μs, V <sub>DS</sub> = 800 V	-	123	-	nC
Reverse Recovery Energy	E <sub>REC</sub>	1	-	4	-	μJ
Peak Reverse Recovery Current	I <sub>RRM</sub>		-	10	-	Α
Charge Time	T <sub>A</sub>	1	-	13	-	ns
Discharge Time	T <sub>B</sub>		-	9	-	ns

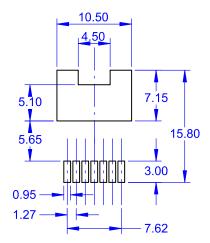
Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

3. E<sub>ON</sub>/E<sub>OFF</sub> result is with body diode

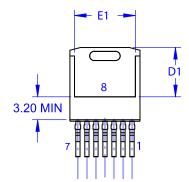
### **PACKAGE DIMENSIONS**

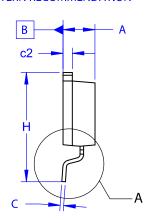
### D<sup>2</sup>PAK7 (TO-263-7L HV) CASE 418BJ **ISSUE B**





LAND PATTERN RECOMMENDATION





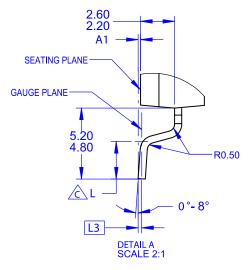
### NOTES:

- A. PACKAGE CONFORMS TO JEDEC TO-263 VARIATION CB EXCEPT WHERE NOTED. B. ALL DIMENSIONS ARE IN MILLIMETERS.
- OUT OF JEDEC STANDARD VALUE.

  D. DIMENSION AND TOLERANCE AS PER ASME
  Y14.5-2009.

  E. DIMENSIONS ARE EXCLUSIVE OF BURRS,
  MOLD FLASH AND TIE BAR PROTRUSIONS.

DIM	MILLIMETERS					
DIM	MIN	NOM	MAX			
Α	4.30	4.50	4.70			
A1	0.00	0.10	0.20			
b2	0.60	0.70	0.80			
b	0.51	0.60	0.70			
С	0.40	0.50	0.60			
c2	1.20	1.30	1.40			
D	9.00	9.20	9.40			
D1	6.15	6.80	7.15			
Е	9.70	9.90	10.20			
E1	7.15	7.65	8.15			
е	~	1.27	7			
Н	15.10	15.40	15.70			
L	2.44	2.64	2.84			
L1	1.00	1.20	1.40			
L3	~	0.25	~			
aaa	~	~	0.25			



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