

NTE454

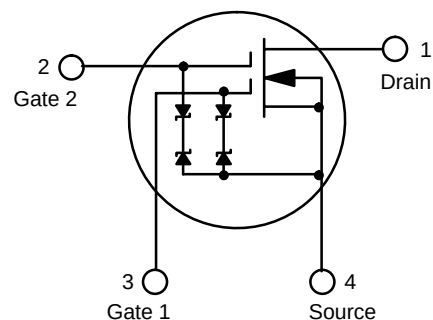
MOSFET, N-Ch, Dual Gate, TV UHF/RF Amp, Gate Protected

Description:

The NTE454 is a depletion mode dual gate MOSFET transistor designed for VHF amplifier and mixer applications.

Features:

- Low Reverse Transfer Capacitance – $C_{rss} = 0.03\text{pf}$ (Max)
- High Forward Transfer Admittance – $|y_{fe}| = 0\text{--}20\text{ mmhos}$
- Diode Protected Gates



Absolute Maximum Ratings:

Drain Source Voltage, V_{DSX}	20Vdc
Drain–Gate Voltage, V_{DG1}	30Vdc
V_{DG2}	30Vdc
Gate Current, I_{G1}	$\pm 10\text{mAdc}$
I_{G2}	$\pm 10\text{mAdc}$
Drain Current–Continuous, I_D	60mAdc
Total Power Dissipation ($T_A = +25^\circ\text{C}$), P_D	360mW
Derate above 25°C	$2.4\text{mW}/^\circ\text{C}$
Total Power Dissipation ($T_C = +25^\circ\text{C}$), P_D	1.2Watt
Derate above 25°C	$8.0\text{mW}/^\circ\text{C}$
Storage Channel Temperature Range, T_{stg}	-65 to $+200^\circ\text{C}$
Junction Temperature Range, T_J	-65 to $+175^\circ\text{C}$
Lead Temperature, 1/16" from Seated Surface for 10 Seconds, T_L	300°C

Electrical Characteristics: ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Characteristics	Symbol	Test Conditions	Min	Typ	Max	Unit
OFF CHARACTERISTICS						
Drain–Source Breakdown Voltage	V _{(BR)DSX}	I _D = 10μAdc, V ₅ = 0, V _{GIS} = V _{G25} = 5.0Vdc	20	–	–	Vdc
Gate 1= Source Breakdown Voltage (Note 1)	V _{(BR)G1SO}	I _{G1} = ±10mAdc, V _{GIS} = V _{DS} = 0	±6.0	±12	±30	Vdc
Gate 2–Source Breakdown Voltage (Note 1)	V _{(BR)G2SO}	I _{G2} = ±10mAdc, V _{G15} = V _{D5} = 0	±5.0	±12	±30	Vdc
Gate 1 to Source Cutoff Voltage	V _{GIS(off)}	V _{DS} = 15Vdc, V _{G2S} = 4.0Vdc, I _D = 20μAdc	–0.5	–1.5	–5.0	Vdc
Gate 2 to Source Cutoff Voltage	V _{G2S(off)}	V _{DS} = 15Vdc, V _{G15} = 0, I _D = 20μAdc	–0.2	–1.4	–5.0	Vdc
Gate 1 Leakage Current	I _{G1SS}	V _{GIS} = ±5.0Vdc, V _{G2S} = V _{DS} = 0	–	±0.04	±10	nAdc
		V _{G2S} = –5.0Vdc, V _{G2S} = V _{DS} = 0, T _A = 150°C	–	–	–10	μAdc
Gate 2 Leakage Current	I _{G2SS}	V _{G2S} = ±5.0Vdc, V _{GIS} = V _{DS} = 0	–	±0.05	±10	nAdc
		V _{G2S} = –5.0Vdc, V _{GIS} = V _{DS} = 0, T _A = 150°C	–	–	–10	μAdc
ON CHARACTERISTICS						
Zero–Gate Voltage Drain Current (Note 2)	I _{DSS}	V _{DS} = 15Vdc, V _{GIS} = 0, V _{G25} = 4.0Vdc	6.0	13	30	mAdc
SMALL–SIGNAL CHARACTERISTICS						
Forward Transfer Admittance (Note 3)	y _{fe}	V _{DS} = 15Vdc, V _{G2S} = 4.0Vdc, V _{GIS} = 0, f = 1.0kHz	8.0	12.8	20	mmhos
Input Capacitance	C _{iss}	V _{DS} = 15Vdc, V _{G2S} = 4.0Vdc, I _D = I _{DSS} , f = 1.0MHz	–	3.3	–	pF
Output Capacitance	C _{oss}	V _{DS} = 15Vdc, V _{G2S} = 4.0Vdc, I _D = I _{DSS} , f = 1.0MHz	–	1.7	–	pF
Reverse Transfer Capacitance	C _{rss}	V _{DS} = 15Vdc, V _{G2S} = 4.0Vdc, I _D = 10mAdc, f = 1.0MHz	0.005	0.014	0.03	pF
FUNCTIONAL CHARACTERISTICS						
Noise Figure	NF	V _{DD} = 18Vdc, V _{GG} = 7.0Vdc, f = 200MHz	–	1.8	4.5	dB
Common Source Power Gain	G _{ps}	V _{DD} = 18Vdc, V _{GG} = 7.0Vdc, f = 200MHz	15	20	25	dB
Bandwidth	BW	V _{DD} = 18Vdc, V _{GG} = 7.0Vdc, f = 200MHz	5.0	–	9.0	MHz
Gain Control Gate Supply Voltage (Note 4)	V _{GG(GC)}	V _{DD} = 18Vdc, ΔG _{ps} = –30dB, f = 200MHz	0	–1.0	–3.0	Vdc

Note 1. All gate breakdown voltages are measured while the device is conducting rated gate current. This ensures that the gate–voltage limiting network is functioning properly.

Note 2. Pulse Test: Pulse Width = $300\mu\text{s}$, Duty Cycle $\leq 2.0\%$

Note 3. This parameter must be measured with bias voltages supplied for less than 6 seconds to avoid overheating.

Note 4. ΔG_{ps} is defined as the change in G_{pe} from the values at $V_{GG} = 7.0\text{V}$ power gain conversion

