

MOSFET – SiC Power, Single N-Channel, TO247-3L

900 V, 20 mΩ, 118 A

NTHL020N090SC1

Features

- Typ. $R_{DS(on)} = 20 \text{ m}\Omega$
- Ultra Low Gate Charge ($Q_{G(tot)} = 196 \text{ nC}$)
- Low Effective Output Capacitance ($C_{oss} = 296 \text{ pF}$)
- 100% UIL Tested
- RoHS Compliant

Typical Applications

- UPS
- DC/DC Converter
- Boost Inverter

MAXIMUM RATINGS ($T_J = 25^\circ\text{C}$ unless otherwise noted)

Parameter			Symbol	Value	Unit
Drain-to-Source Voltage			V _{DSS}	900	V
Gate-to-Source Voltage			V _{GS}	+19/-10	V
Recommended Operation Values of Gate – Source Voltage		T _C < 175°C	V _{GSop}	+15/-5	V
Continuous Drain Current R _{θJC}	Steady State	T _C = 25°C	I _{DC}	118	A
Power Dissipation R _{θJC}			P _{DC}	503	W
Continuous Drain Current R _{θJC}	Steady State	T _C = 100°C	I _{DC}	83	A
Power Dissipation R _{θJC}			P _{DC}	251	W
Pulsed Drain Current (Note 2)		T _A = 25°C	I _{DM}	472	A
Single Pulse Surge Drain Current Capability (Note 3)	T _A = 25°C, t _p = 10 μs, R _G = 4.7 Ω		I _{DSC}	854	A
Operating Junction and Storage Temperature Range			T _J , T _{stg}	-55 to +175	°C
Source Current (Body Diode)			I _S	153	A
Single Pulse Drain-to-Source Avalanche Energy (I _L = 23 A _{pk} , L = 1 mH) (Note 4)			E _{AS}	264	mJ

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

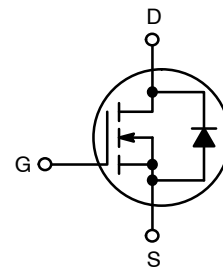
1. The entire application environment impacts the thermal resistance values shown, they are not constants and are only valid for the particular conditions noted.
2. Repetitive rating, limited by max junction temperature.
3. Peak current might be limited by transconductance.
4. E_{AS} of 162 mJ is based on starting $T_J = 25^\circ\text{C}$; $L = 1 \text{ mH}$, $I_{AS} = 23 \text{ A}$, $V_{DD} = 100 \text{ V}$, $V_{GS} = 15 \text{ V}$.



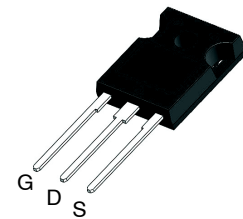
ON Semiconductor®

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$V_{(BR)DSS}$	$R_{DS(on)} \text{ MAX}$	$I_D \text{ MAX}$
900 V	28 mΩ @ 15 V	118 A

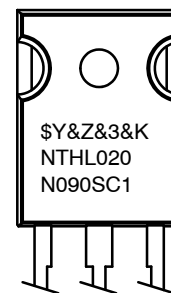


N-CHANNEL MOSFET



TO-247 LONG LEADS
CASE 340CX

MARKING DIAGRAM



$\$Y$ = ON Semiconductor Logo
 $\&Z$ = Assembly Plant Code
 $\&3$ = Date Code (Year & Week)
 $\&K$ = Lot
 NTHL020N090SC1 = Specific Device Code

ORDERING INFORMATION

See detailed ordering and shipping information on page 6 of this data sheet.

Table 1. THERMAL CHARACTERISTICS

Parameter	Symbol	Max	Units
Thermal Resistance Junction-to-Case (Note 1)	$R_{\theta JC}$	0.30	°C/W
Thermal Resistance Junction-to-Ambient (Note 1)	$R_{\theta JA}$	40	°C/W

Table 2. ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$ unless otherwise stated)

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
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OFF CHARACTERISTICS

Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0\text{ V}, I_D = 1\text{ mA}$	900			V
Drain-to-Source Breakdown Voltage Temperature Coefficient	$V_{(BR)DSS}/T_J$	$I_D = 1\text{ mA}$, refer to 25°C		500		mV/°C
Zero Gate Voltage Drain Current	I_{DSS}	$V_{GS} = 0\text{ V}$ $V_{DS} = 900\text{ V}$	$T_J = 25^\circ\text{C}$		100	μA
			$T_J = 175^\circ\text{C}$		250	μA
Gate-to-Source Leakage Current	I_{GSS}	$V_{GS} = +19/-10\text{ V}, V_{DS} = 0\text{ V}$			± 1	μA

ON CHARACTERISTICS

Gate Threshold Voltage	$V_{GS(TH)}$	$V_{GS} = V_{DS}, I_D = 20\text{ mA}$	1.8	2.7	4.3	V
Recommended Gate Voltage	V_{GOP}		-5		+15	V
Drain-to-Source On Resistance	$R_{DS(on)}$	$V_{GS} = 15\text{ V}, I_D = 60\text{ A}, T_J = 25^\circ\text{C}$		20	28	m Ω
		$V_{GS} = 15\text{ V}, I_D = 60\text{ A}, T_J = 175^\circ\text{C}$		27		m Ω
Forward Transconductance	g_{FS}	$V_{DS} = 20\text{ V}, I_D = 60\text{ A}$		49		S

CHARGES, CAPACITANCES & GATE RESISTANCE

Input Capacitance	C_{ISS}	$V_{GS} = 0\text{ V}, f = 1\text{ MHz},$ $V_{DS} = 450\text{ V}$		4415		pF
Output Capacitance	C_{OSS}			296		
Reverse Transfer Capacitance	C_{RSS}			24		
Total Gate Charge	$Q_{G(TOT)}$	$V_{GS} = -5/15\text{ V}, V_{DS} = 720\text{ V},$ $I_D = 60\text{ A}$		196		nC
Threshold Gate Charge	$Q_{G(TH)}$			42		
Gate-to-Source Charge	Q_{GS}			78		
Gate-to-Drain Charge	Q_{GD}			55		
Gate-Resistance	R_G	$f = 1\text{ MHz}$		1.6		Ω

SWITCHING CHARACTERISTICS

Turn-On Delay Time	$t_{d(ON)}$	$V_{GS} = -5/15\text{ V}, V_{DS} = 720\text{ V},$ $I_D = 60\text{ A}, R_G = 2.5\text{ }\Omega,$ Inductive Load		40		ns
Rise Time	t_r			63		
Turn-Off Delay Time	$t_{d(OFF)}$			55		
Fall Time	t_f			13		
Turn-On Switching Loss	E_{ON}			2025		μJ
Turn-Off Switching Loss	E_{OFF}			201		
Total Switching Loss	E_{TOT}			2226		

DRAIN-SOURCE DIODE CHARACTERISTICS

Continuous Drain-Source Diode Forward Current	I_{SD}	$V_{GS} = -5\text{ V}, T_J = 25^\circ\text{C}$			153	A
Pulsed Drain-Source Diode Forward Current (Note 2)	I_{SDM}	$V_{GS} = -5\text{ V}, T_J = 25^\circ\text{C}$			472	A
Forward Diode Voltage	V_{SD}	$V_{GS} = -5\text{ V}, I_{SD} = 30\text{ A}, T_J = 25^\circ\text{C}$		3.8		V

Table 2. ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$ unless otherwise stated)

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
DRAIN-SOURCE DIODE CHARACTERISTICS						
Reverse Recovery Time	t_{RR}	$V_{GS} = -5/15\text{ V}$, $I_{SD} = 60\text{ A}$, $di_S/dt = 1000\text{ A}/\mu\text{s}$, $V_{DS} = 720\text{ V}$		28		ns
Reverse Recovery Charge	Q_{RR}			199		nC
Reverse Recovery Energy	E_{REC}			4		μJ
Peak Reverse Recovery Current	I_{RRM}			14		A
Charge time	T_a			16		ns
Discharge time	T_b			12		ns

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

TYPICAL CHARACTERISTICS

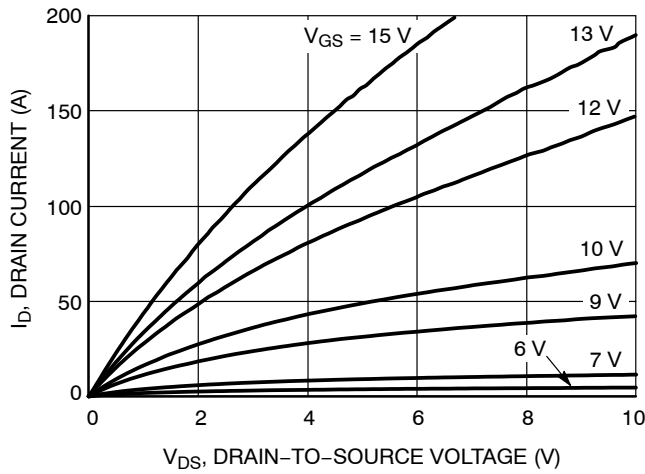


Figure 1. On-Region Characteristics

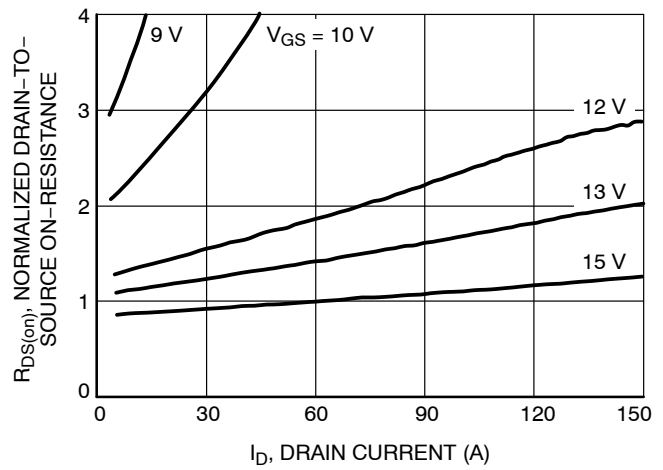


Figure 2. Normalized On-Resistance vs. Drain Current and Gate Voltage

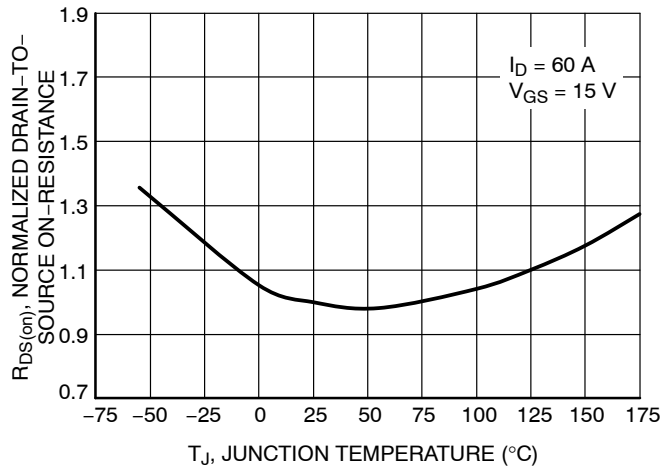


Figure 3. On-Resistance Variation with Temperature

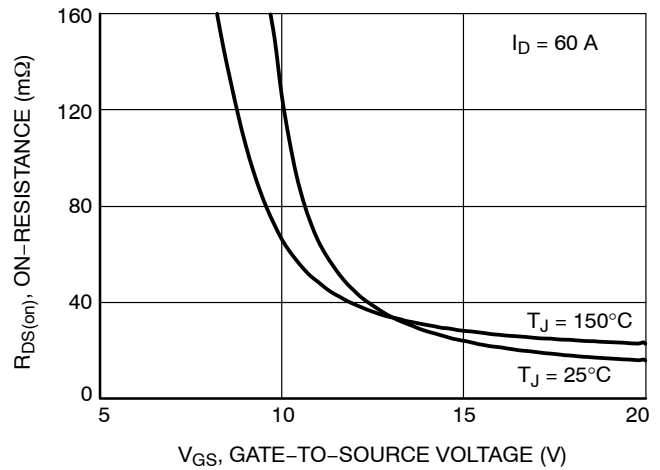


Figure 4. On-Resistance vs. Gate-to-Source Voltage

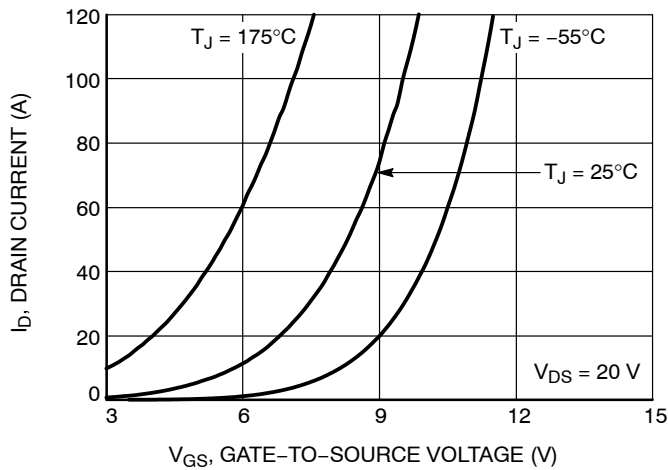


Figure 5. Transfer Characteristics

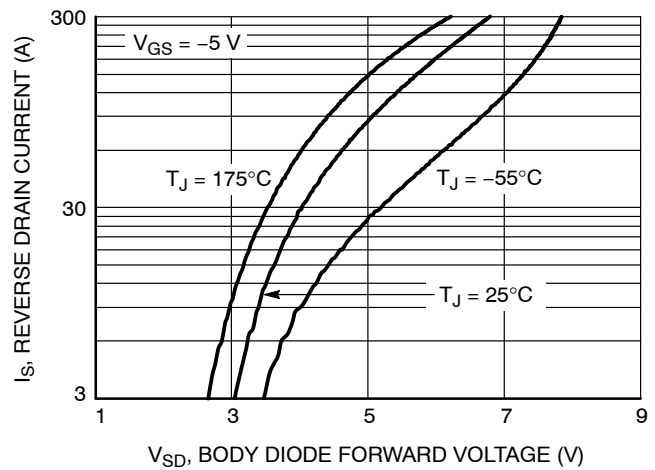


Figure 6. Diode Forward Voltage vs. Current

TYPICAL CHARACTERISTICS

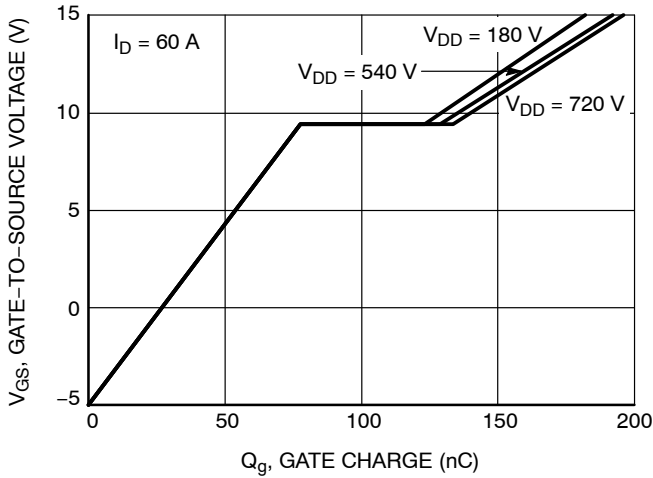


Figure 7. Gate-to-Source Voltage vs. Total Charge

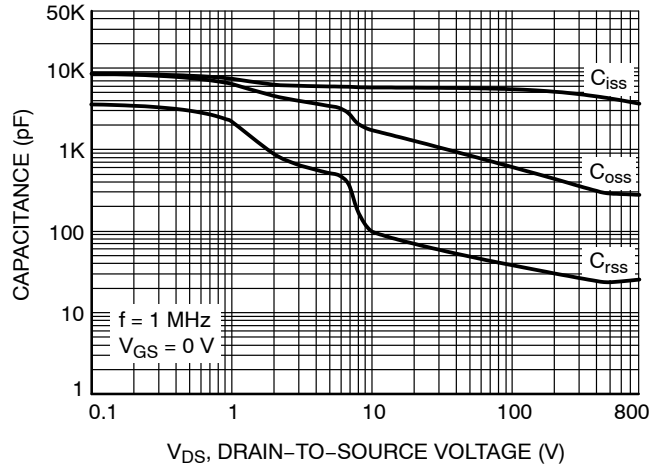


Figure 8. Capacitance vs. Drain-to-Source Voltage

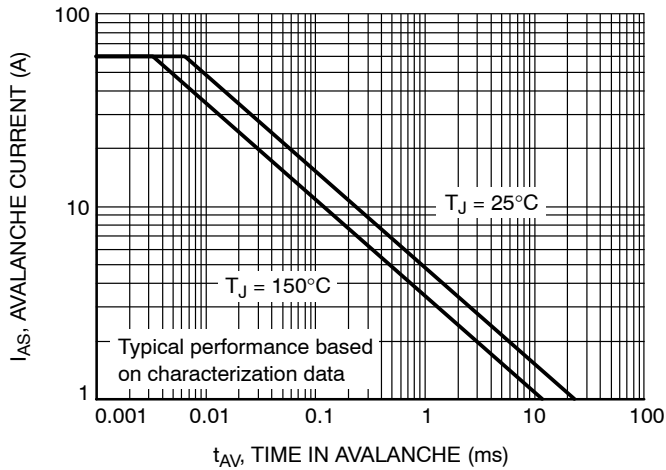


Figure 9. Unclamped Inductive Switching Capability

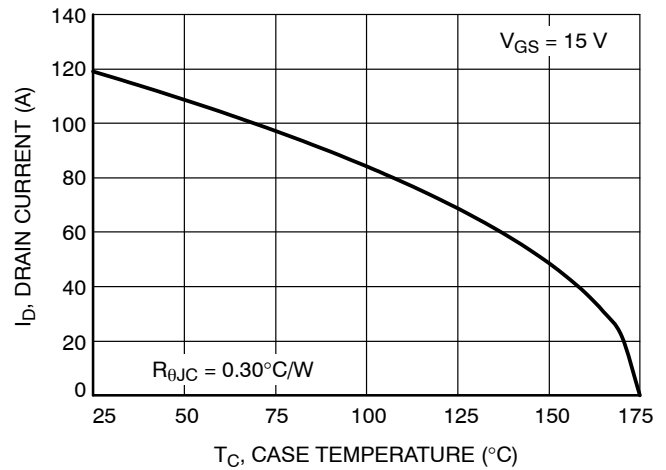


Figure 10. Maximum Continuous Drain Current vs. Case Temperature

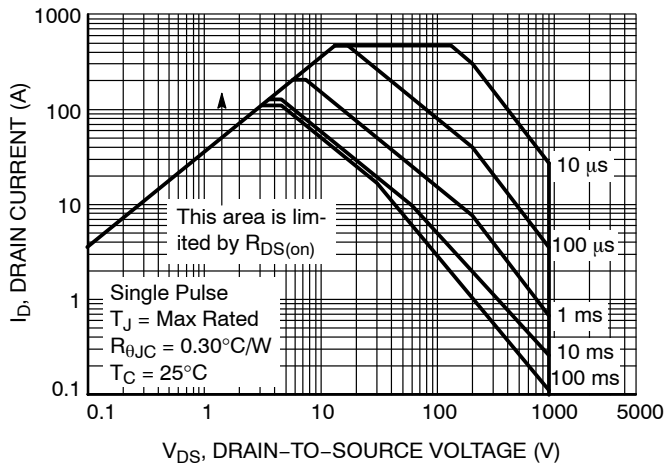


Figure 11. Safe Operating Area

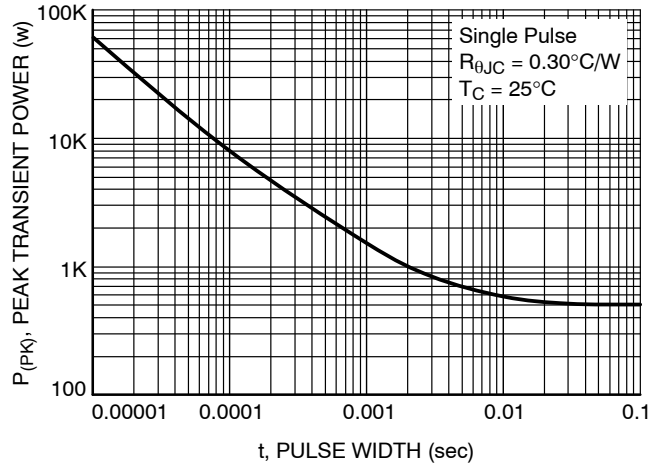


Figure 12. Single Pulse Maximum Power Dissipation

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TYPICAL CHARACTERISTICS

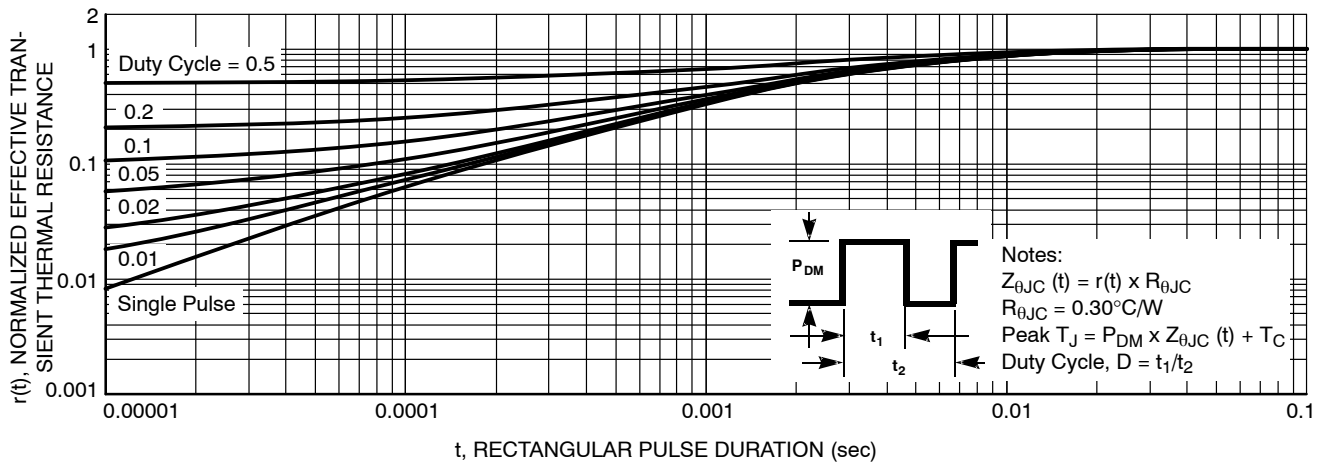


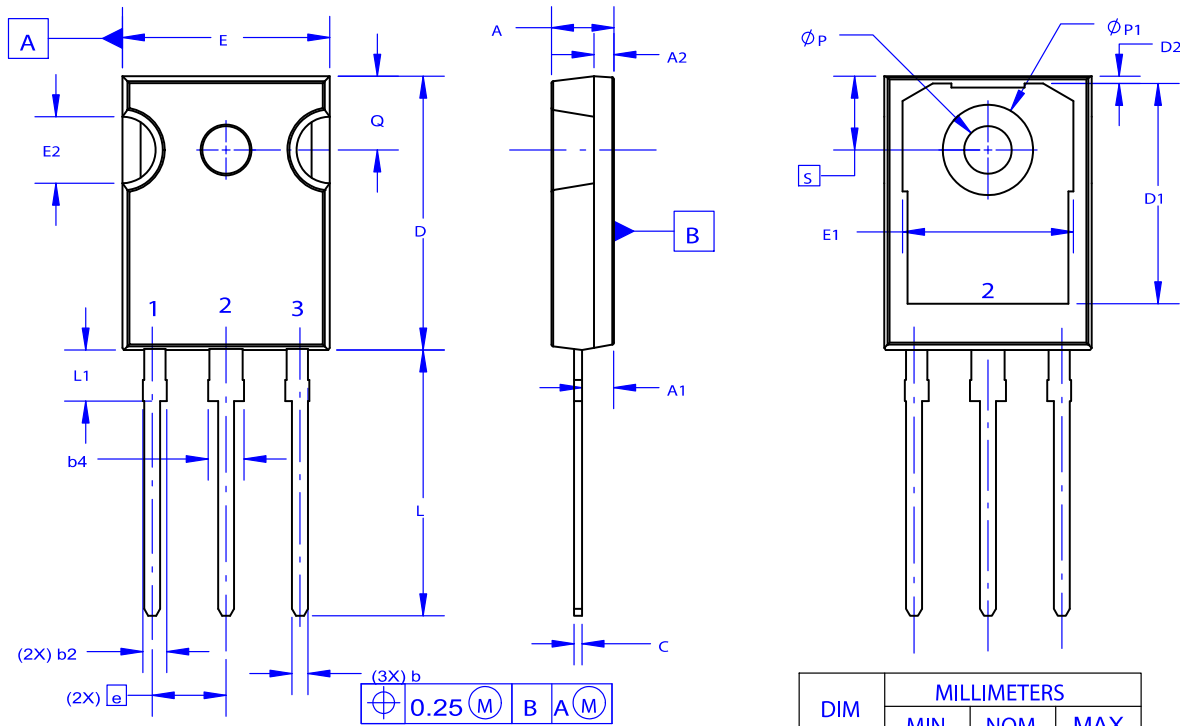
Figure 13. Junction-to-Ambient Transient Thermal Response Curve

PACKAGE MARKING AND ORDERING INFORMATION

Part Number	Top Marking	Package	Packing Method	Reel Size	Tape Size	Quantity
NTHL020N090SC1	NTHL020N090SC1	TO-247 Long Lead	Tube	N/A	N/A	30 Units

PACKAGE DIMENSIONS

TO-247-3LD
CASE 340CX
ISSUE O



NOTES: UNLESS OTHERWISE SPECIFIED.

A. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.

B. ALL DIMENSIONS ARE IN MILLIMETERS.

C. DRAWING CONFORMS TO ASME Y14.5 - 2009.

D. DIMENSION A1 TO BE MEASURED IN THE REGION DEFINED BY L1.

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