Power MOSFET and Schottky Diode

30 V, 5.7 A, Single N-Channel with 30 V, 2.8 A, Schottky Barrier Diode

Features

- FETKY™ Surface Mount Package Saves Board Space
- Independent Pin-Out for MOSFET and Schottky Allowing for Design Flexibility
- Low R_{DS(on)} MOSFET and Low V_F Schottky to Minimize Conduction Losses
- Optimized Gate Charge to Minimize Switching Losses
- This is a Pb-Free Device

Applications

- Disk Drives
- DC-DC Converters
- Printers

MOSFET MAXIMUM RATINGS (T_J = 25°C unless otherwise stated)

Ratir	Symbol	Value	Unit		
Drain-to-Source Voltage	V _{DSS}	30	V		
Gate-to-Source Voltage	Gate-to-Source Voltage				
Continuous Drain		T _A = 25°C	I _D	4.7	Α
Current R _{θJA} (Note 1)		T _A = 70°C		3.8	
Power Dissipation R ₀ JA (Note 1)		T _A = 25°C	P _D	1.6	W
Continuous Drain		T _A = 25°C	I _D	3.3	Α
Current R _{θJA} (Note 2)	Steady	T _A = 70°C		2.6	
Power Dissipation R ₀ JA (Note 2)	State	T _A = 25°C	P _D	0.77	W
Continuous Drain		T _A = 25°C	I _D	5.7	Α
Current R _{0JA} t < 10 s (Note 1)		T _A = 70°C		4.5	
Power Dissipation R _{θJA} t < 10 s (Note 1)		T _A = 25°C	P _D	2.3	W
Pulsed Drain Current		= 25°C, = 10 μs	I _{DM}	19	Α
Operating Junction and	T _J , T _{STG}	-55 to +150	ç		
Source Current (Body D	I _S	1.3	Α		
Lead Temperature for So (1/8" from case for 10 s)	TL	260	°C		

SCHOTTKY MAXIMUM RATINGS (T_J = 25°C unless otherwise stated)

Peak Repetitive Reverse Voltage	V_{RRM}	30	V	
DC Blocking Voltage	V _R	30	٧	
Average Rectified Forward Current, (Note 1)	I _F	2.8	Α	
	t < 10 s		4.1	



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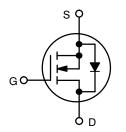
http://onsemi.com

N-CHANNEL MOSFET

V _{(BR)DSS}	R _{DS(on)} Max	I _D Max		
30 V	48 mΩ @ 10 V	5.7 A		
	70 mΩ @ 4.5 V	5 / (

SCHOTTKY DIODE

V _R Max	V _F Max	I _F Max		
30 V	0.5 V	2.8 A		





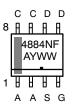
N-Channel MOSFET

Schottky Diode

MARKING DIAGRAM & PIN ASSIGNMENT



SOIC-8 CASE 751 STYLE 18



4884NF = Device Code

A = Assembly Location
Y = Year
WW = Work Week

= Pb-Free Package

ORDERING INFORMATION

Device	Device Package	
NTMD4884NFR2G	SOIC-8 (Pb-Free)	2500/Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

THERMAL RESISTANCE MAXIMUM RATINGS

Parameter MOSFET & Schottky	Symbol	Max	Unit
Junction-to-Ambient - Steady State (Note 1)	$R_{ hetaJA}$	79	
Junction-to-Ambient - t ≤10 s Steady State (Note 1)	$R_{ heta JA}$	54	°C/W
Junction-to-FOOT (Drain) Equivalent to R _{θJC}	$R_{ heta JF}$	50	-C/VV
Junction-to-Ambient - Steady State (Note 2)	$R_{ heta JA}$	163	

$\textbf{ELECTRICAL CHARACTERISTICS} \ (T_J = 25^{\circ}C \ unless \ otherwise \ noted)$

Characteristic	Symbol	Test Cor	ndition	Min	Тур	Max	Unit
OFF CHARACTERISTICS	•					•	
Drain-to-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0 \text{ V}, I_{D}$) = 250 μΑ	30			V
Drain-to-Source Breakdown Voltage Temperature Coefficient	V _{(BR)DSS} /T _J				24		mV/°C
Zero Gate Voltage Drain Current	I _{DSS}	V _{GS} = 0 V,	T _J = 25°C			1.0	
		$V_{DS} = 24 \text{ V}$	T _J = 125°C			20	μΑ
Gate-to-Source Leakage Current	I _{GSS}	$V_{DS} = 0 V, V_0$	_{GS} = ±20 V			±100	nA
ON CHARACTERISTICS (Note 3)					_		
Gate Threshold Voltage	V _{GS(TH)}	V _{GS} = V _{DS} , I _I	ο = 250 μΑ	1.0		2.5	V
Negative Threshold Temperature Coefficient	V _{GS(TH)} /T _J				5.0		mV/°C
Drain-to-Source On Resistance	R _{DS(on)}	V _{GS} = 10 V	I _D = 4.0 A		34	48	0
	, ,	V _{GS} = 4.5 V	I _D = 3.5 A		50	70	mΩ
Forward Transconductance	9FS	$V_{DS} = 5.0 V,$	I _D = 4.0 A		10		S
Gate Resistance	R _G				2.4	3.6	Ω
CHARGES, CAPACITANCES AND GATE RE	SISTANCE					•	
Input Capacitance	C _{ISS}				280	360	pF
Output Capacitance	C _{OSS}	$V_{GS} = 0 \text{ V, f} = V_{DS} = 0 \text{ V}$: 1.0 MHz,		60	80	
Reverse Transfer Capacitance	C _{RSS}	VDS -	15 V		32	42	
Total Gate Charge	Q _{G(TOT)}				2.8	4.2	
Threshold Gate Charge	Q _{G(TH)}	V _{GS} = 4.5 V, V	/ _{DS} = 15 V,		0.4		nC
Gate-to-Source Charge	Q _{GS}	I _D = 4.	.0 A		1.2		
Gate-to-Drain Charge	Q_{GD}				1.0		
Total Gate Charge	Q _{G(TOT)}	V _{GS} = 10 V, V I _D = 4.			5.6	8.0	nC
SWITCHING CHARACTERISTICS (Note 4)					•	•	•
Turn-On Delay Time	t _{d(ON)}				6.0	12	
Rise Time	t _r	V _{GS} = 10 V, V	ne = 15 V.		6.5	13	┪
Turn-Off Delay Time	t _{d(OFF)}	$I_D = 1.0 \text{ A}, R_G = 6.0 \Omega$			14	26	ns
Fall Time	t _f				1.4	7.0	1
DRAIN-TO-SOURCE CHARACTERISTICS						1	
Forward Diode Voltage	V_{SD}	V _{GS} = 0 V	T _J = 25°C		0.8	1.0	V
Ç		$I_D = 1.3 A$	T _J = 125°C		0.65		
Reverse Recovery Time	t _{RR}	1 .,0 0			9.2	20	1
Charge Time	t _a	$V_{GS} = 0 V, d_{IS}/d$	+ = 100 A/us		6.0		ns
Discharge Time	t _b	$I_S = 4$.0 A		3.2		1
Reverse Recovery Time	Q _{RR}				3.3		nC

Surface-mounted on FR4 board using 1 inch sq pad size, 1 oz Cu.
 Surface-mounted on FR4 board using the minimum recommended pad size.

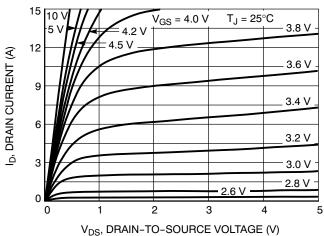
ELECTRICAL CHARACTERISTICS (T_J = 25°C unless otherwise noted)

Characteristic	Symbol		Test Condition	Min	Тур	Max	Unit
 CHOTTKY DIODE ELECTRICAL CHAI	0500	0					

SCHOTTKY DIODE ELECTRICAL CHARACTERISTICS (T_J = 25°C unless otherwise noted)

Parameter	Symbol	Test Con	ditions	Min	Тур	Max	Unit	
Maximum Instantaneous	V _F	I _F = 0.1 A	T _J = 25°C		0.26	0.28	V	
Forward Voltage			T _J = 125°C		0.11	0.13		
		I _F = 2.0 A	T _J = 25°C		0.4	0.50	ataSheet	4U.co
			T _J = 125°C		0.35	0.46		
Maximum Instantaneous	I _R	V _R = 10 V	T _J = 25°C		0.020	0.25	mA	
Reverse Current			T _J = 125°C		10	37		

^{3.} Pulse Test: pulse width \leq 300 μ s, duty cycle \leq 2%.



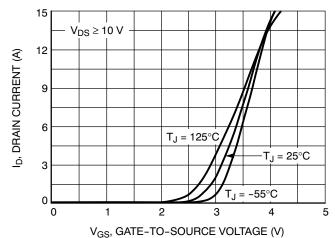


Figure 2. Transfer Characteristics



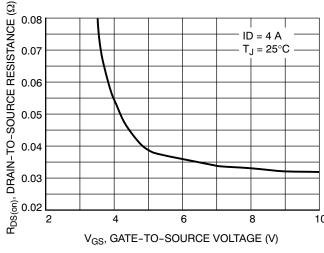


Figure 3. On-Resistance vs. Gate Voltage

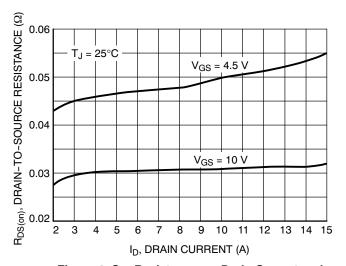


Figure 4. On-Resistance vs. Drain Current and **Gate Voltage**

^{4.} Switching characteristics are independent of operating junction temperatures.

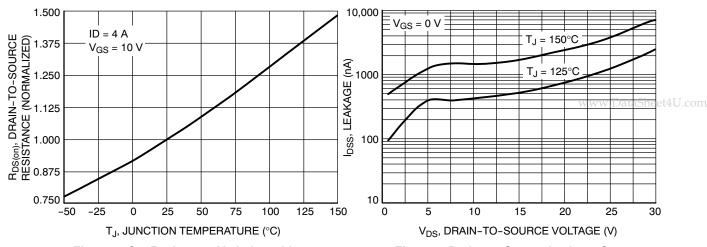


Figure 5. On–Resistance Variation with Temperature

Figure 6. Drain-to-Source Leakage Current vs. Voltage

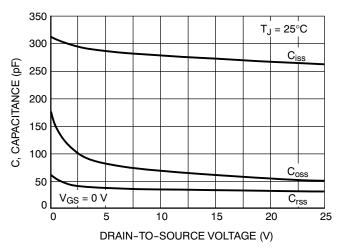


Figure 7. Capacitance Variation

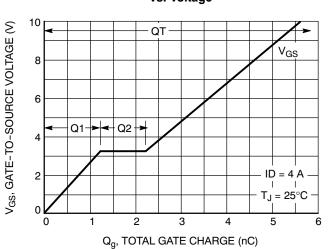


Figure 8. Gate-to-Source and Drain-to-Source Voltage vs. Total Charge

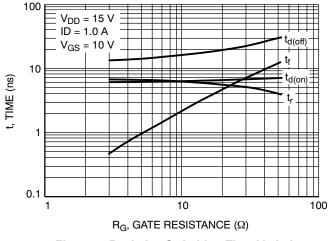


Figure 9. Resistive Switching Time Variation vs. Gate Resistance

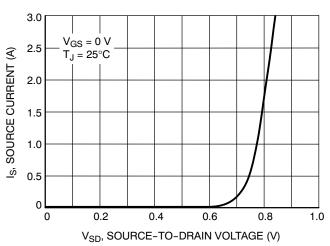


Figure 10. Diode Forward Voltage vs. Current

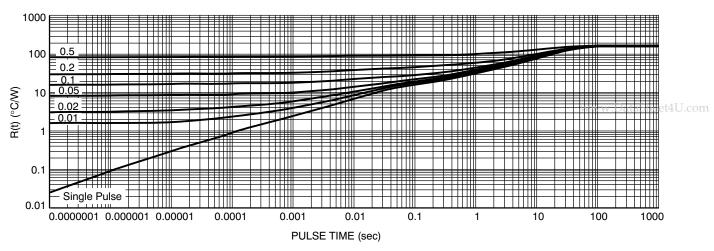


Figure 11. Thermal Response – $R_{\theta JA}$ at Steady State (min pad)

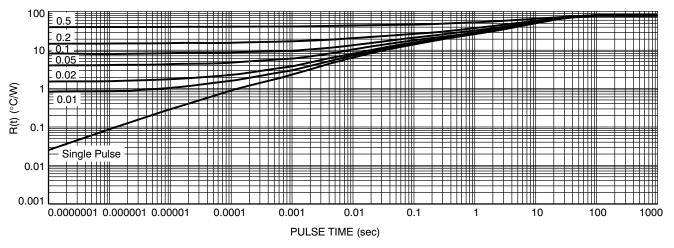


Figure 12. Thermal Response – $R_{\theta JA}$ at Steady State (1 inch sq pad)

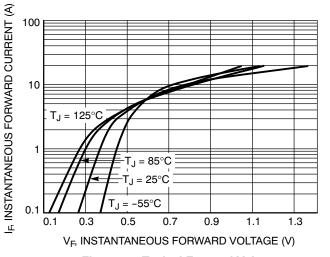


Figure 13. Typical Forward Voltage

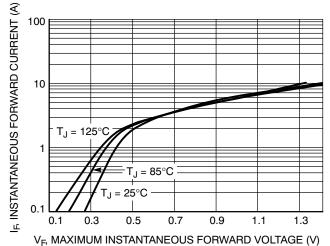


Figure 14. Maximum Forward Voltage

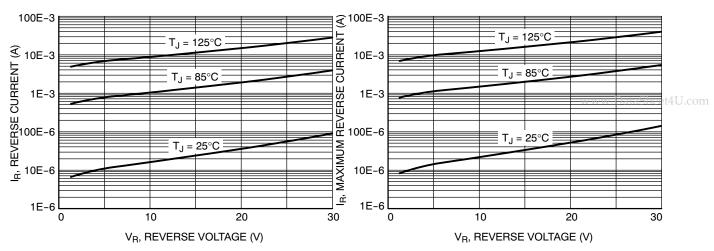


Figure 15. Typical Reverse Current

Figure 16. Maximum Reverse Current

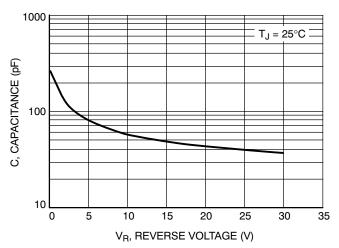
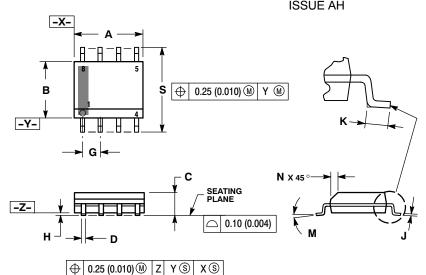


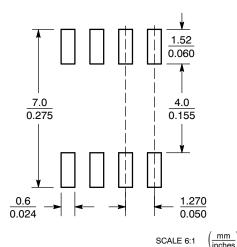
Figure 17. Capacitance

PACKAGE DIMENSIONS

SOIC-8 NB CASE 751-07



SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

NOTES

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 CONTROLLING DIMENSION: MILLIMETER.
- DIMENSION A AND B DO NOT INCLUDE MOLD PROTRUSION
- MAXIMUM MOLD PROTRUSION 0.15 (0.006) eet4U.com PER SIDE
- DIMENSION D DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.127 (0.005) TOTAL IN EXCESS OF THE D DIMENSION AT MAXIMUM MATERIAL CONDITION.
- 751-01 THRU 751-06 ARE OBSOLETE. NEW STANDARD IS 751-07.

	MILLIN	IETERS	INC	HES
DIM	MIN	MIN MAX		MAX
Α	4.80	5.00	0.189	0.197
В	3.80	4.00	0.150	0.157
၁	1.35	1.75	0.053	0.069
D	0.33	0.51	0.013	0.020
G	1.27	1.27 BSC		0 BSC
Н	0.10	0.25	0.004	0.010
Ĺ	0.19	0.25	0.007	0.010
K	0.40	1.27	0.016	0.050
M	0 °	8 °	0 °	8 °
N	0.25	0.50	0.010	0.020
S	5.80	6.20	0.228	0.244

STYLE 18: PIN 1. ANODE

- 2
- ANODE SOURCE 3.
- **GATE**
- DRAIN
- DRAIN 6
- CATHODE
- CATHODE

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