Low Forward Voltage, Low Leakage Trench-based Schottky Rectifier

Features

- Fine Lithography Trench-based Schottky Technology for Very Low Forward Voltage and Low Leakage
- Fast Switching with Exceptional Temperature Stability
- Low Power Loss and Lower Operating Temperature
- Higher Efficiency for Achieving Regulatory Compliance
- High Surge Capability
- These are Pb-Free and Halide-Free Devices

Typical Applications

- Switching Power Supplies including Wireless, Smartphone and Notebook Adapters
- High Frequency and DC-DC Converters
- Freewheeling and OR-ing diodes
- Reverse Battery Protection
- Instrumentation
- LED Lighting

Mechanical Characteristics:

- Case: Epoxy, Molded
- Epoxy Meets Flammability Rating UL 94–0 @ 0.125 in.
- Lead Finish: 100% Matte Sn (Tin)
- Lead and Mounting Surface Temperature for Soldering Purposes: 260°C Max. for 10 Seconds
- Device Meets MSL 1 Requirements



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SCHOTTKY BARRIER RECTIFIERS 3 AMPERES 100 VOLTS

MARKING DIAGRAM



SMB CASE 403A



TH31 = Specific Device Code A = Assembly Location

Y = Year

WW = Work Week ■ Pb–Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

Device	Package	Shipping†	
NTSS3100T3G	SMB (Pb-Free)	5000 / Tape & Reel	

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

MAXIMUM RATINGS

Rating	Symbol	Value	Unit	
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _R	100	V	
Average Rectified Forward Current (T _L = 116°C)	I _{F(AV)}	3.0	А	
Peak Repetitive Forward Current, (Square Wave, 20 kHz, T _L = 109°C)	I _{FRM}	6	А	
Non-Repetitive Peak Surge Current (Surge Applied at Rated Load Conditions Halfwave, Single Phase, 60 Hz)	I _{FSM}	50	А	
Storage Temperature Range	T _{stg}	-65 to +150	°C	
Operating Junction Temperature	TJ	-55 to +150	°C	
ESD Rating (Human Body Model)		1B		
ESD Rating (Machine Model)		M3		

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS

Characteristic	Symbol	Тур	Max	Unit
Thermal Resistance, Junction-to-Lead, Steady State (Assumes 600 mm ² 1 oz. copper bond pad, on a FR4 board)	$R_{ heta JL}$	-	17.4	°C/W

ELECTRICAL CHARACTERISTICS

Instantaneous Forward Voltage (Note 1) ($i_F = 3.0 \text{ Amps}, T_J = 25^{\circ}\text{C}$) ($i_F = 3.0 \text{ Amps}, T_J = 125^{\circ}\text{C}$)	٧ _F	0.874 0.66	0.995 0.685	V
Reverse Current (Note 1) (Rated dc Voltage, T _J = 25°C) (Rated dc Voltage, T _J = 125°C)	İR	2.4 1.0	13 3	μA mA
Diode Capacitance (Rated dc Voltage, T _J = 25°C, f = 1 MHz)	C _d	14.3		pF

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

^{1.} Pulse Test: Pulse Width = 300 μ s, Duty Cycle \leq 2.0%.

TYPICAL CHARACTERISTICS

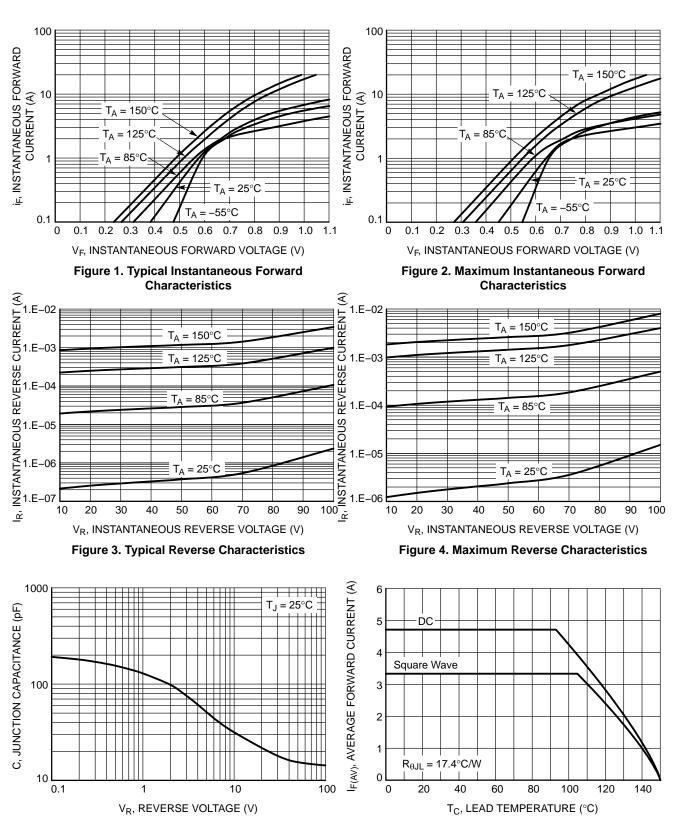


Figure 5. Typical Junction Capacitance

TYPICAL CHARACTERISTICS

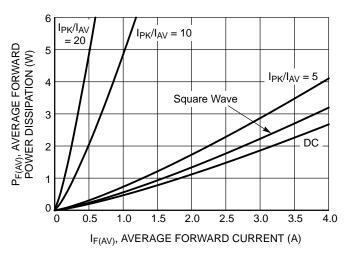


Figure 7. Forward Power Dissipation

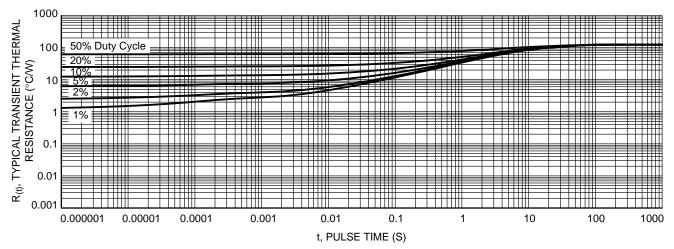
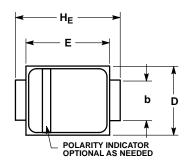
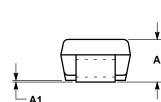


Figure 8. Typical Transient Thermal Response, Junction-to-Ambient

PACKAGE DIMENSIONS

SMB CASE 403A-03 ISSUE J



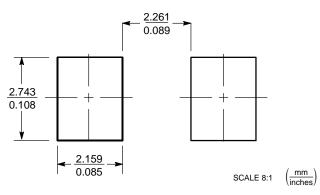


IOTES:

- 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- 2. CONTROLLING DIMENSION: INCH.
- 3. DIMENSION b SHALL BE MEASURED WITHIN DIMENSION L1.

	MILLIMETERS			INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	1.95	2.30	2.47	0.077	0.091	0.097
A1	0.05	0.10	0.20	0.002	0.004	0.008
b	1.96	2.03	2.20	0.077	0.080	0.087
С	0.15	0.23	0.31	0.006	0.009	0.012
D	3.30	3.56	3.95	0.130	0.140	0.156
E	4.06	4.32	4.60	0.160	0.170	0.181
HE	5.21	5.44	5.60	0.205	0.214	0.220
L	0.76	1.02	1.60	0.030	0.040	0.063
L1	0.51 REF			0.020 REF		

SOLDERING FOOTPRINT*



*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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