

Silicon Carbide (SiC) MOSFET - EliteSiC, 40 mohm, 1200 V, M3S, TO-247-4L

NVH4L040N120M3S

Features

- Typ. $R_{DS(on)} = 40 \text{ m}\Omega @ V_{GS} = 18 \text{ V}$
- Ultra Low Gate Charge $(Q_{G(tot)} = 75 \text{ nC})$
- High Speed Switching with Low Capacitance (Coss = 80 pF)
- 100% Avalanche Tested
- AEC-Q101 Qualified and PPAP Capable
- This Device is Halide Free and RoHS Compliant with exemption 7a, Pb-Free 2LI (on second level interconnection)

Typical Applications

- Automotive On Board Charger
- Automotive DC-DC Converter for EV/HEV

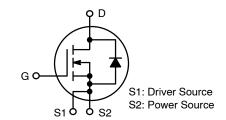
MAXIMUM RATINGS (T_J = 25°C unless otherwise noted)

Parameter			Symbol	Value	Unit
Drain-to-Source Voltage			V_{DSS}	1200	V
Gate-to-Source Voltage			V_{GS}	-10/+22	٧
Recommended Operation Values of Gate-to-Source Voltage		V_{GSop}	-3/+18	>	
Continuous Drain Current (Notes 1, 3)	Steady State	T _C =25°C	I _D	54	Α
Power Dissipation (Note 1)			P _D	231	V
Continuous Drain Current (Notes 1, 3)	Steady State	T _C =100°C	I _D	38	Α
Power Dissipation (Note 1)			P _D	115	W
Pulsed Drain Current (Note 2)	T _C	= 25°C	I _{DM}	134	Α
Operating Junction and Storage Temperature Range			T _J , T _{stg}	-55 to +175	°C
Source Current (Body Diode) T _C = 25°C, V _{GS} = -3 V			I _S	45	Α
Single Pulse Drain-to-Source Avalanche Energy (Note 4)			E _{AS}	143	mJ
Maximum Lead Temperature for Soldering (1/25" from case for 10 s)			TL	270	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

- The entire application environment impacts the thermal resistance values shown, they are not constants and are only valid for the particular conditions noted.
- 2. Repetitive rating, limited by max junction temperature.
- 3. The maximium current rating is based on typical R_{DS(on)} performance.
- 4. EAS of 143 mJ is based on starting $T_J = 25^{\circ}\text{C}$; L = 1 mH, $I_{AS} = 16.9 \text{ A}$, $V_{DD} = 100 \text{ V}$, $V_{GS} = 18 \text{ V}$.

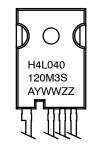
V _{(BR)DSS}	R _{DS(ON)} MAX	I _D MAX
1200 V	54 mΩ @ 18 V	54 A



N-CHANNEL MOSFET



MARKING DIAGRAM



H4L040120M3S = Specific Device Code

A = Assembly Location

Y = Year WW = Work Week ZZ = Lot Traceability

ORDERING INFORMATION

Device	Package	Shipping
NVH4L040N120M3S	TO247-4L	30 Units / Tube

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Table 1. THERMAL CHARACTERISTICS

Parameter		Max	Unit
Junction-to-Case - Steady State (Note 1)		0.65	°C/W
Junction-to-Ambient - Steady State (Note 1)	$R_{ heta JA}$	40	

Table 2. ELECTRICAL CHARACTERISTICS (T_J = 25°C unless otherwise specified)

Parameter	Symbol	Test Condition	Min	Тур	Max	Unit
OFF-STATE CHARACTERISTICS						
Drain-to-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0 \text{ V}, I_D = 1 \text{ mA}$	1200	-	-	V
Drain-to-Source Breakdown Voltage Temperature Coefficient	V _{(BR)DSS} /T _J	I _D = 1 mA, referenced to 25°C (Note 6)	-	0.3	-	V/°C
Zero Gate Voltage Drain Current	I _{DSS}	V _{GS} = 0 V, V _{DS} = 1200 V	-	-	100	μΑ
Gate-to-Source Leakage Current	I _{GSS}	V _{GS} = +22/-10 V, V _{DS} = 0 V	-	-	±1	μΑ
ON-STATE CHARACTERISTICS (Note 2)						
Gate Threshold Voltage	V _{GS(TH)}	$V_{GS} = V_{DS}$, $I_D = 10 \text{ mA}$	2.04	2.9	4.4	V
Recommended Gate Voltage	V _{GOP}		-3	-	+18	V
Drain-to-Source On Resistance	R _{DS(on)}	V _{GS} = 18 V, I _D = 20 A, T _J = 25°C	-	40	54	mΩ
		V _{GS} = 18 V, I _D = 20 A, T _J = 175°C (Note 6)	-	80	-	
Forward Transconductance	9FS	V _{DS} = 10 V, I _D = 20 A (Note 6)	-	16	-	S
CHARGES, CAPACITANCES & GATE RE	SISTANCE			•		•
Input Capacitance	C _{ISS}	V _{GS} = 0 V, f = 1 MHz, V _{DS} = 800 V	-	1700	-	pF
Output Capacitance	C _{OSS}	(Note 6)	-	80	-	
Reverse Transfer Capacitance	C _{RSS}		-	7	-	
Total Gate Charge	Q _{G(TOT)}	$V_{GS} = -3/18 \text{ V}, V_{DS} = 800 \text{ V},$	-	75	-	nC
Threshold Gate Charge	Q _{G(TH)}	I _D = 20 A (Note 6)	-	4.4	-	
Gate-to-Source Charge	Q _{GS}		-	14	-	
Gate-to-Drain Charge	Q _{GD}		-	22	-	
Gate-Resistance	R _G	f = 1 MHz	-	3.8	-	Ω
SWITCHING CHARACTERISTICS				•		•
Turn-On Delay Time	t _{d(ON)}	$V_{GS} = -3/18 \text{ V}, V_{DS} = 800 \text{ V},$	-	12	-	ns
Rise Time	t _r	I_D = 20 A, R_G = 4.7 Ω Inductive load (Notes 5, 6)	-	15	-	
Turn-Off Delay Time	t _{d(OFF)}	, · · · ,	-	35	-	
Fall Time	t _f		-	10	-	
Turn-On Switching Loss	E _{ON}		-	182	-	μJ
Turn-Off Switching Loss	E _{OFF}		-	66	-	1
Total Switching Loss	E _{tot}		-	248	_	1
SOURCE-DRAIN DIODE CHARACTERIS	TICS					
Continuous Source-Drain Diode Forward Current	I _{SD}	$V_{GS} = -3 \text{ V}, T_C = 25^{\circ}\text{C (Note 6)}$	-	_	45	Α
Pulsed Source–Drain Diode Forward Current (Note 2)	I _{SDM}		-	-	134]
Forward Diode Voltage	V _{SD}	V _{GS} = −3 V, I _{SD} = 20 A, T _J = 25°C	_	4.5	_	V

 Table 2. ELECTRICAL CHARACTERISTICS ($T_J = 25^{\circ}C$ unless otherwise specified) (continued)

Parameter	Symbol	Test Condition	Min	Тур	Max	Unit	
SOURCE-DRAIN DIODE CHARACTERISTICS							
Reverse Recovery Time	t _{RR}	$V_{GS} = -3/18 \text{ V}, I_{SD} = 20 \text{ A},$ $dI_S/dt = 1000 \text{ A}/\mu\text{s}, V_{DS} = 800 \text{ V}$	-	16.8	-	ns	
Reverse Recovery Charge	Q _{RR}	dl _S /dt = 1000 A/μs, V _{DS} = 800 V (Note 6)	-	82	_	nC	
Reverse Recovery Energy	E _{REC}		-	7.9	_	μJ	
Peak Reverse Recovery Current	I _{RRM}		-	9.8	_	Α	
Charge Time	T _A		-	9.6	_	ns	
Discharge Time	T _B	1	_	7.2	-	ns	

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

5. E_{ON}/E_{OFF} result is with body diode.

6. Defined by design, not subject to production test.

TYPICAL CHARACTERISTICS

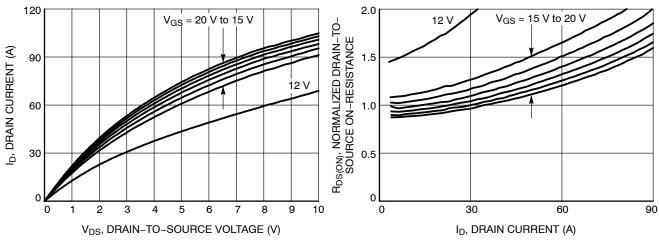


Figure 1. On-Region Characteristics

Figure 2. Normalized On-Resistance vs. Drain Current and Gate Voltage

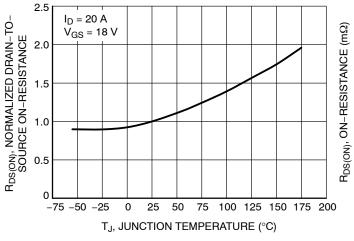


Figure 3. On–Resistance Variation with Temperature

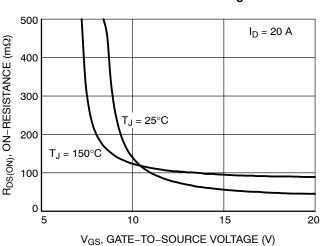


Figure 4. On-Resistance vs. Gate-to-Source Voltage

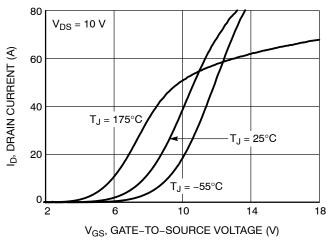


Figure 5. Transfer Characteristics

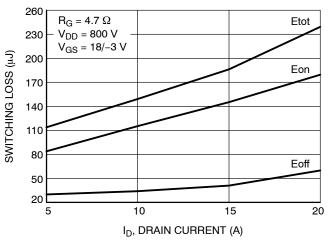


Figure 6. Switching Loss vs. Drain Current

TYPICAL CHARACTERISTICS

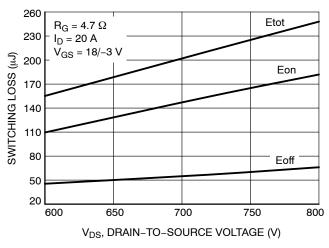


Figure 7. Switching Loss vs. Drain-to-Source Voltage

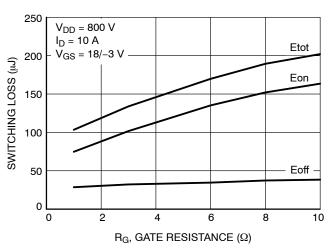


Figure 8. Switching Loss vs. Gate Resistance

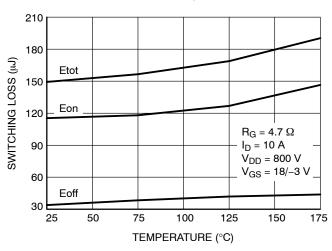


Figure 9. Switching Loss vs. Temperature

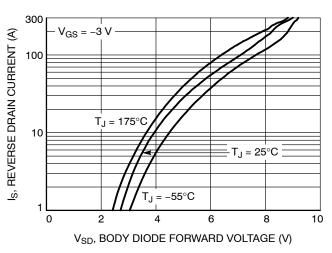


Figure 10. Reverse Drain Current vs. Body Diode Forward Voltage

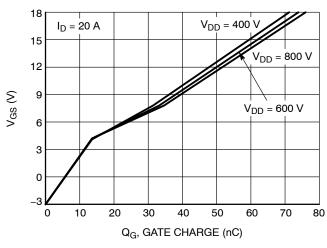


Figure 11. Gate-to-Source Voltage vs. Total Charge

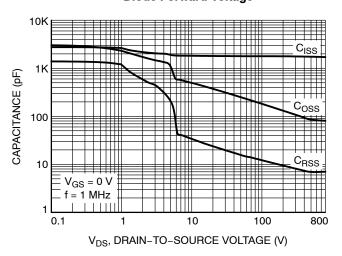


Figure 12. Capacitance vs. Drain-to-Source Voltage

TYPICAL CHARACTERISTICS

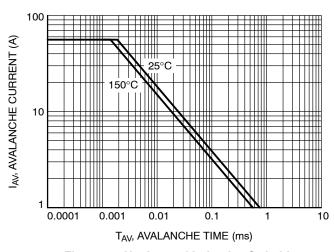


Figure 13. Unclamped Inductive Switching Capability

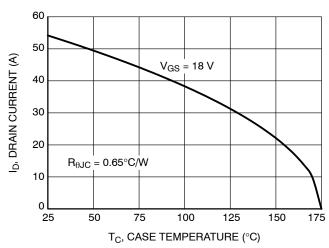


Figure 14. Maximum Continuous Drain Current vs. Case Temperature

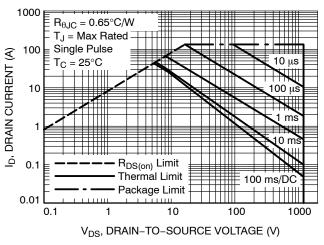


Figure 15. Safe Operating Area

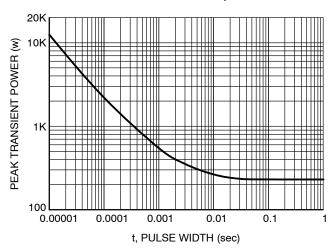


Figure 16. Single Pulse Maximum Power Dissipation

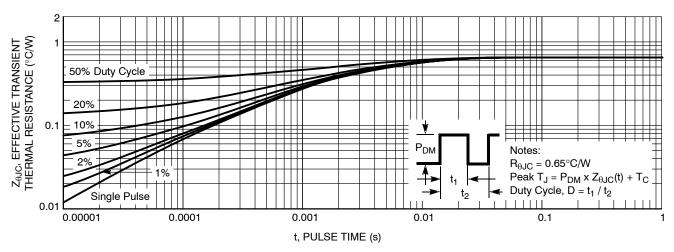


Figure 17. Junction-to-Case Transient Thermal Response

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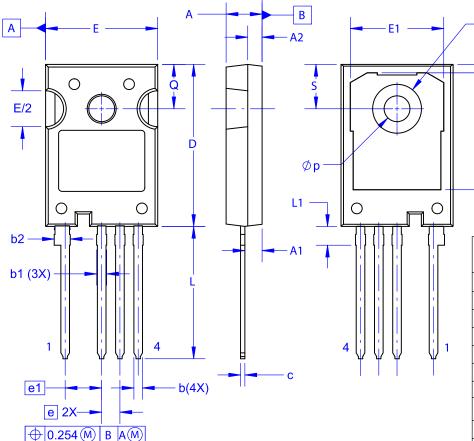
D1

D2



TO-247-4LD CASE 340CJ **ISSUE A**

DATE 16 SEP 2019



NOTES:

- A. NO INDUSTRY STANDARD APPLIES TO THIS PACKAGE.
 B. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD
 FLASH, AND TIE BAR EXTRUSIONS.
 C. ALL DIMENSIONS ARE IN MILLIMETERS.
 D. DRAWING CONFORMS TO ASME Y14.5-2009.

DIM	MILLIMETERS				
DIM	MIN	NOM	MAX		
Α	4.80	5.00	5.20		
A1	2.10	2.40	2.70		
A2	1.80	2.00	2.20		
b	1.07	1.20	1.33		
b1	1.20	1.40	1.60		
b2	2.02	2.22	2.42		
С	0.50	0.60	0.70		
D	22.34	22.54	22.74		
D1	16.00	16.25	16.50		
D2	0.97	1.17	1.37		
е	2.54 BSC				
e1	5	5.08 BSC			
E	15.40	15.60	15.80		
E1	12.80	13.00	13.20		
E/2	4.80	5.00	5.20		
L	18.22	18.42	18.62		
L1	2.42	2.62	2.82		
р	3.40	3.60	3.80		
p1	6.60	6.80	7.00		
Q	5.97	6.17	6.37		
S	5.97	6.17	6.37		

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