

MOSFET – Power, Single N-Channel 60 V, 26.5 mΩ, 20 A

NVTFS5C680NL

Features

- Small Footprint (3.3 x 3.3 mm) for Compact Design
- Low $R_{DS(on)}$ to Minimize Conduction Losses
- Low Capacitance to Minimize Driver Losses
- NVTFS5C680NLW – Wettable Flanks Product
- AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb-Free and are RoHS Compliant

MAXIMUM RATINGS (T_J = 25°C unless otherwise noted)

Parameter		Symbol	Value	Unit
Drain-to-Source Voltage		V _{DSS}	60	V
Gate-to-Source Voltage		V _{GS}	±20	V
Continuous Drain Current R _{θJC} (Notes 1, 2, 3, 4)	Steady State	T _C = 25°C	I _D	A
		T _C = 100°C		
Power Dissipation R _{θJC} (Notes 1, 2, 3)	Steady State	T _C = 25°C	P _D	W
		T _C = 100°C		
Continuous Drain Current R _{θJA} (Notes 1 & 3, 4)	Steady State	T _A = 25°C	I _D	A
		T _A = 100°C		
Power Dissipation R _{θJA} (Notes 1, 3)	Steady State	T _A = 25°C	P _D	W
		T _A = 100°C		
Pulsed Drain Current	T _A = 25°C, t _p = 10 µs	I _{DM}	80	A
Operating Junction and Storage Temperature		T _J , T _{stg}	-55 to +175	°C
Source Current (Body Diode)		I _S	17	A
Single Pulse Drain-to-Source Avalanche Energy (I _{L(pk)} = 1 A)		E _{AS}	51	mJ
Lead Temperature for Soldering Purposes (1/8" from case for 10 s)	T _L	260	°C	

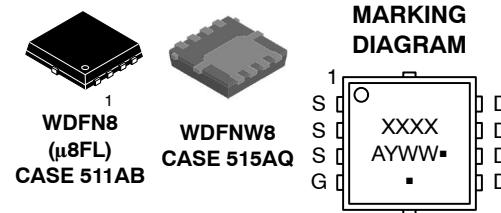
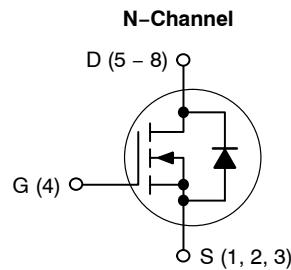
Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL RESISTANCE MAXIMUM RATINGS (Note 1)

Parameter	Symbol	Value	Unit
Junction-to-Case – Steady State (Note 3)	R _{θJC}	7.32	°C/W
Junction-to-Ambient – Steady State (Note 3)	R _{θJA}	49	

1. The entire application environment impacts the thermal resistance values shown, they are not constants and are only valid for the particular conditions noted.
2. Psi (Ψ) is used as required per JESD51-12 for packages in which substantially less than 100% of the heat flows to single case surface.
3. Surface-mounted on FR4 board using a 650 mm², 2 oz. Cu pad.
4. Continuous DC current rating. Maximum current for pulses as long as 1 second is higher but is dependent on pulse duration and duty cycle.

V _{(BR)DSS}	R _{DS(on)} MAX	I _D MAX
60 V	26.5 mΩ @ 10 V	20 A
	42.5 mΩ @ 4.5 V	



XXXX = Specific Device Code
 A = Assembly Location
 Y = Year
 WW = Work Week
 ■ = Pb-Free Package
 (Note: Microdot may be in either location)

ORDERING INFORMATION

See detailed ordering, marking and shipping information in the package dimensions section on page 5 of this data sheet.

NVTFS5C680NL

ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
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OFF CHARACTERISTICS

Drain-to-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0 \text{ V}, I_D = 250 \mu\text{A}$	60			V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{GS}} = 0 \text{ V}, V_{\text{DS}} = 60 \text{ V}$	$T_J = 25^\circ\text{C}$		10	μA
			$T_J = 125^\circ\text{C}$		100	
Gate-to-Source Leakage Current	I_{GSS}	$V_{\text{DS}} = 0 \text{ V}, V_{\text{GS}} = +20 \text{ V}$			100	nA

ON CHARACTERISTICS (Note 5)

Gate Threshold Voltage	$V_{\text{GS}(\text{TH})}$	$V_{\text{GS}} = V_{\text{DS}}, I_D = 13 \mu\text{A}$	1.2		2.2	V
Drain-to-Source On Resistance	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}} = 10 \text{ V}, I_D = 10 \text{ A}$		22	26.5	$\text{m}\Omega$
		$V_{\text{GS}} = 4.5 \text{ V}, I_D = 10 \text{ A}$		34	42.5	
Forward Transconductance	g_{FS}	$V_{\text{DS}} = 15 \text{ V}, I_D = 10 \text{ A}$		20		S

CHARGES AND CAPACITANCES

Input Capacitance	C_{iss}	$V_{\text{GS}} = 0 \text{ V}, f = 1.0 \text{ MHz}, V_{\text{DS}} = 25 \text{ V}$		327		pF
Output Capacitance	C_{oss}			161		
Reverse Transfer Capacitance	C_{rss}			6.0		
Total Gate Charge	$Q_{\text{G}(\text{TOT})}$	$V_{\text{GS}} = 4.5 \text{ V}, V_{\text{DS}} = 48 \text{ V}, I_D = 10 \text{ A}$		2.9		nC
Threshold Gate Charge	$Q_{\text{G}(\text{TH})}$			0.8		
Gate-to-Source Charge	Q_{GS}			1.2		
Gate-to-Drain Charge	Q_{GD}			0.8		
Total Gate Charge	$Q_{\text{G}(\text{TOT})}$	$V_{\text{GS}} = 10 \text{ V}, V_{\text{DS}} = 48 \text{ V}, I_D = 10 \text{ A}$		6.0		nC

SWITCHING CHARACTERISTICS (Note 6)

Turn-On Delay Time	$t_{\text{d}(\text{on})}$	$V_{\text{GS}} = 4.5 \text{ V}, V_{\text{DS}} = 48 \text{ V}, I_D = 10 \text{ A}, R_G = 1.0 \Omega$		6.5		ns
Rise Time	t_r			25		
Turn-Off Delay Time	$t_{\text{d}(\text{off})}$			13		
Fall Time	t_f			23		

DRAIN-SOURCE DIODE CHARACTERISTICS

Forward Diode Voltage	V_{SD}	$V_{\text{GS}} = 0 \text{ V}, I_S = 10 \text{ A}$	$T_J = 25^\circ\text{C}$		0.9	1.2	V
			$T_J = 125^\circ\text{C}$		0.8		
Reverse Recovery Time	t_{RR}	$V_{\text{GS}} = 0 \text{ V}, dI_S/dt = 100 \text{ A}/\mu\text{s}, I_S = 10 \text{ A}$			17		ns
Charge Time	t_a				8.0		
Discharge Time	t_b				9.0		
Reverse Recovery Charge	Q_{RR}				7.0		nC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

5. Pulse Test: Pulse Width $\leq 300 \mu\text{s}$, Duty Cycle $\leq 2\%$.

6. Switching characteristics are independent of operating junction temperatures.

TYPICAL CHARACTERISTICS

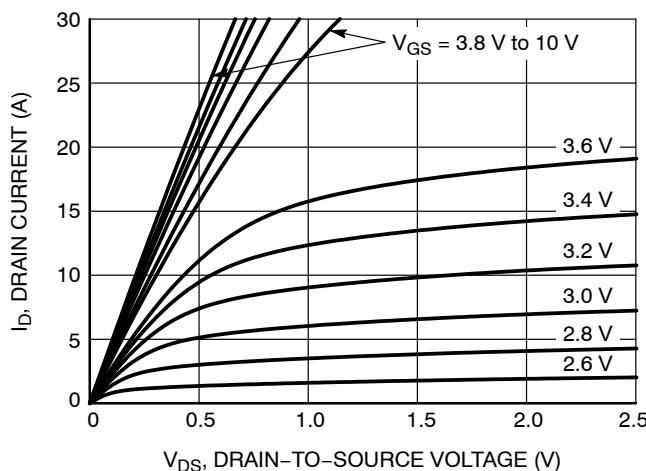


Figure 1. On-Region Characteristics

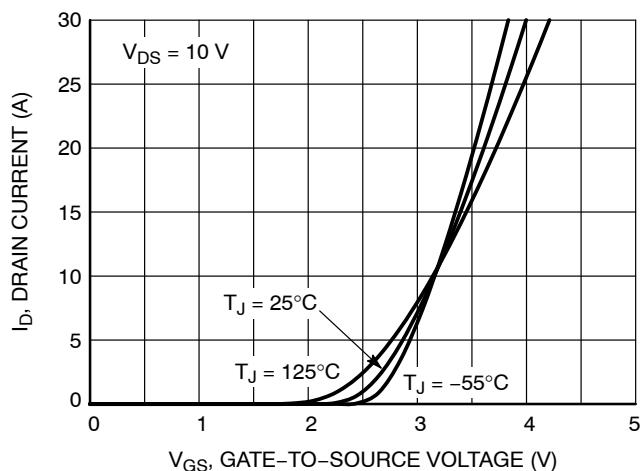


Figure 2. Transfer Characteristics

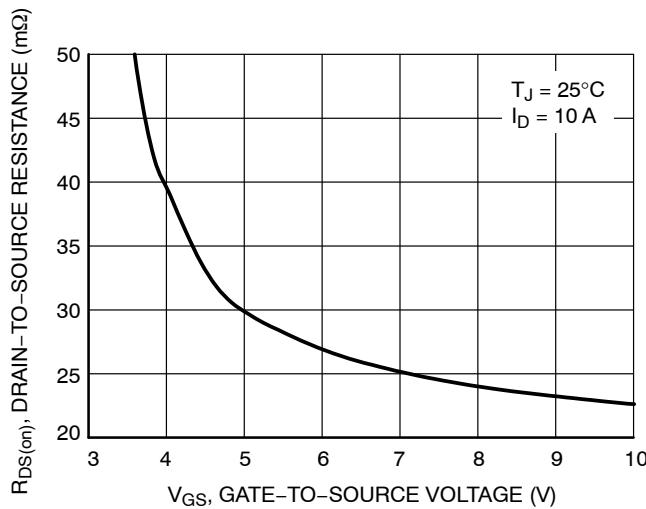


Figure 3. On-Resistance vs. Gate-to-Source Voltage

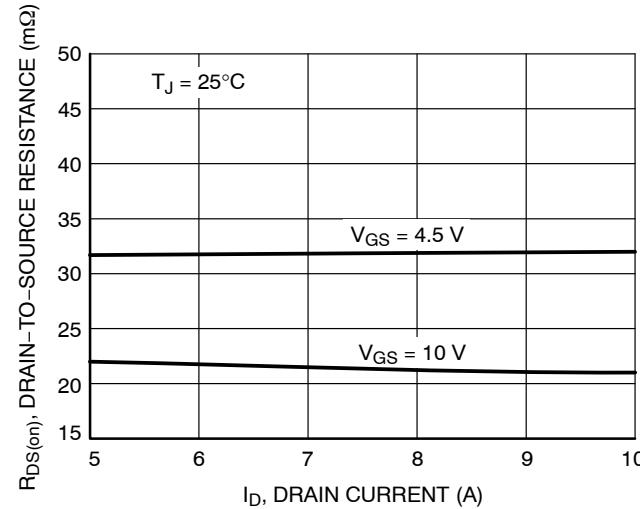


Figure 4. On-Resistance vs. Drain Current and Gate Voltage

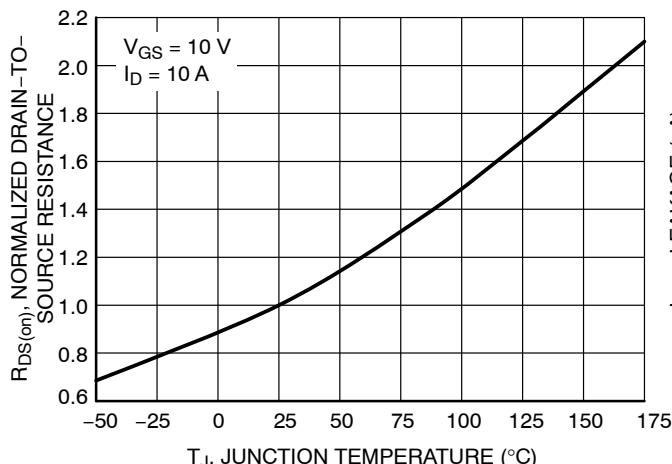


Figure 5. On-Resistance Variation with Temperature

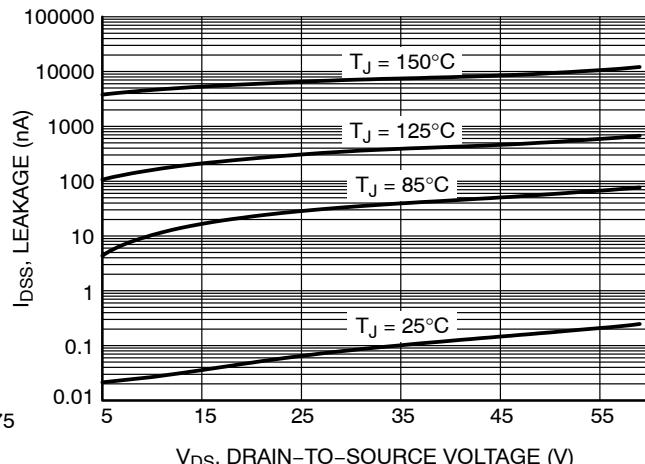
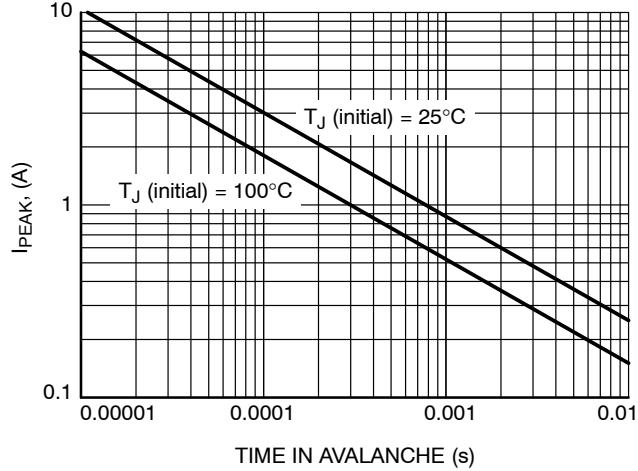
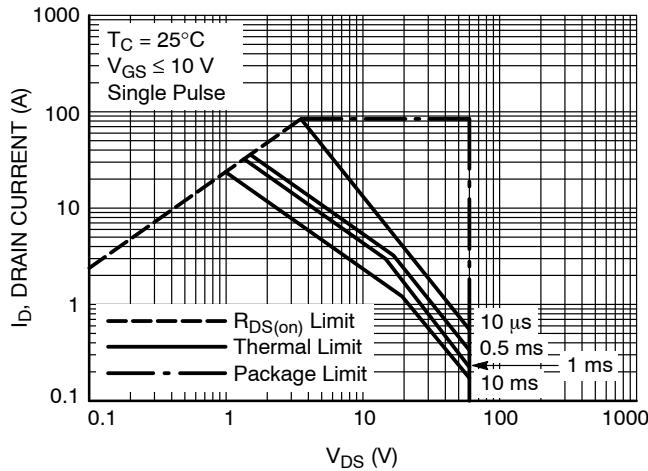
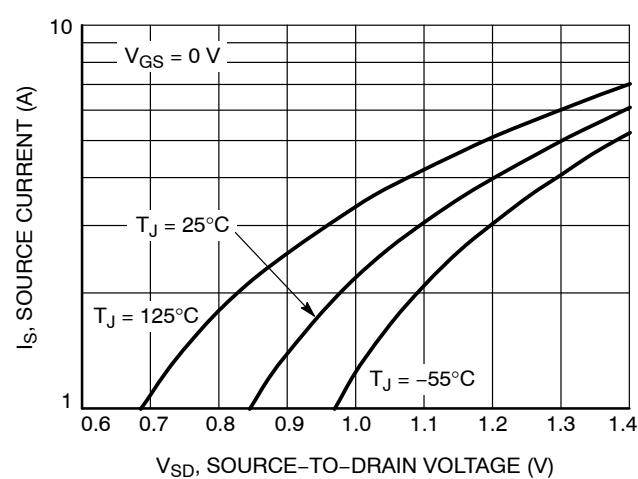
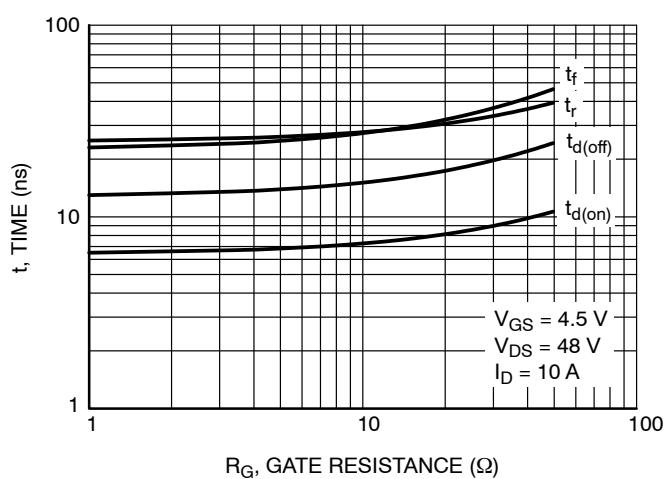
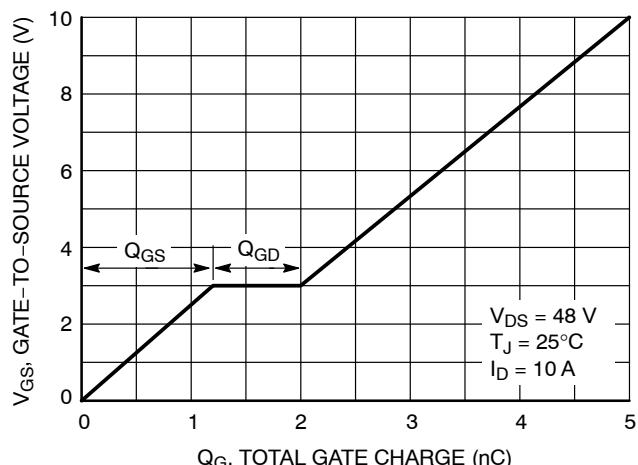
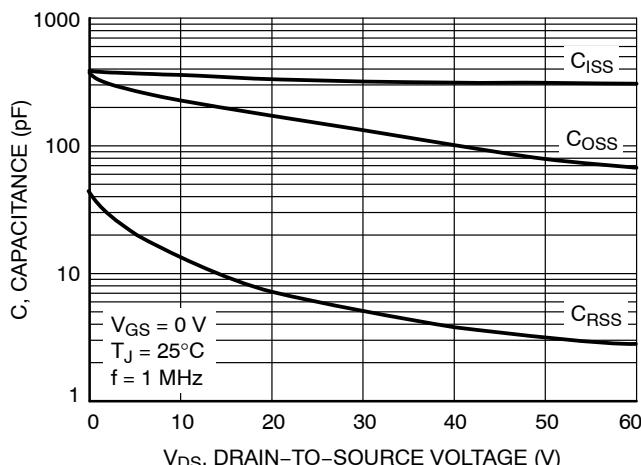


Figure 6. Drain-to-Source Leakage Current vs. Voltage

TYPICAL CHARACTERISTICS



NVTFS5C680NL

TYPICAL CHARACTERISTICS

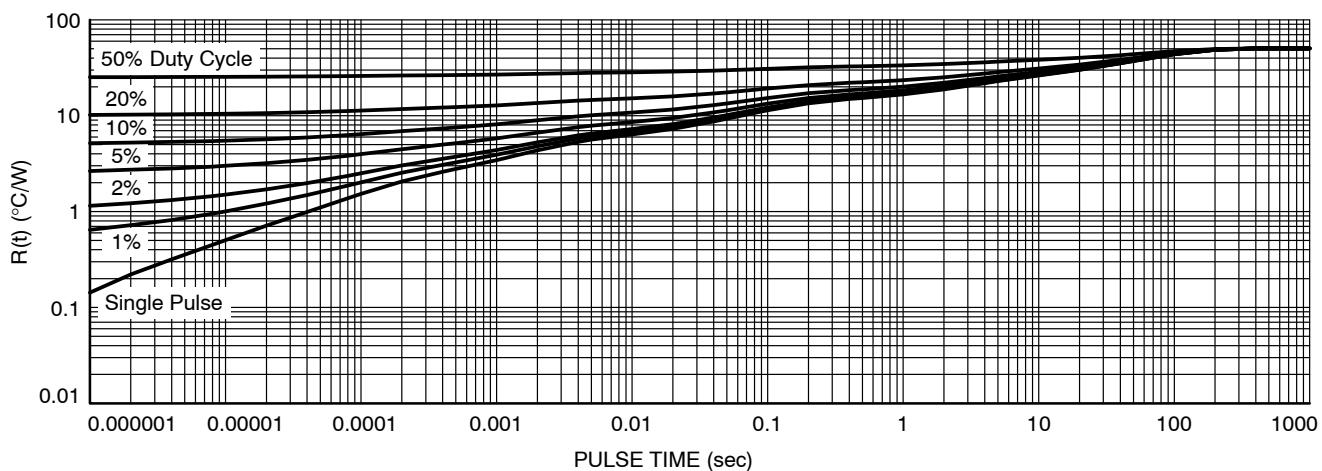


Figure 13. Thermal Characteristics

DEVICE ORDERING INFORMATION

Device	Marking	Package	Shipping [†]
NVTFS5C680NLTAG	680L	WDFN8 (Pb-Free)	1500 / Tape & Reel
NVTFS5C680NLWFTAG, NVTFS5C680NLWFETAG	80LW	WDFNW8 (Pb-Free)	1500 / Tape & Reel

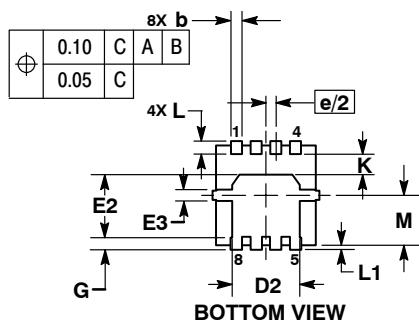
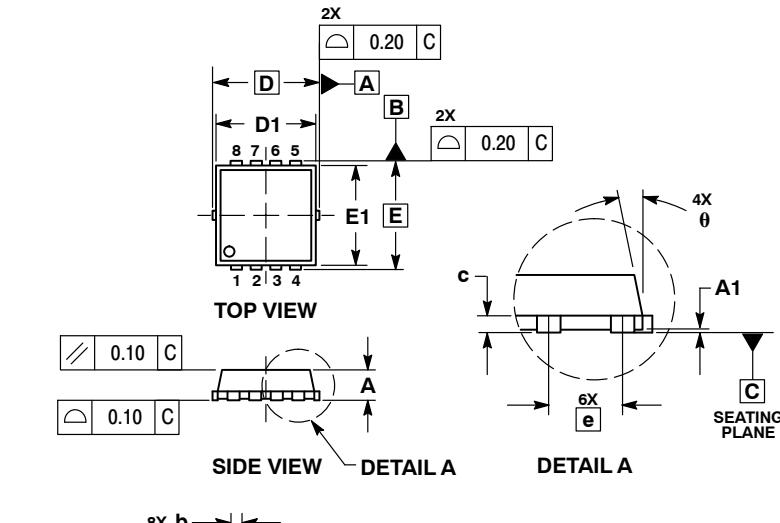
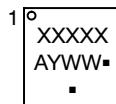
[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.



SCALE 2:1

WDFN8 3.3x3.3, 0.65P
CASE 511AB
ISSUE D

DATE 23 APR 2012

GENERIC
MARKING DIAGRAM*

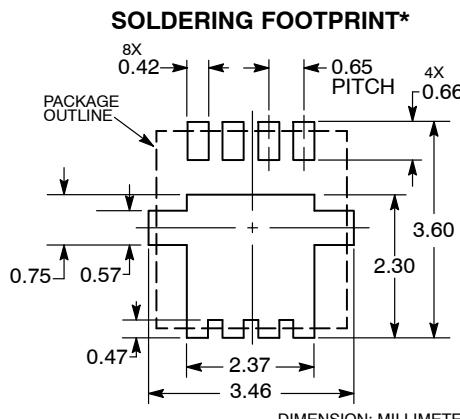
XXXXX = Specific Device Code
A = Assembly Location
Y = Year
WW = Work Week
□ = Pb-Free Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "□", may or may not be present. Some products may not follow the Generic Marking.

NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. DIMENSION D1 AND E1 DO NOT INCLUDE MOLD FLASH PROTRUSIONS OR GATE BURRS.

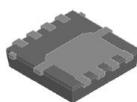
DIM	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.70	0.75	0.80	0.028	0.030	0.031
A1	0.00	---	0.05	0.000	---	0.002
b	0.23	0.30	0.40	0.009	0.012	0.016
c	0.15	0.20	0.25	0.006	0.008	0.010
D	3.30	BSC	3.30	0.130	BSC	0.130
D1	2.95	3.05	3.15	0.116	0.120	0.124
D2	1.98	2.11	2.24	0.078	0.083	0.088
E	3.30	BSC	3.30	0.130	BSC	0.130
E1	2.95	3.05	3.15	0.116	0.120	0.124
E2	1.47	1.60	1.73	0.058	0.063	0.068
E3	0.23	0.30	0.40	0.009	0.012	0.016
e	0.65	BSC	0.65	0.026	BSC	0.026
G	0.30	0.41	0.51	0.012	0.016	0.020
K	0.65	0.80	0.95	0.026	0.032	0.037
L	0.30	0.43	0.56	0.012	0.017	0.022
L1	0.06	0.13	0.20	0.002	0.005	0.008
M	1.40	1.50	1.60	0.055	0.059	0.063
θ	0	°	---	12	°	0



*For additional information on our Pb-Free strategy and soldering details, please download the **onsemi** Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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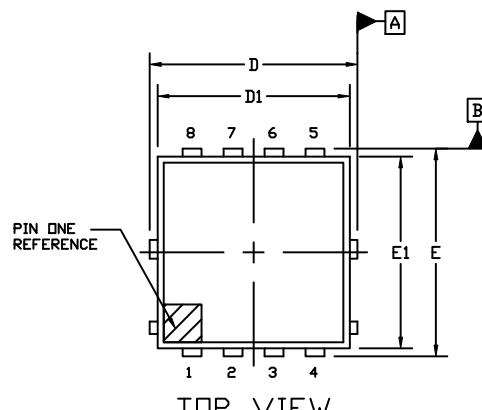
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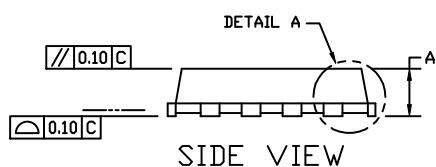
WDFNW8 3.3x3.3, 0.65P

CASE 515AQ
ISSUE O

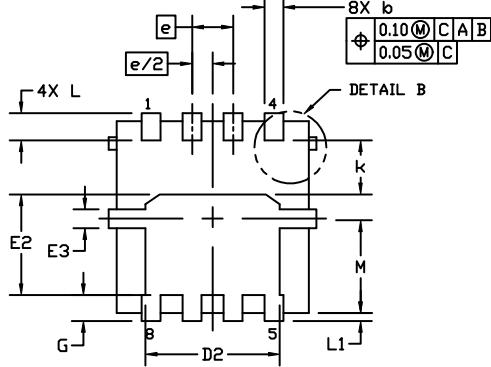
DATE 30 APR 2021



TOP VIEW



SIDE VIEW



BOTTOM VIEW

GENERIC
MARKING DIAGRAM*

XXXXX = Specific Device Code
 A = Assembly Location
 Y = Year
 WW = Work Week
 ■ = Pb-Free Package

(Note: Microdot may be in either location)

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "■", may or may not be present. Some products may not follow the Generic Marking.

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