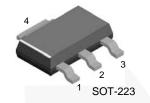


NZT660 / NZT660A PNP Low Saturation Transistor

Description

These devices are designed with high-current gain and low saturation voltage with collector currents up to 3 A continuous.



1. Base 2,4. Collector 3. Emitter

Ordering Information

Part Number	Marking	Package	Packing Method
NZT660	660	SOT-223 4L	Tape and Reel
NZT660A	660A	SOT-223 4L	Tape and Reel

Absolute Maximum Ratings(1),(2)

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at Values are at T_A = 25°C unless otherwise noted.

Symbol	Parameter	Val	Unit	
	r al ameter	NZT660	NZT660A	Onit
V _{CEO}	Collector-Emitter Voltage	-60	-60	V
V _{CBO}	Collector-Base Voltage	-80	-60	V
V _{EBO}	Emitter-Base Voltage	-5	-5	V
Ι _C	Collector Current - Continuous	-3	-3	А
T _J , T _{STG}	Operating and Storage Junction Temperature Range	-55 to +150	-55 to +150	°C

Notes:

1. These ratings are based on a maximum junction temperature of 150°C.

2. These are steady state limits. Fairchild Semiconductor should be consulted on application involving pulsed or low-duty cycle operation.

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Thermal Characteristics⁽³⁾

Values are at $T_A = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter	Max.	Unit
PD	Total Device Dissipation	2	W
$R_{ extsf{ heta}JA}$	R _{0JA} Thermal Resistance, Junction to Ambient		°C/W

Note:

3. PCB size: FR-4 76 x 114 x 1.57 mm³ (3.0 inch x 4.5 inch x 0.062 inch) with minimum land pattern size.

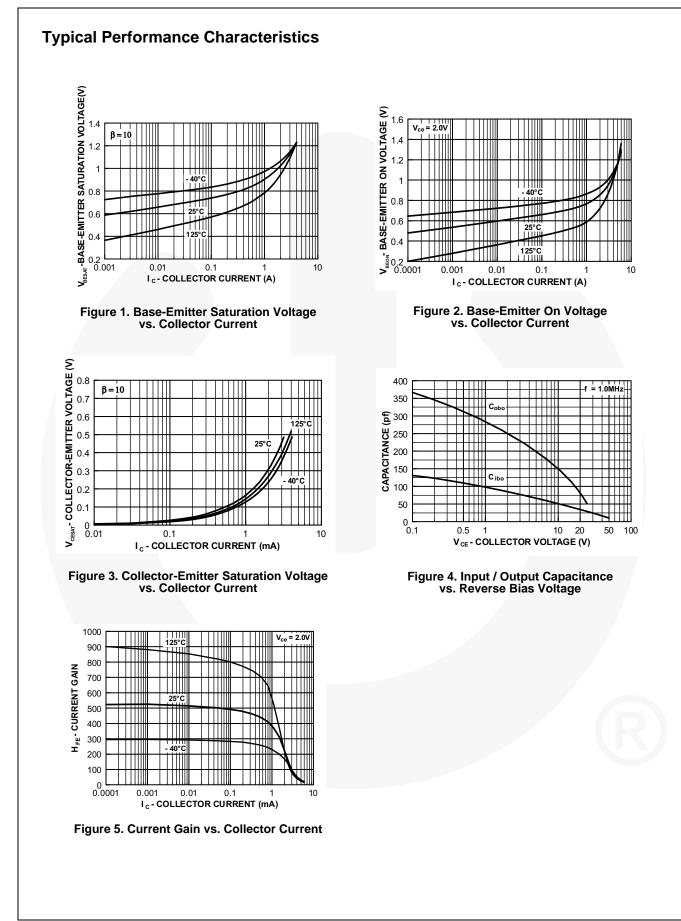
Electrical Characteristics

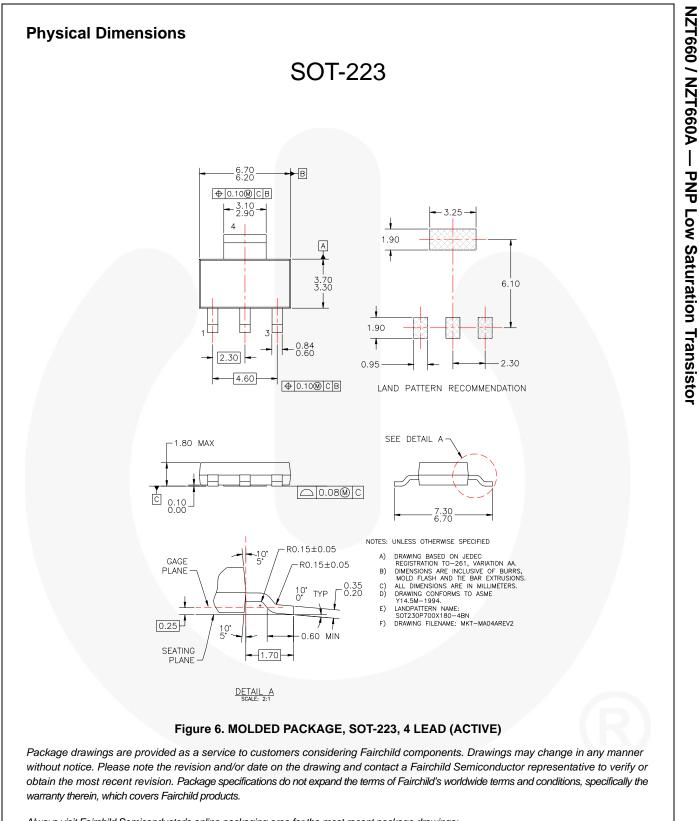
Values are at $T_A = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter	Conditions		Min.	Max.	Unit
BV _{CEO}	Collector-Emitter Breakdown Voltage	I _C = -10 mA		-60		V
BV	Collector-Base Breakdown Voltage	I _C = -100 μA	NZT660	-80		v
BV _{CBO} Co			NZT660A	-60		
ΒV _{EBO}	Emitter-Base Breakdown Voltage	I _E = -100 μA		-5		V
1	Collector-Base Cut-Off Current	V _{CB} = -30 V			-100	nA
I _{CBO} Collector-	Collector-Base Cut-On Current	$V_{CB} = -30 \text{ V}, \text{ T}_{A} = 100^{\circ}\text{C}$			-10	μΑ
I _{EBO}	Emitter-Base Cut-Off Current	V _{EB} = -4 V			-100	nA
h _{FE} DC Current Gain ⁽⁴⁾		I_{C} = -100 mA, V_{CE} = -2 V		70		
		I _C = -500 mA, V _{CE} = -2 V	NZT660	100	300	
	DC Current Gain ⁽⁴⁾		NZT660A	250	550	
		$I_{C} = -1 \text{ A}, V_{CE} = -2 \text{ V}$		80		
		$I_{C} = -3 \text{ A}, \text{ V}_{CE} = -2 \text{ V}$		25		
V _{CE} (sat) Collector-Emitter Sa Voltage ⁽⁴⁾		I _C = -1 A, I _B = -100 mV			-300	
		I _C = -3 A, I _B = -300 mV	NZT660		-550	mV
	onage		NZT660A		-500	
V _{BE} (sat)	Base-Emitter Saturation Voltage ⁽⁴⁾	I _C = -1 A, I _B = -100 mV			-1.25	V
V _{BE} (on)	Base-Emitter On Voltage ⁽⁴⁾	$I_{C} = -1 \text{ A}, V_{CE} = -2 \text{ V}$			-1	V
C _{ob}	Output Capacitance	$V_{CB} = -10 \text{ V}, \text{ I}_{E} = 0, \text{ f} = 1 \text{ MHz}$			45	pF
f _T	Transition Frequency	$I_{C} = -100 \text{ mA}, V_{CE} = -5 \text{ V},$ f = 100 MHz		75		MHz

Note:

4. Pulse test: pulse width \leq 300 µs, duty cycle \leq 2.0%.





Always visit Fairchild Semiconductor's online packaging area for the most recent package drawings: <u>http://www.fairchildsemi.com/dwg/MA/MA04A.pdf</u>.

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Definition of	Terms
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