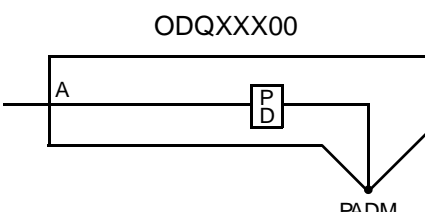


## AMI5HG 0.5 micron CMOS Gate Array

### Description

ODQXXX00 is a non-buffered, resistive analog crystal oscillator output pad piece with ESD protection.

Logic Symbol	Truth Table	Pin Loading										
 <p>The logic symbol for ODQXXX00 shows an input terminal 'A' on the left. A horizontal line connects 'A' to a rectangular block labeled 'P D' (representing a pull-down resistor). From the right side of the 'P D' block, a line goes down and then right to a terminal labeled 'PADM'.</p>	<table border="1"> <thead> <tr> <th>A</th> <th>PADM</th> </tr> </thead> <tbody> <tr> <td>L</td> <td>L</td> </tr> <tr> <td>H</td> <td>H</td> </tr> </tbody> </table>	A	PADM	L	L	H	H	<table border="1"> <thead> <tr> <th></th> <th>Load</th> </tr> </thead> <tbody> <tr> <td>A</td> <td>2.4 eqI</td> </tr> </tbody> </table>		Load	A	2.4 eqI
A	PADM											
L	L											
H	H											
	Load											
A	2.4 eqI											

### HDL Syntax

Verilog ..... ODQXXX00 *inst\_name* (PADM, A);  
 VHDL ..... *inst\_name*: ODQXXX00 port map (PADM, A);

### Power Characteristics

Parameter	Value	Units
Static $I_{DD}$ ( $T_J = 85^\circ\text{C}$ )	TBD	nA
$EQL_{pd}$	137.5	Eq-load

See page 2-15 for power equation.

Pad  
Logic