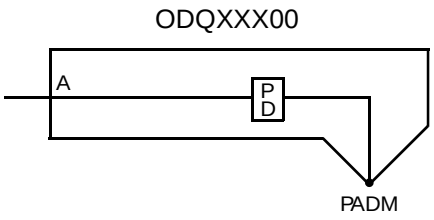
### Description

ODQXXX00 is a non-buffered, resistive analog crystal oscillator output pad piece with ESD protection.

**Logic Symbol**




**Truth Table**

| A | PADM |
|---|---|
| L | L |
| H | H |

**Pin Loading**

| | Load |
|---|---|
| A | 2.4 eql |

#### HDL Syntax

Verilog ..... ODQXXX00 *inst_name* (PADM, A);

VHDL..... *inst_name*: ODQXXX00 port map (PADM, A);

### Power Characteristics

| Parameter | Value | Units |
|---|---|---|
| Static $I_{DD}$ ($T_J$ = 85°C) | TBD | nA |
| $EQL_{pd}$ | 137.5 | Eq-load |

See page 2-15 for power equation.