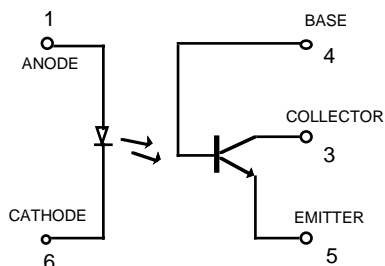




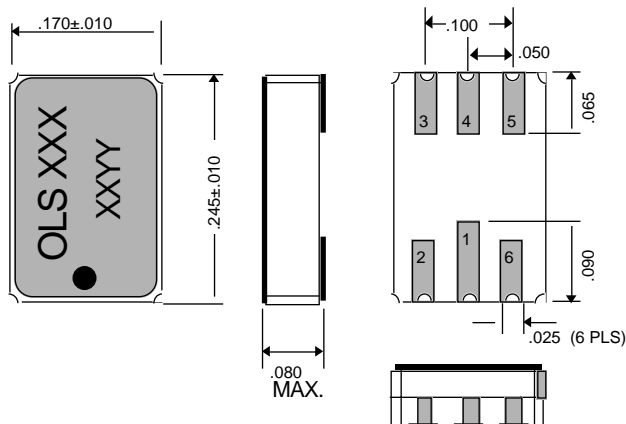
# ISO LINK

## OLS 249

Radiation Tolerant **Phototransistor**  
Hermetic Surface Mount **Optocoupler**



SCHEMATIC



DataSheet4U.com PACKAGE OUTLINE

## Features

- ◆ Hermetic SMT package
- ◆ 1500 Vdc electrical isolation
- ◆ High current transfer ratio
- ◆ Small package size
- ◆ High reliability and rugged construction
- ◆ Hi-rel screening available
- ◆ Radiation tolerant

## Description

The OLS249 consists of a light emitting diode optically coupled to a NPN silicon phototransistor mounted and coupled in a custom hermetic surface mount LCC package. The very low input current makes the OLS249 well suited for direct CMOS to LSTTL / TTL interfaces.

Electrical parameters are similar to the JEDEC registered 4N49 optocoupler but with much better CTR degradation characteristics due to radiation exposure

Special electrical parametric selections are available on request.

### NOTES:

1. Measured between pins 1, 2 and 6 shorted together and pins 3,4 and 5 shorted together.  $T_A = 25^\circ\text{C}$  and duration = 1 second.
2. Derate linearly to  $125^\circ\text{C}$  free-air temperature at  $0.67\text{ mA}/^\circ\text{C}$  above  $65^\circ\text{C}$ .
3. For pulse width  $\leq 1\ \mu\text{s}$ , pulse repetition rate  $\leq 300\text{ pps}$ .
4. Derate linearly to  $125^\circ\text{C}$  free-air temperature at  $3.0\text{ mW}/^\circ\text{C}$  above  $25^\circ\text{C}$

## Absolute Maximum Ratings

Coupled	
Input to Output Isolation Voltage <sup>1</sup>	± 1500 Vdc
Storage Temperature Range	-65 °C to + 150 °C
Operation Temperature Range	-55 °C to + 125 °C
Mounting Temperature Range ( 10 seconds max. )	240 °C
Input Diode	
Average Input Current <sup>2</sup>	40 mA
Peak Forward Current <sup>3</sup>	1 A
Reverse Voltage	2.0 V
Output Detector	
Collector - Emitter Voltage	40 V
Emitter - Base Voltage	7 V
Collector - Base Voltage	45 V
Continuous Collector Current	50 mA
Power Dissipation <sup>4</sup>	300 mW

## ELECTRICAL CHARACTERISTIC ( T<sub>A</sub> = 25 °C, Unless Otherwise Specified )

Parameter	Symbol	Min	Max	Units	Test Conditions	Fig.	Note
On-State Collector Current	I <sub>C</sub> (ON)	2.0	12	mA	I <sub>F</sub> = 1 mA, V <sub>CE</sub> = 5.0V	2,3	
		2.8		mA	I <sub>F</sub> = 2 mA, V <sub>CE</sub> = 5.0V, T <sub>A</sub> = -55°C		
		2.0		mA	I <sub>F</sub> = 2 mA, V <sub>CE</sub> = 5.0V, T <sub>A</sub> = 100°C		
On-State Coll.-Base Current	I <sub>CB</sub> (ON)	30		μA	I <sub>F</sub> = 10 mA, V <sub>CB</sub> = 5.0V		
Saturation Voltage	V <sub>CE</sub> (SAT)		0.3	V	I <sub>F</sub> = 2mA, I <sub>C</sub> = 2.0mA		
Breakdown Voltage	BV <sub>CEO</sub>	40	45	V	I <sub>CE</sub> = 1 mA		
		45		V	I <sub>CB</sub> = 100 μA		
		7		V	I <sub>EB</sub> = 100 μA		
Off-State Leakage Current	I <sub>CE</sub> (OFF)		100	nA	V <sub>CE</sub> = 20V		
			100	μA	V <sub>CE</sub> = 20V, T <sub>A</sub> = 100 °C		
			10	nA	V <sub>CB</sub> = 20V		
Input Forward Voltage	V <sub>F</sub>	1.8	2.2	V	I <sub>F</sub> = 10mA, T <sub>A</sub> = -55°C	1	
		1.4	1.8	V	I <sub>F</sub> = 10mA	1	
		1.2	1.6	V	I <sub>F</sub> = 10mA, T <sub>A</sub> = 100°C	1	
Input Reverse Current	I <sub>R</sub>		100	μA	V <sub>R</sub> = 2.0V		
Input to Output Resistance	r <sub>I-O</sub>	10 <sup>11</sup>		Ω	V <sub>I-O</sub> = ±1000Vdc		1
Input to Output Capacitance	c <sub>I-O</sub>		5	pF	V <sub>I-O</sub> = 0V, f = 1 MHz		1
Rise Time	t <sub>r</sub>		25	μS	V <sub>CC</sub> = 10V, R <sub>L</sub> = 100 Ω	4	
Fall Time	t <sub>f</sub>		25	μS	I <sub>F</sub> = 5mA		

## TYPICAL PERFORMANCE CURVES

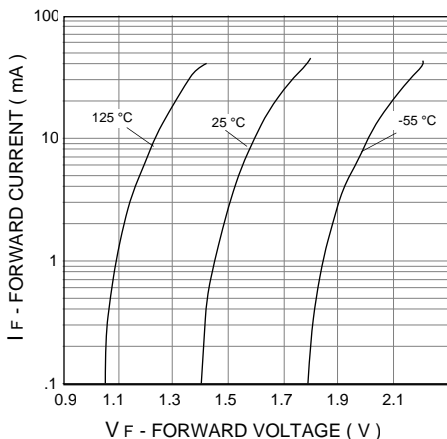


Fig. 1 - Diode Forward Characteristics

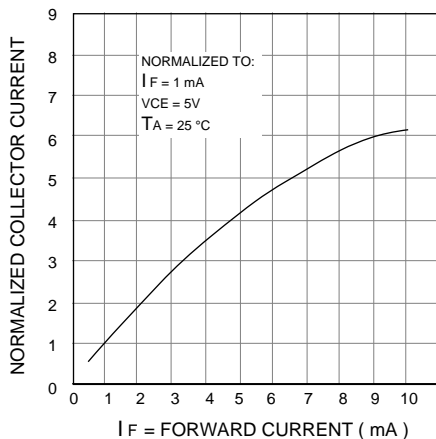


Fig. 2 - Normalized  $I_c$  vs.  $I_F$

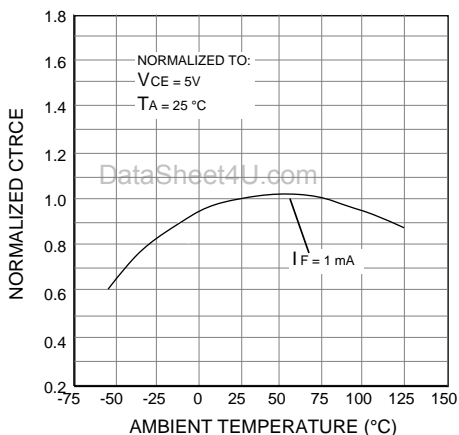


Fig. 3 - Normalized CTR vs. Temperature

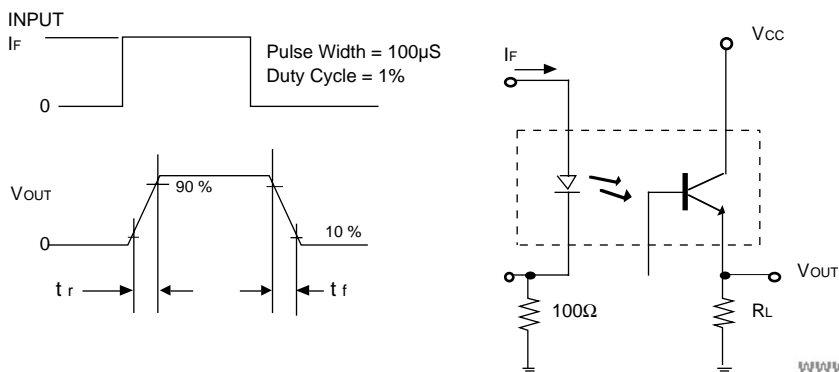


Fig. 4 - Switching Test Circuit