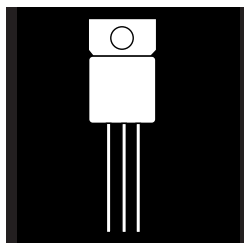


OM6001ST OM6003ST OM6101ST OM6103ST
OM6002ST OM6004ST OM6102ST OM6104ST

POWER MOSFET IN HERMETIC ISOLATED JEDEC TO-257AA PACKAGE



**100V Thru 500V, Up To 14 Amp, N-Channel
MOSFET With Or Without Zener Gate
Clamp Protection**

FEATURES

- Isolated Hermetic Metal Package
- Bi-Lateral Zener Gate Protection (Optional)
- Fast Switching, Low Drive Current
- Ease Of Paralleling For Added Power
- Low $R_{DS(on)}$
- Available Screened To MIL-S-19500, TX, TXV And S Levels

DESCRIPTION

This series of hermetically packaged products feature the latest advanced MOSFET and packaging technology. They are ideally suited for Military requirements where small size, high performance and high reliability are required, and in applications such as switching power supplies, motor controls, inverters, choppers, audio amplifiers and high energy pulse circuits. The MOSFET gates are protected using bi-lateral zener clamps in the OM6101ST series.

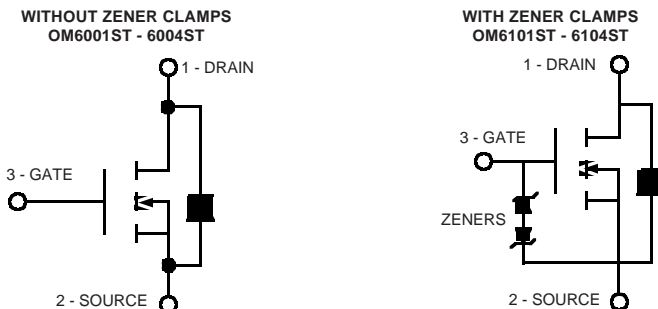
MAXIMUM RATINGS

PART NUMBER	V_{DS}	$R_{DS(on)}$	I_D
OM6001ST/OM6101ST	100 V	.20	14 A
OM6002ST/OM6102ST	200 V	.44	9 A
OM6003ST/OM6103ST	400 V	1.05	5.5 A
OM6004ST/OM6104ST	500 V	1.60	4.5 A

Note: OM6101ST thru OM6104ST is supplied with zener gate protection.
OM6001ST thru OM6004ST is supplied without zener gate protection.

3.1

SCHEMATIC



ELECTRICAL CHARACTERISTICS: ($T_C = 25^\circ\text{C}$ unless otherwise noted)
STATIC P/N OM6101ST / OM6001ST (100V)

ELECTRICAL CHARACTERISTICS: ($T_C = 25^\circ\text{C}$ unless otherwise noted)
STATIC P/N OM6102ST / OM6002 ST (200V)

Parameter	Min.	Typ.	Max.	Units	Test Conditions
BV_{DSS} Drain-Source Breakdown Voltage	100			V	$V_{GS} = 0$, $I_D = 250\text{ mA}$
$V_{GS(th)}$ Gate-Threshold Voltage	2.0		4.0	V	$V_{DS} = V_{GS}$, $I_D = 250\text{ mA}$
I_{GSS} Gate-Body Leakage (OM6101)			± 500	nA	$V_{GS} = \pm 12.8\text{ V}$
I_{GSS} Gate-Body Leakage (OM6001)			± 100	nA	$V_{GS} = \pm 20\text{ V}$
I_{DSS} Zero Gate Voltage Drain Current		0.1	0.25	mA	$V_{DS} = \text{Max. Rat.}$, $V_{GS} = 0$
		0.2	1.0	mA	$V_{DS} = 0.8\text{ Max. Rat.}$, $V_{GS} = 0$, $T_C = 125^\circ\text{ C}$
$I_{D(on)}$ On-State Drain Current ¹	14			A	$V_{DS} = 2 V_{DS(on)}$, $V_{GS} = 10\text{ V}$
$V_{DS(on)}$ Static Drain-Source On-State Voltage ¹		1.2	1.60	V	$V_{GS} = 10\text{ V}$, $I_D = 8\text{ A}$
$R_{DS(on)}$ Static Drain-Source On-State Resistance ¹			0.20		$V_{GS} = 10\text{ V}$, $I_D = 8\text{ A}$
$R_{DS(on)}$ Static Drain-Source On-State Resistance ¹			0.40		$V_{GS} = 10\text{ V}$, $I_D = 8\text{ A}$, $T_C = 125\text{ C}$

Parameter	Min.	Typ.	Max.	Units	Test Conditions
BV_{DSS} Drain-Source Breakdown Voltage	200			V	$V_{GS} = 0$, $I_D = 250\text{ mA}$
$V_{GS(th)}$ Gate-Threshold Voltage	2.0		4.0	V	$V_{DS} = V_{GS}$, $I_D = 250\text{ mA}$
I_{GSS} Gate-Body Leakage (OM6102)			± 500	nA	$V_{GS} = \pm 12.8\text{ V}$
I_{GSS} Gate-Body Leakage (OM6002)			± 100	nA	$V_{GS} = \pm 20\text{ V}$
I_{DSS} Zero Gate Voltage Drain Current		0.1	0.25	mA	$V_{DS} = \text{Max. Rat.}$, $V_{GS} = 0$
		0.2	1.0	mA	$V_{DS} = 0.8\text{ Max. Rat.}$, $V_{GS} = 0$, $T_C = 125^\circ\text{ C}$
$I_{D(on)}$ On-State Drain Current ¹	9.0			A	$V_{DS} = 2 V_{DS(on)}$, $V_{GS} = 10\text{ V}$
$V_{DS(on)}$ Static Drain-Source On-State Voltage ¹		1.25	2.2	V	$V_{GS} = 10\text{ V}$, $I_D = 5.0\text{ A}$
$R_{DS(on)}$ Static Drain-Source On-State Resistance ¹			0.44		$V_{GS} = 10\text{ V}$, $I_D = 5.0\text{ A}$
$R_{DS(on)}$ Static Drain-Source On-State Resistance ¹			0.88		$V_{GS} = 10\text{ V}$, $I_D = 5.0\text{ A}$, $T_C = 125\text{ C}$

DYNAMIC


DYNAMIC


g_{fs} Forward Transductance ¹	4.0			S (M)	$V_{DS} = 2 V_{DS(on)}$, $I_D = 8\text{ A}$
C_{iss} Input Capacitance		750		pF	$V_{GS} = 0$
C_{oss} Output Capacitance		250		pF	$V_{DS} = 25\text{ V}$
C_{rss} Reverse Transfer Capacitance		100		pF	$f = 1\text{ MHz}$
$t_{d(on)}$ Turn-On Delay Time		15		ns	$V_{DD} = 30\text{ V}$, $I_D @ 8\text{ A}$
t_r Rise Time		35		ns	$R_g = 7.5\text{ }\Omega$, $V_{DS} = 10\text{ V}$
$t_{d(off)}$ Turn-Off Delay Time		38		ns	
t_f Fall Time		23		ns	

g_{fs} Forward Transductance ¹	3.0	5.8		S (M)	$V_{DS} = 2 V_{DS(on)}$, $I_D = 5.0\text{ A}$
C_{iss} Input Capacitance		780		pF	$V_{GS} = 0$
C_{oss} Output Capacitance		150		pF	$V_{DS} = 25\text{ V}$
C_{rss} Reverse Transfer Capacitance		55		pF	$f = 1\text{ MHz}$
$t_{d(on)}$ Turn-On Delay Time		9		ns	$V_{DD} = 75\text{ V}$, $I_D @ 5.0\text{ A}$
t_r Rise Time		18		ns	$R_g = 7.5\text{ }\Omega$, $V_{GS} = 10\text{ V}$
$t_{d(off)}$ Turn-Off Delay Time		45		ns	
t_f Fall Time		27		ns	

BODY-DRAIN DIODE RATINGS AND CHARACTERISTICS

BODY-DRAIN DIODE RATINGS AND CHARACTERISTICS

I_S Continuous Source Current (Body Diode)			- 14	A	Modified MOSPOWER symbol showing the integral P-N Junction rectifier. 
I_{SM} Source Current ¹ (Body Diode)			- 56	A	
V_{SD} Diode Forward Voltage ¹			- 2.5	V	$T_C = 25\text{ C}$, $I_S = -14\text{ A}$, $V_{GS} = 0$
					$T_C = 25\text{ C}$, $I_S = -12\text{ A}$, $V_{GS} = 0$
t_{rr} Reverse Recovery Time		100		ns	$T_J = 150\text{ C}$, $I_F = I_S$, $di_F/ds = 100\text{ A/ms}$

I_S Continuous Source Current (Body Diode)			- 9	A	Modified MOSPOWER symbol showing the integral P-N Junction rectifier. 
I_{SM} Source Current ¹ (Body Diode)			- 36	A	
V_{SD} Diode Forward Voltage ¹			- 2	V	$T_C = 25\text{ C}$, $I_S = -9\text{ A}$, $V_{GS} = 0$
					$T_C = 25\text{ C}$, $I_S = -8\text{ A}$, $V_{GS} = 0$
t_{rr} Reverse Recovery Time		250		ns	$T_J = 150\text{ C}$, $I_F = I_S$, $di_F/ds = 100\text{ A/ms}$

1 Pulse Test: Pulse Width 300msec, Duty Cycle 2%.

1 Pulse Test: Pulse Width 300msec, Duty Cycle 2%.

ELECTRICAL CHARACTERISTICS: ($T_C = 25^\circ\text{C}$ unless otherwise noted)
STATIC P/N OM6103ST / OM6003ST (400V)

ELECTRICAL CHARACTERISTICS: ($T_C = 25^\circ\text{C}$ unless otherwise noted)
STATIC P/N OM6104ST / OM6004ST (500V)

Parameter	Min.	Typ.	Max.	Units	Test Conditions
BV_{DSS} Drain-Source Breakdown Voltage	400			V	$V_{GS} = 0$, $I_D = 250\text{ mA}$
$V_{GS(th)}$ Gate-Threshold Voltage	2.0		4.0	V	$V_{DS} = V_{GS}$, $I_D = 250\text{ mA}$
I_{GSS} Gate-Body Leakage (OM6103)			± 500	nA	$V_{GS} = \pm 12.8\text{ V}$
I_{GSS} Gate-Body Leakage (OM6003)			± 100	nA	$V_{GS} = \pm 20\text{ V}$
I_{DSS} Zero Gate Voltage Drain Current		0.1	0.25	mA	$V_{DS} = \text{Max. Rat.}$, $V_{GS} = 0$
		0.2	1.0	mA	$V_{DS} = 0.8\text{ Max. Rat.}$, $V_{GS} = 0$, $T_C = 125^\circ\text{ C}$
$I_{D(on)}$ On-State Drain Current ¹	5.5			A	$V_{DS} = 2 V_{DS(on)}$, $V_{GS} = 10\text{ V}$
$V_{DS(on)}$ Static Drain-Source On-State Voltage ¹		2.4	3.15	V	$V_{GS} = 10\text{ V}$, $I_D = 3.0\text{ A}$
$R_{DS(on)}$ Static Drain-Source On-State Resistance ¹			1.05		$V_{GS} = 10\text{ V}$, $I_D = 3.0\text{ A}$
$R_{DS(on)}$ Static Drain-Source On-State Resistance ¹			2.0		$V_{GS} = 10\text{ V}$, $I_D = 3.0\text{ A}$, $T_C = 125\text{ C}$

Parameter	Min.	Typ.	Max.	Units	Test Conditions
BV_{DSS} Drain-Source Breakdown Voltage	500			V	$V_{GS} = 0$, $I_D = 250\text{ mA}$
$V_{GS(th)}$ Gate-Threshold Voltage	2.0		4.0	V	$V_{DS} = V_{GS}$, $I_D = 250\text{ mA}$
I_{GSS} Gate-Body Leakage (OM6104)			± 500	nA	$V_{GS} = \pm 12.8\text{ V}$
I_{GSS} Gate-Body Leakage (OM6004)			± 100	nA	$V_{GS} = \pm 20\text{ V}$
I_{DSS} Zero Gate Voltage Drain Current		0.1	0.25	mA	$V_{DS} = \text{Max. Rat.}$, $V_{GS} = 0$
		0.2	1.0	mA	$V_{DS} = 0.8\text{ Max. Rat.}$, $V_{GS} = 0$, $T_C = 125^\circ\text{ C}$
$I_{D(on)}$ On-State Drain Current ¹	4.5			A	$V_{DS} = 2 V_{DS(on)}$, $V_{GS} = 10\text{ V}$
$V_{DS(on)}$ Static Drain-Source On-State Voltage ¹		3.25	4.00	V	$V_{GS} = 10\text{ V}$, $I_D = 2.5\text{ A}$
$R_{DS(on)}$ Static Drain-Source On-State Resistance ¹			1.6		$V_{GS} = 10\text{ V}$, $I_D = 2.5\text{ A}$
$R_{DS(on)}$ Static Drain-Source On-State Resistance ¹		2.9	3.3		$V_{GS} = 10\text{ V}$, $I_D = 2.5\text{ A}$, $T_C = 125\text{ C}$

DYNAMIC


DYNAMIC


g_{fs} Forward Transconductance ¹	3.0	3.6		S (M)	$V_{DS} = 2 V_{DS(on)}$, $I_D = 3.0\text{ A}$
C_{iss} Input Capacitance		700		pF	$V_{GS} = 0$
C_{oss} Output Capacitance		70		pF	$V_{DS} = 25\text{ V}$
C_{rss} Reverse Transfer Capacitance		20		pF	$f = 1\text{ MHz}$
$t_{d(on)}$ Turn-On Delay Time		18		ns	$V_{DD} = 175\text{ V}$, $I_D @ 3.0\text{ A}$
t_r Rise Time		20		ns	$R_{\theta} = 10\text{ W}$, $V_{GS} = 10\text{ V}$
$t_{d(off)}$ Turn-Off Delay Time		40		ns	
t_f Fall Time		25		ns	

g_{fs} Forward Transconductance ¹	2.5	2.8		S (M)	$V_{DS} = 2 V_{DS(on)}$, $I_D = 2.5\text{ A}$
C_{iss} Input Capacitance		700		pF	$V_{GS} = 0$
C_{oss} Output Capacitance		90		pF	$V_{DS} = 25\text{ V}$
C_{rss} Reverse Transfer Capacitance		30		pF	$f = 1\text{ MHz}$
$t_{d(on)}$ Turn-On Delay Time		18		ns	$V_{DD} = 225\text{ V}$, $I_D @ 2.5\text{ A}$
t_r Rise Time		20		ns	$R_{\theta} = 7.5\text{ W}$, $V_{GS} = 10\text{ V}$
$t_{d(off)}$ Turn-Off Delay Time		42		ns	
t_f Fall Time		25		ns	

BODY-DRAIN DIODE RATINGS AND CHARACTERISTICS

BODY-DRAIN DIODE RATINGS AND CHARACTERISTICS

I_S Continuous Source Current (Body Diode)			- 5.5	A	Modified MOSPOWER symbol showing the integral P-N Junction rectifier. 
I_{SM} Source Current ¹ (Body Diode)			- 22	A	
V_{SD} Diode Forward Voltage ¹			- 1.6	V	$T_C = 25\text{ C}$, $I_S = -5.5\text{ A}$, $V_{GS} = 0$ $T_C = 25\text{ C}$, $I_S = -4.5\text{ A}$, $V_{GS} = 0$
t_{rr} Reverse Recovery Time		470		ns	$T_J = 150\text{ C}$, $I_F = I_S$, $dl_F/ds = 100\text{ A/ms}$

I_S Continuous Source Current (Body Diode)			- 4.5	A	Modified MOSPOWER symbol showing the integral P-N Junction rectifier. 
I_{SM} Source Current ¹ (Body Diode)			- 18	A	
V_{SD} Diode Forward Voltage ¹			- 1.4	V	$T_C = 25\text{ C}$, $I_S = -4.5\text{ A}$, $V_{GS} = 0$ $T_C = 25\text{ C}$, $I_S = -4\text{ A}$, $V_{GS} = 0$
t_{rr} Reverse Recovery Time		430		ns	$T_J = 150\text{ C}$, $I_F = I_S$, $dl_F/ds = 100\text{ A/ms}$

1 Pulse Test: Pulse Width 300msec, Duty Cycle 2%.

1 Pulse Test: Pulse Width 300msec, Duty Cycle 2%.

OM6001ST - OM6104ST

ABSOLUTE MAXIMUM RATINGS ($T_C = 25^\circ\text{C}$ unless otherwise noted)

Parameter	OM6001ST OM6101ST	OM6002ST OM6102ST	OM6003ST OM6103ST	OM6004ST OM6104ST	Units
V_{DS}	100	200	400	500	V
V_{DGR}	100	200	400	500	V
$I_D @ T_C = 25^\circ\text{C}$	± 14	± 9	± 5.5	± 4.5	A
$I_D @ T_C = 100^\circ\text{C}$	± 9	± 6	± 3.5	± 3	A
I_{DM}	± 56	± 36	± 22	± 18	A
$P_D @ T_C = 25^\circ\text{C}$	50	50	50	50	W
$P_D @ T_C = 100^\circ\text{C}$	20	20	20	20	W
Junction To Case	0.4	0.4	0.4	0.4	W/ $^\circ\text{C}$
Junction To Ambient	.015	.015	.015	.015	W/ $^\circ\text{C}$
T_J	Operating and				
T_{stg}	Storage Temperature Range				$^\circ\text{C}$
Lead Temperature	(1/16" from case for 10 secs.)				$^\circ\text{C}$

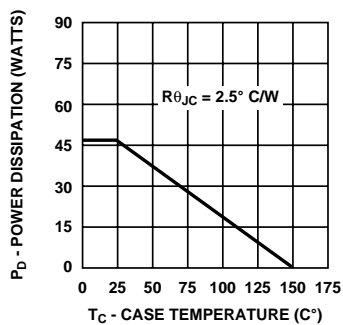
1 Pulse Test: Pulse width 300 μsec . Duty Cycle 2%.

2 Package Pin Limitations = 16 amps

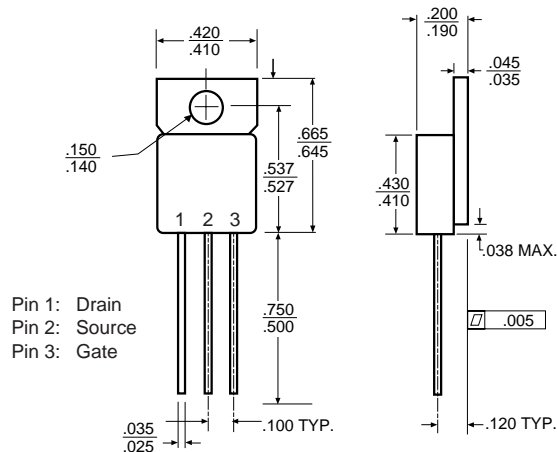
THERMAL RESISTANCE (MAXIMUM) at $T_A = 25^\circ\text{C}$

R_{thJC}	Junction-to-Case	2.5	$^\circ\text{C/W}$
R_{thJA}	Junction-to-Ambient	65	$^\circ\text{C/W}$ Free Air Operation

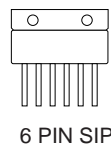
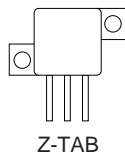
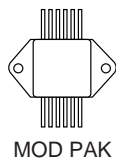
POWER DERATING



MECHANICAL OUTLINE WITH PIN CONNECTION



PACKAGE OPTIONS



Note: MOSFETs are also available in Z-Tab, dual and quad pak styles. Duals and quads available in non-gate versions only. Please call the factory for more information.