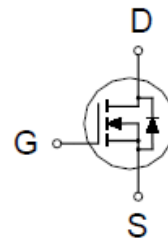
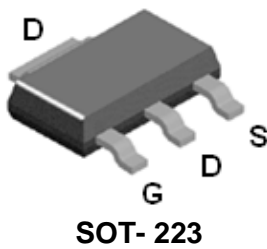


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PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
650V	14Ω @ $V_{GS} = 10V$	0.3A



ABSOLUTE MAXIMUM RATINGS ($T_A = 25\text{ °C}$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Gate-Source Voltage		V_{GS}	±30	V
Continuous Drain Current	$T_A = 25\text{ °C}$	I_D	0.3	A
	$T_A = 70\text{ °C}$		0.2	
Pulsed Drain Current ¹		I_{DM}	1.5	
Avalanche Current		I_{AS}	0.5	
Avalanche Energy	L = 10mH	E_{AS}	1.25	mJ
Power Dissipation	$T_A = 25\text{ °C}$	P_D	2	W
	$T_A = 70\text{ °C}$		1.3	
Operating Junction & Storage Temperature Range		T_J, T_{STG}	-55 to 150	°C

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Ambient	$R_{\theta JA}$		64	°C / W

¹Pulse width limited by maximum junction temperature.

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ELECTRICAL CHARACTERISTICS (T_J = 25 °C, Unless Otherwise Noted)

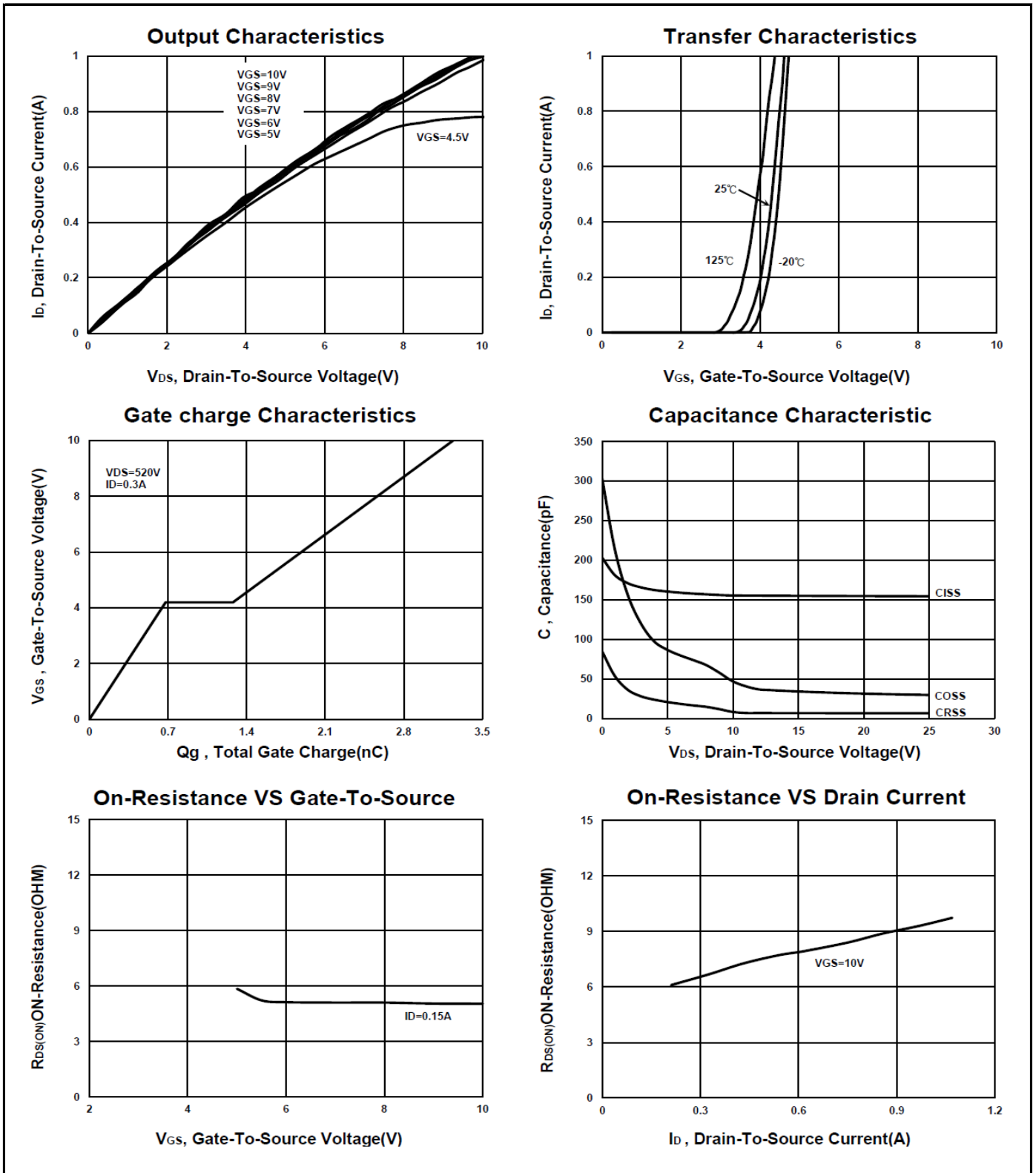
PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNITS
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 250μA	650			V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250μA	2	3.1	4	V
Gate-Body Leakage	I _{GSS}	V _{DS} = 0V, V _{GS} = ±30V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 650V, V _{GS} = 0V			1	μA
		V _{DS} = 520V, V _{GS} = 0V, T _J = 55 °C			10	
Drain-Source On-State Resistance ¹	R _{DS(ON)}	V _{GS} = 10V, I _D = 0.15A	0.1	9.2	14	Ω
Forward Transconductance ¹	g _{fs}	V _{DS} = 10V, I _D = 0.15A		1.2		S
DYNAMIC						
Input Capacitance	C _{iss}	V _{GS} = 0V, V _{DS} = 25V, f = 1MHz		160		pF
Output Capacitance	C _{oss}			30		
Reverse Transfer Capacitance	C _{rss}			7		
Total Gate Charge ²	Q _g	V _{DS} = 520V, V _{GS} = 10V, I _D = 0.3A		3.3		nC
Gate-Source Charge ²	Q _{gs}			0.8		
Gate-Drain Charge ²	Q _{gd}			0.6		
Turn-On Delay Time ²	t _{d(on)}	V _{DS} = 325V, I _D ≅ 0.3A V _{GS} = 10V, R _{GS} = 25Ω		40		nS
Rise Time ²	t _r			95		
Turn-Off Delay Time ²	t _{d(off)}			164		
Fall Time ²	t _f			105		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (T_J = 25 °C)						
Continuous Current	I _S				1	A
Forward Voltage ¹	V _{SD}	I _F = 0.3A, V _{GS} = 0V			1.5	V
Reverse Recovery Time	t _{rr}	I _F = 0.3A, di/dt = 100A / μS		249		nS
Reverse Recovery Charge	Q _{rr}				0.63	

¹Pulse test : Pulse Width ≤ 300 μsec, Duty Cycle ≤ 2%.

²Independent of operating temperature.

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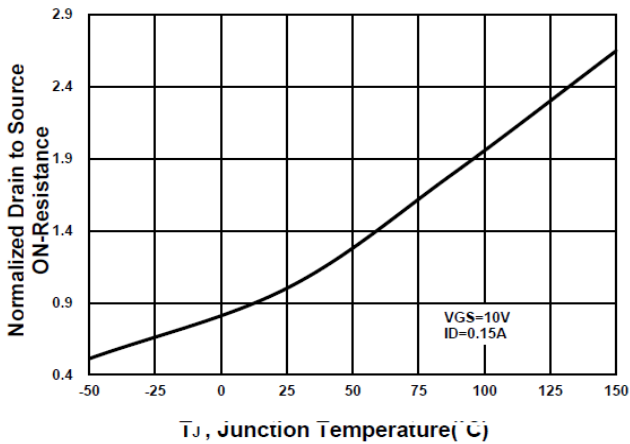
N-Channel Enhancement Mode MOSFET



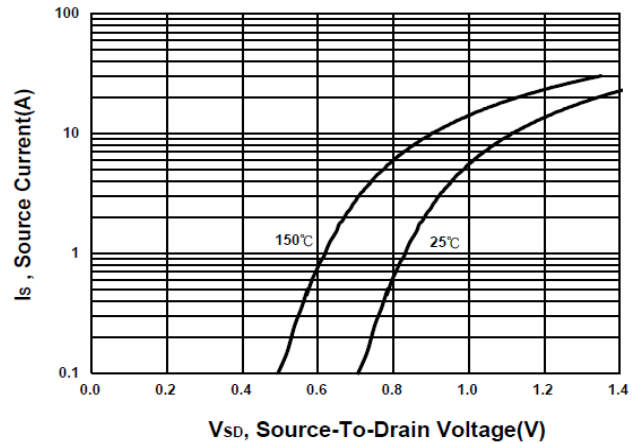
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N-Channel Enhancement Mode MOSFET

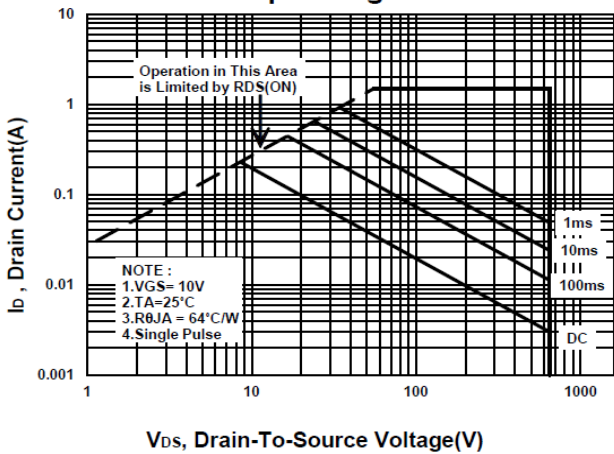
On-Resistance VS Temperature



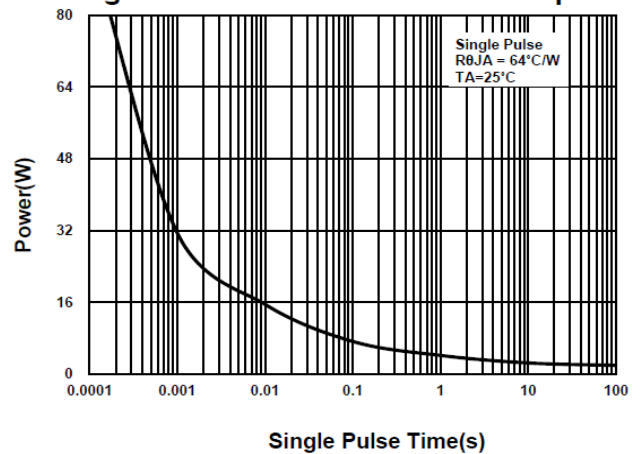
Source-Drain Diode Forward Voltage



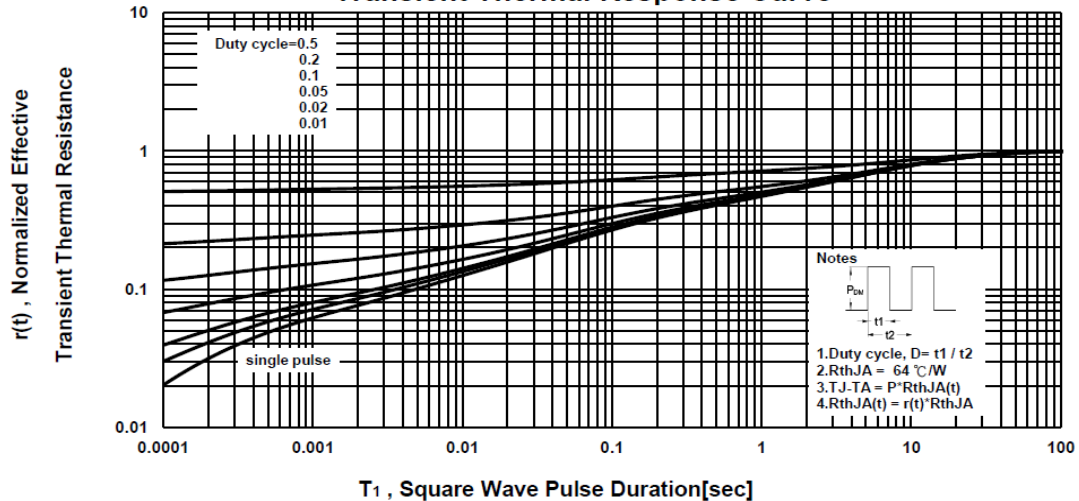
Safe Operating Area



Single Pulse Maximum Power Dissipation



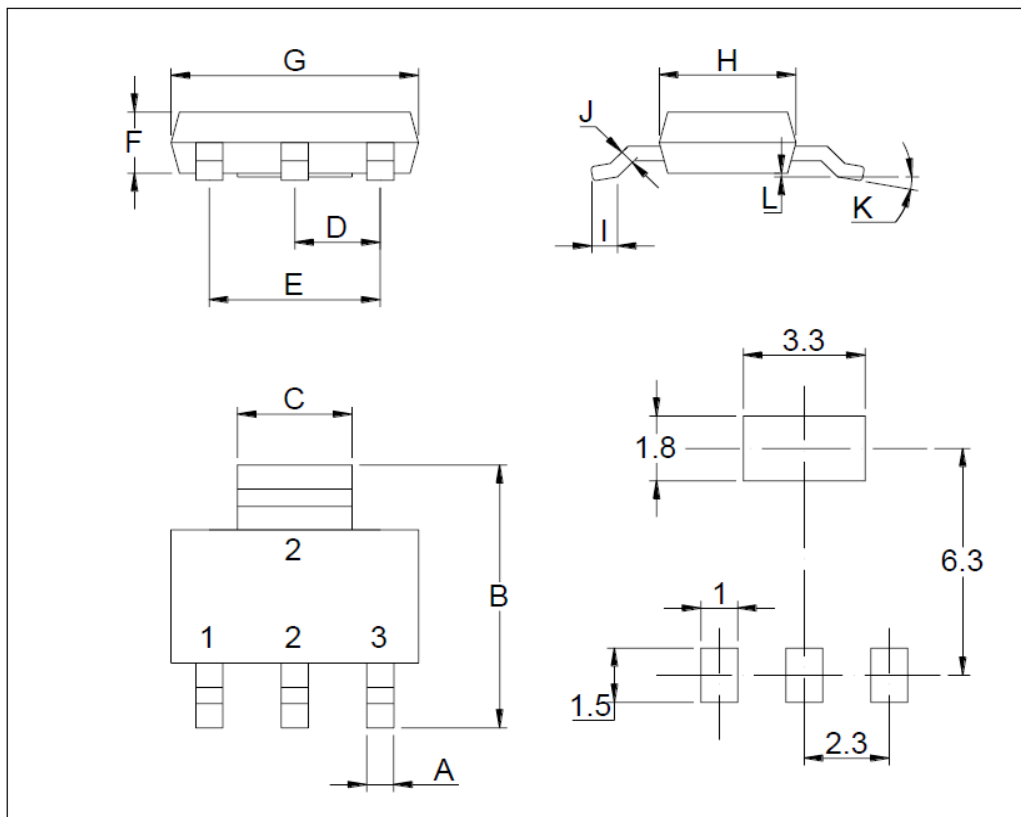
Transient Thermal Response Curve



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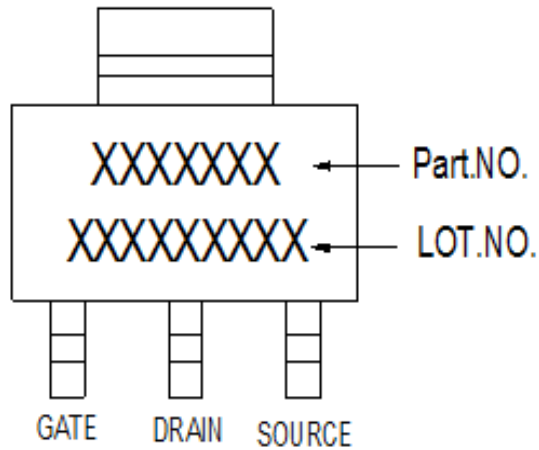
SOT-223 MECHANICAL DATA

Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	0.60	0.76	0.84	H	3.30	3.50	3.70
B	6.70	7.00	7.30	I	0.50	1.00	1.20
C	2.85	3.00	3.10	J	0.23	0.3	0.4
D	2.25	2.30	2.35	K	0°		10°
E	4.35	4.60	4.85	L	0	0.1	0.2
F	1.40	1.60	1.80	M			
G	6.30	6.50	6.80	N			

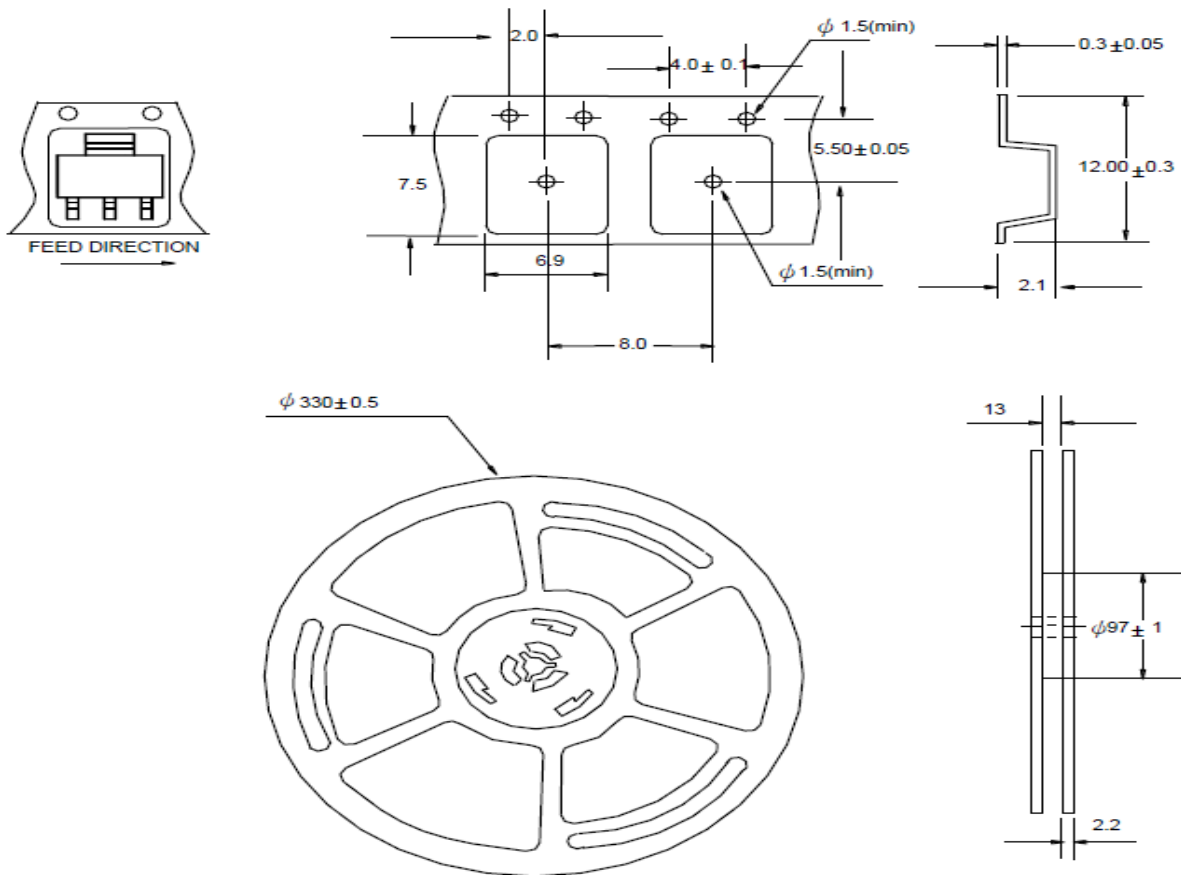


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A. Marking Information



B. Tape&Reel Information:2500pcs/Reel

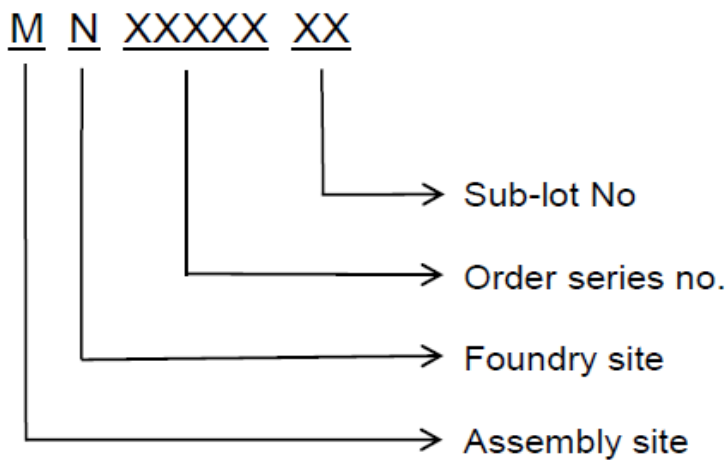


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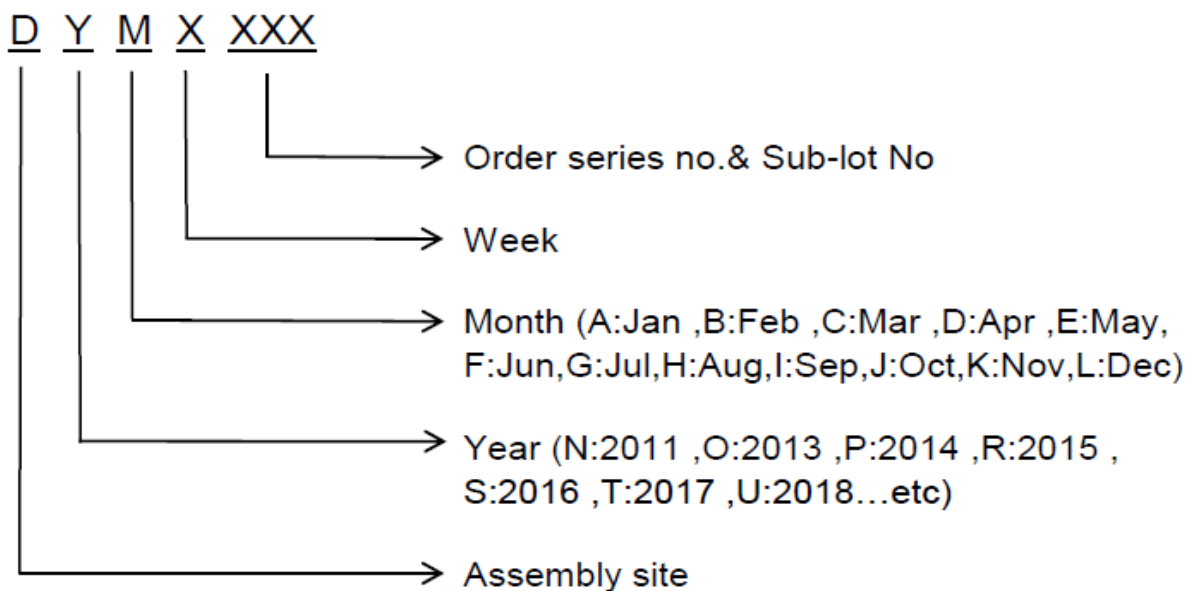
N-Channel Enhancement Mode MOSFET

C. Lot No.&Date Code rule

1.Lot No.



2.Date Code





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D.Label rule

标签内容(Label content)



1	Label Size	30 * 90 mm
2	Font style	Times New Roman or Arial (或可区分英文”0”和数字”0”，”G和”Q”的字型即可)
3	U-NIKC	Height: 4 mm
4	Package	Height: 2 mm
5	Date	Height: 2 mm Shipping date: YYYY/MM/DD, ex. 2008/09/12
6	Device	Height: 3 mm (Max: 16 Digit)
7	Lot	Height: 3 mm (Max: 9 Digit) Sub lot
8	D/C	Height: 3 mm (Max: 7 Digit)
9	QTY	Height: 3 mm (Max: 6 Digit) Thousand mark is no needed
10	RoHS label	 long axis: 12 mm minor axis:6 mm bottom color: White Font color: Black Font style: Arial
11	Halogen Free label	 Diameter: 10 mm bottom color: Green Font color: Black Font style: Arial
12	Scan information	Device / Lot / D/C / QTY , Insert “ / “ between every parts. for example: P3055LDG/G12345601/GGG2301/2000 DPI (Dots per inch): Over 300 dpi Code : Code 128 Height: 6 mm at least